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Zibar, Darko

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High-Speed Clock Recovery and Demodulation using Short Pulse Sources and Phase-Locked Loop Techniques

Darko Zibar

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COM•DTU

Department of Communications, Optics & Materials

Technical University of Denmark

Building 345V

2800 Kgs. Lyngby

DENMARK

Abstract

We present a modelling technique and noise analysis of a clock recovery scheme based on an optoelectronic phase-locked loop. We treat the problem using techniques from stochastic processes and stochastic differential equations. A set of stochastic differential (Langevin) equations describing the optoelectronic phase-locked loop are derived. By using small-signal analysis, the Langevin equations are linearized and the associated system of stochastic differential equations is solved using Fourier techniques. Numerical simulations are then used to investigate the performance of the optoelectronic phase-locked loop with noise at a bit-rate of 160 Gb/s. It has been shown that it is important to reduce the time delay in the loop since it results in the increased timing jitter of the recovered clock signal. We also investigate the requirement for the free-running timing jitter of the local electrical and optical oscillator. We show that it is possible to obtain recovered clock signal with less timing jitter than the input data signal as long as the jitter of the free-running electrical oscillator is less than the input data signal timing jitter. Using the guidelines from the numerical simulations, optoelectronic phase-locked loop based clock recovery operating at 320 Gb/s is demonstrated.

Optical regenerator with clock recovery, based on an optoelectronic phase-locked loop, is also described using techniques from stochastic calculus. An analytical expression for the power spectral density of the retimed data signal is derived. We use numerical simulation to investigate the performance of the optical regenerator operating at 40 Gb/s and 160 Gb/s. We have shown that for flat-top input data signal pulses and sufficiently narrow optical clock signal pulses, the timing jitter of the retimed data optical data signal can be significantly reduced compared to the jitter of the degraded input data signal. The optical clock signal pulse width needs to be relatively short compared to the optical data signal pulse width in order for the retimed data signal timing jitter to coincide with the recovered clock

timing jitter, i.e. 3.5 ps at 40 Gb/s and 0.5 ps at 160 Gb/s.

In the last part of the thesis, a novel phase-locked coherent optical phase demodulator with feedback and sampling, to be used in phase-modulated radio-over-fibre optical links, is also presented, theoretically investigated and experimentally demonstrated. It is experimentally shown that the proposed approach results in 18 dB of spur-free-dynamic-range improvement compared to a traditional demodulator without feedback. A new time-domain, large signal, numerical model of the phase-locked coherent demodulator is developed and shown to be in excellent agreement with experimental results. Numerical simulations are used to investigate how loop gain, LO phase-modulator non-linearities, amplitude modulation, amplitude and timing jitter influence the dynamical behavior of the demodulator in terms of the signal-to-intermodulation ratio and signal-to-noise ratio of the demodulated signal. Furthermore, in order to alleviate non-linearities associated with the LO phase-modulator, we report on a novel technique for cancelation of the 3rd order intermodulation product of the demodulated signal. The proposed cancelation technique does not depend on input RF signal power and frequency.

Resumé (in Danish)

Denne afhandling drejer sig om modelleringsteknik og støjanalyse i forbindelse med klokgendannelsessystemer, som er baseret på en optoelektronisk faselåst sløjfe. Der er brugt teknikker fra stokastiske processer og stokastiske differentialligninger til at løse problemet. Stokastiske differentialligninger, der beskriver en optoelektronisk faselåst sløjfe, bliver udledt. Der er anvendt småsignalanalyse til at linearisere ligningssystemet, og det fremkomne ligningssystem bliver løst ved hjælp af Fourier transformationsteknikker. Endvidere er der udført numeriske simuleringer for at undersøge virkemåden af en optoelektronisk faselåst sløjfe ved 160 Gb/s. Det vises, at det er vigtigt at reducere tidsforsinkelsen inde i sløjfen, da der ellers vil opstå forøget støj på det gendannede kloksignal. Endvidere undersøges kravene til de lokaloscillatorer, der indgår i sløjfen. Det vises, at hvis den elektriske oscillator i sløjfen har mindre støj end det indkommende datasignal, så kan man stadigvæk opnå støjreduktion, selv om den optiske lokaloscillator har mere støj end det indkommende datasignal. Ved at følge retningslinier fra teorien demonstreres klokgendannelse fra et 320 Gb/s datasignal.

Optisk regenerering med klokgendannelse beskrives også. Dette gøres ved hjælp af teknikker fra stokastisk teori, og endelig bliver udledt et analytisk udtryk for frekvensspektret af det regenererede datasignal. Dernæst udføres numeriske simuleringer for at undersøge en optisk regenerator, der arbejder ved enten 40 Gb/s eller 160 Gb/s. Det vises, at det indkommende datasignal skal have firkantede pulser for at en væsentlig støjreduktion kan opnås. Klokpulserne skal have en bredde i tid på 3.5 ps og 0.5 ps for systemer, der arbejder ved henholdsvis 40 Gb/s og 160 Gb/s for at støjen på det regenererede datasignal er lige så lille som støjen fra kloksignalet.

I sidste del af afhandlingen bliver en ny faselåst, kohærent og optisk fasemodulator med tilbagekobling samt sampling og beregnet til fasemodulerede optiske forbindelser præsenteret, teoretisk undersøgt og eksperimentelt demonstreret. Det vises eksperimentelt, hvordan den nye faselåste demodula-

tor giver en forbedring på 18 dB i dynamisk område sammenlignet med en traditionel fasedemodulator uden tilbagekobling. Endvidere er en ny tidsdomæne numerisk model blevet udviklet, og den vises at give resultater, som er i god overensstemmelse med eksperimentelle data. Numeriske simuleringer bruges til at undersøge, hvordan sløjfeforstærkning, ikke-linearitet i fasedemodulatoren, amplitudemodulation, amplitude- og tidsjitter indvirker på signal-intermodulationsforholdet og signal-støj forholdet. Endelig præsenteres en ny lineariseringsteknik, som er uafhængig af RF signaleffekt og frekvens, og som derved kan overvinde fasedemodulatorens ikke-linearitet.

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Ph.D. publications

Peer-reviewed journal papers: (6)

- [J1] **Darko Zibar**, Jesper Mørk, Leif K. Oxenløwe and Anders T. Clausen, "Phase noise analysis of clock recovery based on an optoelectronic phase-locked loop," *Journal of Lightwave Technology*, vol. 25, no. 3, pp. 901–914, March 2007
- [J2] **Darko Zibar**, Leif A. Johansson, Hsu-Feng Chou, Anand Ramaswamy, Mark Rodwell and John E. Bowers, "Dynamic range enhancement of a novel phase-locked coherent optical phase demodulator," *Optics Express*, vol. 15, no. 1, pp. 33–44, January 2007
- [J3] **Darko Zibar**, Leif A. Johansson, Hsu-Feng Chou, Anand Ramaswamy, Mark Rodwell and John E. Bowers, "Novel optical phase demodulator based on a sampling phase-locked loop," *Photonics Technology Letters*, vol. 19, no. 9, pp. 686–688, May 2007
- [J4] **Darko Zibar**, Leif K. Oxenløwe, Hans Christian H. Mulvad, Jesper Mørk, Michael Galili, Anders T. Clausen, Palle Jeppesen, "The effect of timing jitter on a 160 Gb/s demultiplexer," *Photonics Technology Letters*, vol. 19, no. 13, pp. 957–959, July 2007
- [J5] **Darko Zibar**, Leif A. Johansson, Hsu-Feng Chou, Anand Ramaswamy, Mark Rodwell and John E. Bowers, "Phase-Locked Coherent Demodulator with Feedback and Sampling for Optically Phase Modulated Microwave Links," *accepted for publication in Journal of Lightwave Technology, special issue on Microwave Photonics, 2008*
- [J6] Hsu-Feng Chou, Anand Ramaswamy, **Darko Zibar**, Leif A. Johansson, Mark Rodwell, John E. Bowers and Larry Coldren, "Highly-linear coherent receiver with feedback," *Photonics Technology Letters*, vol. 19, no. 12, pp. 940–942, June 2007

Peer-reviewed conference papers: (18)

- [C1] Francesca Parmigani, Leif K. Oxenløwe, Michael Galili, Morten Ibsen, **Darko Zibar**, Perikles Petropoulos, David Richardson, Anders T. Clausen and Palle Jeppesen, "All-optical 160 Gb/s RZ data retiming system incorporating a pulse shaping fibre Bragg grating," in *Proceedings of European Conference on Optical Communication 2007, Berlin, Germany*, paper 5.3.1, 2007
- [C2] Leif Oxenløwe, Francesca Parmigani, Michael Galili, **Darko Zibar**, Anders T. Clausen, David Richardson, Morten Ibsen, Perikles Petropoulos and Palle Jeppesen, "160 Gb/s retiming using rectangular pulses generated using a superstructured fibre Bragg grating," in *Proceedings of OptoElectronics and Communications Conference 2007, Yokohama, Japan*, Paper 13B3-4, 2007
- [C3] Leif A. Johansson, **Darko Zibar**, Anand Ramaswamy, Larry Col-dren, Mark Rodwell and John E. Bowers, "Analysis of sampled optical phase-lock loops," in *Proceedings of International Topical Meeting on Microwave Photonics (MWP) 2007, Victoria Canada*, paper Th4-32, 2007
- [C4] **Darko Zibar**, Leif K. Oxenløwe, Jesper Mørk, Anders T. Clausen and Palle Jeppesen, "Analysis of the effects of pulse shape and width on the retiming properties of a 3R regenerator," in *Proceedings of European Conference on Laser and Electro-Optics (CLEO/Europe) 2007, Munich, Germany*, paper CI2, 2007
- [C5] **Darko Zibar**, Leif A. Johansson, Hsu-Feng Chou, Anand Ramaswamy, Mark Rodwell and John E. Bowers, "Investigation of a novel optical phase demodulator based on sampling phase-locked loop," in *Proceedings of International Topical Meeting on Microwave Photonics (MWP) 2006, Grenoble France*, paper P1, 2006 (Best student paper award)
- [C6] **Darko Zibar**, Leif A. Johansson, Hsu-Feng Chou, Anand Ramaswamy and John E. Bowers, "Time-domain analysis of a novel phase-locked coherent optical demodulator," in *Proceeding of Conference on Coherent Optical Technologies and Applications (COTA) 2006, Whistler, Canada*, paper JWB1, 2006
- [C7] **Darko Zibar**, Jesper Mørk, Leif K. Oxenløwe, Anders T. Clausen and Palle Jeppesen, "Reduction of timing jitter by clock recovery based on an optical phase-locked loop," in *Proceeding of Conference*

on Laser Electro Optics (CLEO) 2006, Long Beach California, USA,
paper CMJ4, 2006

- [C8] **Darko Zibar**, Leif K. Oxenløwe, Hans Christian H. Mulvad, Jesper Mørk, Michael Galili, Anders T. Clausen, Palle Jeppesen, "The impact of gating timing jitter on a 160 Gb/s demultiplexer," *in Proceeding of Optical Fiber Communication Conference (OFC) 2006, Anaheim California, USA*, paper OTuB2, 2006
- [C9] (invited paper) Leif K. Oxenløwe, Michael Galili, Hans Christian H. Mulvad, Anders Clausen, **Darko Zibar** and Palle Jeppesen, "Solutions for ultra high-speed optical wavelength conversion and clock recovery," *in Proceedings of International Conference on Transparent Optical Networks (ICTON), Nottingham, United Kingdom*, paper Tu.D2.3, 2006
- [C10] Hsu-Feng Chou, Anand Ramaswamy, **Darko Zibar**, Leif A. Johansson, John E. Bowers, Mark Rodwell and Larry Coldren, "SFDR improvement of a coherent receiver using feedback," *in Proceeding of Conference on Coherent Optical Technologies and Applications (COTA) 2006, Whistler, Canada*, paper CFA3, 2006
- [C11] Hsu-Feng Chou, Leif A. Johansson, **Darko Zibar**, Anand Ramaswamy, Mark Rodwell and John E. Bowers, "All-optical coherent receiver with feedback and sampling," *in Proceedings of International Topical Meeting on Microwave Photonics (MWP) 2006, Grenoble, France*, paper W3.2, 2006
- [C12] **Darko Zibar**, Jesper Mørk, Mads P. Sørensen, Leif K. Oxenløwe, Michael Galili, Anders T. Clausen and Palle Jeppesen, "Detailed modelling and experimental characterization of an ultra-fast optoelectronic clock recovery," *in Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland*, paper We4.P.111, 2005
- [C13] **Darko Zibar**, Jesper Mørk, Leif K. Oxenløwe, Michael Galili, Anders T. Clausen, "Timing jitter analysis for clock recovery circuits based on an optoelectronic phase-locked loop (OPLL)," *in Proceedings Conference on Laser and Electro Optics (CLEO) 2005, Baltimore, Maryland, USA*, paper CMZ4, 2005
- [C14] Leif K. Oxenløwe, **Darko Zibar**, Michael Galili, Anders T. Clausen, Lotte J. Christiansen, Palle Jeppesen, "Clock recovery for 320 Gb/s OTDM data using filtering assisted XPM in an SOA," *in Proceedings*

of Conference on Laser Electro Optics, Europe (CLEO), Munich, Germany, pp. 486, 2005

- [C15] Leif K. Oxenløwe, **Darko Zibar**, Michael Galili, Anders T. Clausen, Lotte J. Christiansen, Palle Jeppesen, "Filtering-assisted cross-phase modulation in a semiconductor optical amplifier enabling 320 Gb/s clock recovery," *in Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland*, pp. 113-114, 2005
- [C16] Michael Galili, Leif K. Oxenløwe, **Darko Zibar**, Anders T. Clausen, Hans J. Deyerel, Nikolai Plougman, Martin Christensen and Palle Jeppesen, "160 Gb/s notch-filtered Raman assisted XPM wavelength conversion," *in Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland*, pp. 113-114, 2005
- [C17] **Darko Zibar**, Leif K. Oxenløwe, Anders T. Clausen, Jesper Mørk and Palle Jeppesen, "Analysis of the effects of time delay in clock recovery circuits based on phase-locked loops," *in Proceedings of Laser Electro Optic Society (LEOS) 2004 Annual meeting, Puerto Rico, USA*, paper TuR4, 2004
- [C18] Michael Galili, Leif K. Oxenløwe, **Darko Zibar**, Anders T. Clausen and Palle Jeppesen, "160 Gb/s Raman-assisted SPM wavelength converter," *in Proceedings of European Conference on Optical Communications (ECOC) 2004, Stockholm, Sweden*, post-deadline paper Th4.3.1, 2004

List of acronyms

A/D	Analog to Digital
AL	Active Lag
ASE	Amplified Spontaneous Emission
A/D	Analog to Digital
AL	Active Lag
ASE	Amplified Spontaneous Emission
BW	Bandwidth
CR	Clock Recovery
CW	Continuous Wave
EAM	Electroabsorbtion Modulator
EDFA	Erbium Doped Fibre Amplifier
E/O	Electrical Optical
ERGO-PGL	Erbium Glass Oscillator Pulse Generating Laser
ETDM	Electrical Time Division Multiplexing
FWM	Four Wave Mixing
FWHM	Full Width Half Maximum
HNLF	Highly Non Linear Fibre
InP DHBT	Ind. Phosph. Double Heterojunction Bipolar Transistor
IP	Internet Protocol
ISF	Impulse Sensitivity Function
LiNBO	Lithium Niobate Oxygen
LNA	Low Noise Amplifier

LO	Local Oscillator
LP	Low Pass
NF	Noise Figure
NSDE	Non-linear Stochastic Differential Equation
NOLM	Nonlinear Optical Loop Mirror
MZM	Mach Zender Modulator
O/E	Optical Electrical
OPLL	Optoelectronic Phase Locked Loop
OTDM	Optical Time Division Multiplexing
PC	Phase Comparator
PDF	Probability Density Function
PI	Proportional Integrator
PLL	Phase Locked Loop
PRBS	Pseudo Random Bit Sequence
PSD	Power Spectral Density
PTER	Pulse Tail Extinction Ratio
RF	Radio Frequency
RZ	Return to Zero
RoF	Radio over Fibre
SFDR	Spurious Free Dynamic Range
SIR	Signal to Intermodulation Ratio
SNR	Signal to Noise Ratio
SMF	Single Mode Fibre
SOA	Semiconductor Optical Amplifier
SSCR	Single Sideband to Carrier Ratio
TMLL	Tunable Mode Locked Laser
VCO	Voltage Controlled Oscillator
WDM	Wavelength Division Multiplexing
XPM	Cross Phase Modulation

List of frequently used symbols

A	Gain of the electrical amplifier in the loop
$A_0(t)$	Amplitude of the envelope of the optical pulse source
a_1, a_2, a_3	Linear, quadr. and cubic term of input phase modulator
b_1, b_2, b_3	Linear, quadr. and cubic term of photodiode response
c_1, c_2, c_3	Linear, quadr. and cubic term of LO phase modulator
c_{in}	Constant for phase noise associated with input data signal
c_{vco}	Constant for phase noise associated with VCO
c_{clk}	Constant for phase noise associated with optical pulse source
$c_{i \cdot m}$	$i \cdot m$ th Fourier coefficient of the optical pulse source
D_1, D_2, D_3	Linear, quadr. and cubic term of phase mod. amp. response
$D_i^{(1)}$	First order Kramers-Moyal expansion coefficients
D	Matrix containing amplitudes of noise sources
$E_{in}(t)$	Input optical signal to the phase-locked demodulator
$E_{LO}(t)$	LO optical signal in the phase-locked demodulator
$e(t)$	Error signal for control of OPLL
$e_n(t)$	Error signal normalized with $\Delta\omega$
$\hat{\mathbf{e}}_n$	Unit vectors (n is an integer)
f_0	Frequency of the input data signal aggregate bit rate
f_0/m	Rep. freq. of the optical pulse source envelope (m is an integer)
f_0''	Repetition frequency of the VCO

f_{ch}	Characteristic knee frequency of optical pulse source
$f_{LF}(t)$	BW of the loop filter of the phase-locked demodulator
$F(\omega)$	PSD of a signal with amplitude and phase noise
G	Gain of the phase comparator
$G(\omega)$	Power spectral density of a signal with phase noise
$G_{opl}(s)$	Open-loop transfer function of the OPLL
$H_{opl}(s)$	Closed-loop transfer function of the OPLL
$I_1(t)$	Out. current of the first photodiode in the balanced rec.
$I_2(t)$	Out. current of the second photodiode in the balanced rec.
\mathbf{I}	Unity matrix
K	Overall loop gain in the PLL coherent demodulator
K_{vco}	Gain of the VCO
$M_{in}(t)$	Input RF signal modulation depth
$P_{in}(t)$	Intensity of input data signal envelope
$P_{clk}(t)$	Intensity of optical pulse source envelope
$P_{ps}(t)$	Intensity of in. data signal after pulse shaper
$P_{3R}(t)$	Intensity of regen. optical data signal
$P_{LO}(t)$	Intensity of the LO signal envelope
$p_y(y; t)$	PDF of the inten. of the recovered clock signal envelope
$p_{\alpha_{clk}}(y; t)$	PDF of the optical pulse source phase noise
R	Photodiode respons. in OPLL model set-up
R_{pd}	Photodiode respons. in PLL demodulator model set-up
$R_L(t)$	Load impedance of the balanced receiver
$R_{\beta, \beta}(t, \tau)$	Autocorrelation fun. of a signal with amp. noise
$R_{f, f}(t, \tau)$	Autocorrel. fun. of a signal with amp. and phase noise
$R_{g, g}(t, \tau)$	Autocorrelation fun. of a signal with phase noise
$S_{P_{clk}, P_{clk}}(\omega)$	PSD of the recovered optical clock signal
$S_{v, v}(\omega)$	PSD of the recovered electrical clock signal

$S_{3R}(\omega)$	PSD of the regenerated data signal
s_i	i th Fourier coefficient of the input data signal
$s'_0 c'_0$	Remaining DC level in the error signal
T_0	Half width at $1/e$ intensity point of the envelope
T_{FWHM}	Full width half maximum of the envelope
T_p	Repetition period of the optical pulse source envelope
$V_{\pi, in}$	Voltage of input phase modulator to obtain π phase shift
$V_{\pi, LO}$	Voltage of LO phase modulator to obtain π phase shift
\mathbf{V}_i	Eigenvectors (i is an integer)
$V_{in}(t)$	Input RF signal to the antenna base station
$V_{pd}(t)$	Voltage across the load impedance
$v(t)$	Output signal of the VCO
V_1, V_2	Amplitudes of the input RF signals to the antenna
$V_{out}(t)$	Output signal of the phase-locked demodulator
$V_{ref}(t)$	Output signal of the open loop PLL demodulator
$w_{jp}(t)$	Numerically generated random numbers
$Y(\omega)$	Power spectral density of Langevin noise
$\alpha(t)$	Signal phase noise
$\alpha_{in}(t)$	Input data signal phase noise in OPLL
$\alpha_{vco}(t)$	VCO signal phase noise
$\alpha_{clk}(t)$	Optical pulse source signal phase noise in OPLL
$\beta(t)$	Amplitude noise
$\gamma_e(t)$	Phase diff. between the VCO and optical pulse source
$\Gamma(t)$	Langevin noise force
$\Gamma_{in}(t)$	Langevin noise force associated with input signal in OPLL
$\Gamma_{vco}(t)$	Langevin noise force associated with VCO
$\Gamma_{clk}(t)$	Langevin noise force associated with optical source in OPLL
$\mathbf{\Gamma}$	Vector containing noise sources
$\Delta\omega$	Angular frequency

δ_{lk}	Kronecker delta function
δ_t	Dirac delta function
λ_i	Eigenvalues (i is an integer)
$\mu(\cdot)$	Unit step function
$\sigma_\varphi^2(t, \tau)$	Variance of the recovered optical clock
σ_{clk}^2	Variance of the recovered optical clock phase noise
ζ_i	Overall loop gain in the OPLL
ζ_1	$2ARGs_1c_{16}$
η	Low-frequency amplitude noise spectrum
τ_1	Integration time of PI filter
τ_2	Constant determining the DC gain of PI filter
$\tau_{LF}(t)$	Time constant inversely proportional to f_{LF}
τ_{ch}	Time constant inversely prop. to characteristic knee frequency
τ_d	Time delay in the loop
$\tau_{jitt}^{P_{clk}}$	Integrated jitter of the recovered optical signal
τ_{jitt}^v	Integrated jitter of the recovered electrical signal
$\phi_e(t)$	Phase error between input signal and optical pulse source
$\phi_{in}(t)$	Phase of the input optical signal to the PLL demodulator
$\phi_{LO}(t)$	Phase of the LO optical signal in the PLL demodulator
$\psi_e(t)$	Input signal to the PI filter
ω_1, ω_2	Angular frequencies of the input RF signals at the antenna
ω_R	Angular roll-off frequency of the amplitude noise spectrum

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Chapter 1

Introduction

This chapter will give an overview of some of the current trends in optical communication and also introduce the main topics of this thesis. The first trend which is described is the high-speed trend which aims at increasing the overall capacity of fibre optic networks. The second trend which is described is the "revival" of optical coherent receiver structures in order to realize linear optical links for transport of wireless signals to and from antenna base stations.

1.1 High-speed trends in optical communication

The ability of optical fibre communication systems to provide high capacity has had a huge impact on the telecommunication industry and on our lives. The deployment of erbium-doped fibre amplifiers in the early 1990s, the rapid development in high-speed integrated electronic and wavelength division multiplexing has enabled high-capacity telecommunication networks and taken us with the speed of light into the information age. Even though the optical communication industry has suffered from over-capacity and lack of investment until recently, the optimism is slowly coming back. The never-ending introduction of new broadband services (high-definition TV, interactive TV, intranet, internet TV, IP telephony etc.) has resulted in a growth of the total data traffic and this demand on bandwidth is expected to continue to increase at exponential growth rates [1]. Furthermore, the deployment of fibre-to-the-customer premises, aiming at providing high bandwidth to access nodes at homes, buildings and street cabinets, has started and is now progressing fast. The projected number of homes that will be physically connected with a fibre connection is growing and is es-

timated to approach 70 million worldwide by 2010 [1]. This will have a big impact on the overall capacity of global fibre-optic networks. In addition, convergence between fixed and wireless broadband access networks is an inevitable reality – customers want access to the same applications whatever the device (fixed or mobile) and anytime. It is predicted that the number of mobile broadband subscribers is growing at a fast pace and by 2010 that number will reach 170 million [2]. This too will add extra load on the overall capacity. Moreover, a deployment of ultra-high speed data buses for connecting processor elements and shared, memories thus reducing the number of input-output back-plane connectors has also been suggested [3, 4].

With the evolution of the telecommunication sector, the nature of data traffic has moved from voice oriented to IP (Internet Protocol) oriented. The projected data traffic annual growth rates are 8% for voice, 34% for transaction data and 157% for IP traffic [1]. Based on actual data the growth of total traffic is 22/45% in 2006 according to low/high estimates [1]. However, these low/high estimates correspond to average traffic as opposed to peak traffic which is typically 50% greater. Taking the high growth scenario into account, the available capacity of installed fibres will be used up by the year 2015 [1]. In order to avoid this unwanted scenario and keep up with the increase in data traffic, a capacity upgrade for future fibre optic communication system is imperative.

In today's fibre-optic networks, the most common way of increasing the capacity is achieved by a combination of Wavelength Division Multiplexing (WDM) and Electrical Time Division Multiplexing (ETDM). In WDM systems, multiple optical carriers at different wavelengths are modulated using ETDM. In ETDM systems, a data signal is electrically multiplexed in time domain from low-speed channels to provide high-speed serial data. Today, optical transmission systems are typically running at the aggregate bit-rate of 2.5 Gb/s and 10 Gb/s per channel. However, optical communication systems at the aggregate bit-rate of 40 Gb/s per channel are commercially available and are being installed [5].

The combination of WDM and TDM technologies can significantly increase the overall capacity of fibre-optic networks. A recent experiment resulted in a new world record by demonstrating transmission of 25.6 Tb/s over 240 km [6]. This was achieved by wavelength division multiplexing of 160 channels on a 50 GHz spacing grid in the C+L bands. Each channel contained two polarization-multiplexed 85.4 Gb/s signals. For such high ca-

capacity optical communication systems, consisting of many optical channels, the optical amplifiers (Erbium Doped Fibre Amplifier (EDFA) and Raman amplifier) are critical technologies to enable long-haul transmission. We should therefore mention a transmission experiment where the total capacity of 20.4 Tb/s (204 channels \times 111 Gb/s) was achieved over 240 km (3×80 km) using 10.2 THz inline hybrid Raman/EDFA and high-gain bidirectional distributed Raman amplification [7]. These world-record experiments demonstrate the potential of WDM and optical technologies to significantly increase the overall system capacity of fibre-optic networks.

In addition, due to the demand for higher-data capacities, the race to increase single channel bit-rate has once again started to attract considerable attention from the industry and universities alike. In the last few years, there has been a lot of work on 100 Gb/s single channel ETDM communication systems, (see [8–11] and a review paper [12]). Sub-system components, essential for the operation of 100 Gb/s optical communication systems, such as a Trans-Impedance Amplifier (TIA), a baseband amplifier, a multiplexer, frequency dividers and demultiplexers, operating at 100 Gb/s, have all been demonstrated [11, 12]. Moreover, an 165 Gb/s integrated ETDM multiplexer has been very recently demonstrated in InP Double Heterojunction Bipolar Transistor (DHBT) technology, pushing the state-of-the-art in electronic circuit design [13]. However, one of the remaining challenges for the ETDM transmission systems is clock and data recovery. So far, the highest data rate at which an integrated clock and data recovery circuit has been demonstrated is 80 Gb/s, and was realized in InP DHBT technology [14]. It should however also be mentioned that a phase-locked loop based electrical clock recovery operating at 100 Gb/s has been demonstrated using off-the-shelf bulk components [15]. In reference [10], an integrated phase-locked loop based clock recovery for ETDM systems operating at 107 Gb/s has recently been demonstrated. However, error-free demultiplexing has not been achieved using the recovered clock signal. Demonstrating an integrated clock recovery for 100 Gb/s ETDM systems which can provide error free demultiplexing still remains a major challenge.

Even though ETDM systems are under rapid development, the most practical and effective way to realize ultra-high speed single channel optical transmission systems is still to use Optical Time Division Multiplexing (OTDM). In OTDM systems, each channel is modulated onto a train of short optical pulses, all at the same wavelength. The channels are then multiplexed into a high speed OTDM data signal by bit-interleaving the modulated pulse-trains, and subsequently transmitted through a fibre link.

After transmission, the data signal at the aggregate bit-rate is optically demultiplexed to extract the single channels at the lower ETDM bit-rate. For a detailed description of OTDM systems, see section 1.1.1.

OTDM systems can be used to generate single channel data systems up to 640 Gb/s and beyond [16–20]. The highest reported single channel bit-rate based on OTDM is 2.56 Tb/s [20]. In addition, 1.28 Tb/s single channel bit-rate experiments should also be mentioned [21, 22]. This certainly shows the potential of using the OTDM technique to increase the single channel bit-rate. However, one of the challenges associated with such high-speed OTDM systems is clock extraction and demultiplexing. Successful demultiplexing from 640 Gb/s to 10 Gb/s has been performed using fibre-based switches [16–19]. Fibre-based optical switches are promising due to their ultra-fast response. The highest bit-rate from which a clock has been extracted from in an OTDM system is 400 Gb/s using an optoelectronic phase-locked loop made from bulk components [23]. However, the extracted clock from the 400 Gb/s OTDM data signal was not used in a transmission experiment, so the quality and the performance of the recovered clock could not be evaluated. Clock extraction from high-speed data signals is a non trivial task since phase noise (timing jitter) requirements become more demanding as the data signal bit-rate is increased. This has a significant impact on the design of the clock recovery circuit. Therefore, many challenges associated with clock extraction in high-speed OTDM optical communication systems still remain to be solved and investigated. Moreover, a comprehensive theoretical framework to describe clock extraction in OTDM systems has been missing.

In this thesis, we focus on OTDM optical communication systems with special emphasis on optoelectronic phase-locked loop for clock extraction, data signal regeneration and demultiplexing. In general terms, the effect of signal timing jitter on clock recovery, regeneration and demultiplexing is investigated thoroughly.

A detailed theoretical analysis of a clock recovery scheme based on an optoelectronic phase-locked loop is presented. The analysis emphasizes the phase noise performance, taking into account the noise of the input data signal, the local Voltage Controlled Oscillator (VCO) and the laser employed in the loop. The effects of loop delay time and the laser transfer function are included in the stochastic differential equations describing the system and a detailed timing jitter analysis of this type of optoelectronic clock recovery for high-speed OTDM systems is performed. The impact

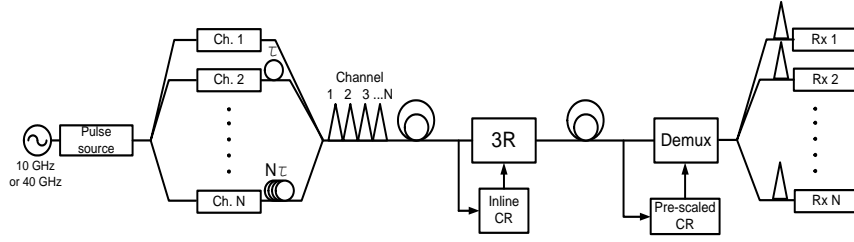


Figure 1.1: Basic OTDM system for point-to-point transmission. τ : delay, N : number of channels (integer), 3R: signal regenerator.

of the loop length on the clock signal jitter is furthermore investigated. We investigate numerically the timing jitter requirements for the combined electrical/optical local oscillators, in order for the recovered clock signal to have less jitter than that of the input signal. In addition, a novel scheme for optical clock extraction based on an optoelectronic phase-locked loop is presented and demonstrated at 320 Gb/s.

Thereafter, the focus is shifted toward optical regeneration (retiming) in the presence of optoelectronic phase-locked loop based clock recovery. We analytically model the regenerator as an Ornstein-Uhlenbeck process and we end up with an analytical expression for the power spectral density of the retimed OTDM data signal [24]. The effects of OTDM data signal jitter, pulse shape, recovered clock jitter and pulse width on the jitter of the retimed OTDM data signal are investigated.

For the optical demultiplexer, the impact of control signal timing jitter on a 160 Gb/s demultiplexer is investigated. This is achieved by using two different pulse sources with different noise properties. Furthermore, we investigate the interplay between the control signal pulse width and timing jitter to achieve error-free performance of the system.

1.1.1 OTDM system description

A schematic drawing of an OTDM system is shown in Figure 1.1. The considered OTDM system can be divided into the following subsystems: a transmitter, a transmission link (including a regenerator) and a receiver. At the transmitter, an optical pulse source produces short optical pulses with a repetition frequency, f_{base} , at the base rate (e.g. 10 GHz or 40 GHz). In particular, the following pulse sources have been used for experimental demon-

stration of OTDM systems: the semiconductor mode-locked laser [41], the mode-locked fibre-ring laser [42], the solid state mode-locked Erbium Glass Oscillator Pulse Generating Laser [43] and the electro-absorption modulators [44]. It has been shown that the pulse width (full-width-half-maximum) of the optical pulse source needs to be less than 40% of the bit slot of the OTDM signal [45]. Furthermore, for the f_{base} of 40 GHz, the pulses are required to have a pulse-tail-extinction-ratio (PTER) of 27 dB, 33 dB and 37 dB for OTDM signal bit-rates of 160 Gb/s, 320 Gb/s and 640 Gb/s, respectively [45]. Another important issue of the pulse sources is chirp and timing jitter. Ideally, it is desirable to have transform limited pulses resulting in the narrowest spectrum, i.e. the chirp is zero. The pulse source should avoid having too much of timing jitter since it may result in inter-channel interference of the OTDM signal. It has been shown that the integrated timing jitter of the pulse source needs to be less than $\frac{1}{12}$ of the OTDM signal time slot in order to obtain a BER of 10^{-9} [46].

Following Figure 1.1, we observe that the optical pulse train is split and data modulated, at the base rate, to give N individual Return to Zero (RZ) channels, where N is an integer. Mach-Zehnder or electro-absorption modulators can be used to impose data modulation on the optical signal. The N channels are then appropriately delayed and subsequently bit-interleaved to form a high-speed OTDM signal with an aggregate bit-rate of $N \times f_{base}$.

The high speed OTDM data signal is then transmitted through the fibre link, including dispersion compensation. During the transmission, the high-speed OTDM data signal will experience different types of degradation, e.g. amplified spontaneous emission, non-linearities and jitter. It is therefore necessary to regenerate the degraded OTDM data signal and thereby extend the transmission span. Signal regeneration can be performed using all optical regeneration schemes. Using the all-optical regeneration, optical-electrical-optical conversion is avoided. The all optical signal regeneration can be performed using semiconductor-optical-amplifiers in a Mach-Zehnder interferometer configuration [47–49], electro-absorption modulators [50–52], non-linear optical loop mirrors [53], self-phase-modulation in highly non-linear optical fibres [54] or a Kerr switch in highly non-linear optical fibre [55]. Due to the ultra-fast response of optical fibres and semiconductor components, all-optical regeneration has great potential for high speed OTDM signals. Using a Kerr switch in optical fibres, all-optical regeneration up to 160 Gb/s has been demonstrated [55].

At the receiver, the data signal is actively demultiplexed to a frequency

of the base rate, which is accessible by electronic processing. Demultiplexing has been demonstrated using semiconductor [56–58] or fibre-based solutions [16–19]. Using fibre-based demultiplexers, optical demultiplexing from 640 Gb/s to 10 Gb/s has been performed [17–19]. Moreover, optical demultiplexing from 320 Gb/s to 40 Gb/s has been demonstrated using semiconductor components [56].

In order to perform all optical signal regeneration and demultiplexing, a clock recovery unit is needed. For retiming purposes, a clock signal at the line rate of the OTDM data signal is needed and the recovered clock must then have less jitter than the degraded data signal [59]. For demultiplexing purposes, a recovered clock signal at the base rate is needed, i.e. so called pre-scaled clock recovery. In order to obtain an error free demultiplexing operation ($\text{BER}=10^{-9}$), the integrated recovered clock jitter needs to satisfy the following relation [46]:

$$\tau_{clk,jitt}^2 \leq \sqrt{(2/n)^2 - \tau_{sig,jitt}^2} \quad (1.1)$$

where $\tau_{sig,jitt}^2$ is the integrated jitter of the OTDM data signal which is to be demultiplexed. $n = 1$ for 40 Gb/s, $n = 2$ for 80 Gb/s and $n = 4$ for 160 Gb/s. Clock recovery in OTDM transmission systems can be performed using all-optical filtering [60,61], injection locking of mode-locked fibre/semiconductor lasers or electro-absorption modulators [62]–[67], and optoelectronic phase-locked loops [68]–[78].

1.2 Linear photonic RF front-end trends

Due to the convergence of wireless and fixed fibre optic networks, transmission of wireless signals over optical fibres is currently receiving a lot of attention. Typically, fibre-optic networks combine wireless and fixed signal transport infrastructure by using optical fibres to connect antenna base stations with the central office [25].

Driven by the increasing capacity in fixed fibre optic networks, future wireless communication systems aim at supporting applications which require high capacity, e.g. wireless video distribution systems, wireless controlled tele-surgery, airborne radar surveillance for environmental applications, all-weather landing systems, wireless point-to-point links for disaster recovery and broadband wireless access networks. In order to satisfy these high-capacity demands, wireless communication systems will move towards

higher carrier frequencies ([25,26] and references therein). The millimeter-wave band (30 GHz – 300 GHz) has therefore recently attracted considerable attention and research. The future applications mentioned above are also expected to require receivers that can accommodate different ranges of the received power level of the employed radio frequencies (RF), i.e. higher dynamic range ([27,28] and references therein). The ability to handle higher dynamic range translates into higher system capacity and therefore better economy. In general, the optical link, connecting the antenna base station and the central office, must exhibit large bandwidth, low noise and high dynamic range in order to preserve the benefits of higher carrier frequencies. The future goal, towards which many research groups are working, is therefore high performance transport of high-frequency wireless signals to and from antenna base stations over optical fibre links (radio-over-fibre links). However, moving towards higher carrier frequencies and at the same time requiring high dynamic range is very challenging, and many systems issues remain to be investigated and solved.

Most techniques for encoding a wireless signal onto an optical carrier use intensity modulation which typically results in a non-linear modulation that significantly limits the dynamic range. However, optical phase modulation for transmission of wireless signals from the antenna base station has recently attracted a lot of attention due to the linear response of conventional optical phase modulators [29]- [37]. The phase modulation thus has no fundamental limit on the dynamic range. This aspect also tends to improve the noise figure and link gain since higher power levels of the wireless RF signals can be tolerated [36,38]. In addition, the operational frequency of optical phase modulators can be extended to about the 100 GHz range [39].

In phase-modulated optical links, the wireless signal at the antenna base station is thus encoded on the phase of the optical carrier and then transmitted to the central office via fixed fibre infrastructure. However, the challenge is now moved to the receiver side. A traditional phase demodulator based on optical interference (coherent receiver) has a sinusoidal response and thus limits the dynamic range of a phase modulated link. The advantages of linear phase modulation are thereby lost. The challenge to implement a linear phase modulated link lies in the receiver structure. A few methods have recently been demonstrated to realize a linear coherent receiver either by using phase-locked loop [29]- [35] or post-detection digital-signal-processing [36,37]. So far, the highest operational frequency a linear coherent receiver has achieved is 1.45 GHz, using a broadband optical phase-lock loop [40]. The achieved dynamic range was 113 dB·Hz^{2/3}

in 1 Hz bandwidth.

In this thesis, focus is on a novel linear phase-locked coherent receiver with feedback and sampling for a phase modulated optical link. The aim is to achieve a dynamic range of $90 \text{ dB}\cdot\text{Hz}^{2/3}$ in a 500 MHz bandwidth at carrier frequencies exceeding 1 GHz.

A new time-domain numerical model of the phase-locked coherent receiver with feedback and sampling is developed, and the model is compared to experimental results. Using the numerical model, a detailed analysis is presented. We investigate how loop gain, tracking phase-modulator non-linearities and amplitude modulation influence the signal-to-intermodulation ratio of the demodulated signal. Furthermore, we propose a novel method to cancel out non-linearities associated with the tracking phase modulator in the receiver and inherent non-linear response of the balanced receiver. The proposed cancellation technique is investigated in terms of input (wireless) signal power and frequency. In addition, the effects of amplitude and timing jitter of the optical pulse source on the signal-to-noise ratio of the demodulated signal are investigated. Experimental results, for two-tone measurement, using the optical phase-locked coherent receiver with feedback and optical sampling are furthermore presented.

1.3 Main contributions of the thesis

Theoretical investigations and modelling of the PLLs including noise is important in order to understand the limitations and improve the properties of circuits based on phase-locking. A large amount of literature is available on this topic, see references [80, 94]. Possibly, the most general and rigorous treatment of the topic is that of Mehrotra . However, compared to electrical PLLs treated by Mehrotra in [94], the loop length of an optoelectronic PLL used for clock extraction in OTDM systems is longer and therefore the effect of time delay on the timing jitter of the recovered clock signal must be determined. Furthermore, in an optoelectronic PLL used in OTDM systems there are two oscillators, an electrical VCO and an optical clock generating laser, the timing jitter of the extracted clock signal will thereby be influenced by the phase noise of both oscillators. The phase noise of the recovered clock signal is filtered by the laser transfer function, with a characteristic knee frequency, f_{ch} , and thus this feature must also be included in the model equations. In addition, useful OTDM clock extraction optimization tools with emphasis on the timing jitter have not been

reported so far.

In this thesis, a detailed model of a clock recovery scheme based on an optoelectronic phase-locked loop taking into account the noise of the input data signal, the local Voltage Controlled Oscillator (VCO) and the laser employed in the loop is developed. The effects of loop time delay and the laser transfer function are also included in the model. We compute novel analytical expressions for correlation functions of the recovered clock signals. Using the correlation functions, analytical expressions for power spectral density and probability density function of the recovered clock signal are computed. Furthermore, a novel analytical expression for power spectral density of regenerated data signal, in the presence of clock recovery, is computed. Using the expressions, the timing jitter of the recovered optical clock, electrical clock and regenerated data signal is calculated, optimized and compared.

A novel phase-locked receiver with feedback and sampling for linear optical phase demodulation is presented. This thesis provides first detailed analysis and deeper understanding of the proposed phase-locked receiver. In addition, we report on a novel cancellation technique, for reducing the non-linearity associated with the tracking phase-modulator in the phase-locked receiver with feedback and sampling for phase modulated analog optical links.

A review of different linearization (cancellation) techniques can be found in reference [137]. So far, linearization techniques have been applied to intensity modulated analog optical links and mostly concentrated on the transmitter side. In many cases, the linearizer circuit was design to cancel either quadratic or cubic nonlinearity and cancellation of nonlinearities occurred in relatively narrow band (input RF signal power and frequency) [137]. We show that we can simultaneously cancel nonlinearities associated with the balanced receiver and tracking LO phase modulator by purely adjusting the loop gain and tailoring the nonlinearities of the tracking LO phase modulator. No extra circuitry is needed in order to obtain the cancellation. The proposed cancellation technique is frequency and power independent and has not been reported previously.

1.4 Structure of the thesis

The thesis is organized as follows: Chapter 2 to 4 deal with phase noise, clock recovery and optical regeneration with a special focus on timing jitter.

Chapter 5 presents a novel phase-locked coherent receiver with feedback and sampling for optical phase demodulation. All chapters are aimed at being self-contained and can be read independently of each other.

Chapter 2 deals with the effects of amplitude and phase noise on the power spectral density of a signal. A novel analytical expression for the power spectral density of a signal with amplitude and phase noise is derived. Furthermore, general stochastic differential equations are briefly reviewed. Analytical and numerical solutions of stochastic differential equations are also presented.

Chapter 3 deals with clock recovery for optical communication systems. A detailed theoretical model of a phase-locked loop clock recovery is developed and presented. Using the model the effects of signal timing jitter, loop gain, time delay on the recovered clock signal are determined. Furthermore, experimental results for 320 Gb/s clock recovery.

Chapter 4 deals with optical signal regeneration and demultiplexing. An analytical expression for the power spectral density of the regenerated clock signal is derived and the effects of signal timing jitter, pulse shape and pulse width on the regenerator performance are investigated at 160 Gb/s. Furthermore, the effects of timing jitter on a 160 Gb/s NOLM based demultiplexer are experimentally investigated. We also experimentally investigate how the impact of timing jitter on the NOLM based demultiplexer can be reduced.

Chapter 5 deals with a novel phase-locked coherent optical phase demodulator with feedback and sampling. A time domain numerical model is developed and the performance of the demodulator with feedback and sampling is investigated in terms of loop gain, tracking phase modulator, input signal frequency and power, amplitude and timing jitter noise of the optical pulse source. Furthermore, experimental results for the phase-locked coherent demodulator with feedback and sampling are presented. The entire work presented in Chapter 5 was performed at the University of California, Santa Barbara, (UCSB) in close collaboration with Prof. John E. Bowers, Leif A. Johansson, Hsu-Feng Chou and Anand Ramaswamy and Prof. Mark Rodwell.

Chapter 6 summarizes the main achievements and conclusions of the thesis.

Chapter 2

Noise Sources in Oscillators and Langevin Equations

Oscillators are an integral part of many electronic and optical systems, and constitute a main part of phase-locked loops. There is therefore wide range of applications where oscillators are used, e.g. clock generation in microprocessors, wireless and wireline communication systems. In ETDM optical communication systems, oscillators are used at the transmitter, regenerator and receiver to provide synchronization. In wireless communications systems, oscillators are used for frequency translation and channel selection.

In this chapter, a brief review of oscillator fundamentals and noise is given. The effect of amplitude and phase noise on the signal power spectral density is introduced and mathematically described. The combined effect of amplitude and phase noise on the power spectral density of a signal is illustrated with some numerical examples and compared to a measured power spectral density of an oscillator.

Stochastic differential equations (Langevin equations) describing systems with noise are also briefly reviewed. The associated system of stochastic differential equations describing the Ornstein-Uhlenbeck process, obtained by linearization of Langevin equations, is solved using Fourier techniques and we derive novel analytical expressions for the correlation functions. Moreover, a numerical scheme for solution of stochastic differential equations is presented and demonstrated.

The theory and results derived in this chapter are general and constitute a backbone for Chapter 3, 4 and 5.

2.1 Oscillator fundamentals

Oscillators produce a periodic output in the form of voltage. In simple terms, an oscillator can be considered as a unity-gain negative feedback amplifier [79]. A schematic of an oscillator is shown in Figure 2.1.

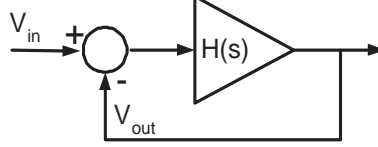


Figure 2.1: A schematic of an oscillator circuit. $H(s)$ is a transfer function of the amplifier. The Figure is taken from reference [79].

The closed loop transfer function, $G(s)$, of an oscillator circuit shown in Figure 2.1 is expressed as [79]:

$$G(s) = \frac{V_{out}(s)}{V_{in}(s)} = \frac{H(s)}{1 + H(s)} \quad (2.1)$$

where $s = j\omega$ and ω is angular frequency. $V_{in}(s)$ and $V_{out}(s)$ are Laplace transformation of input and output signal, respectively. and $H(s)$ is the transfer function of the amplifier. In order for oscillation to occur at frequency $\omega = \omega_0$, the transfer function of the amplifier $H(j\omega_0)$ needs to equal -1 (the closed loop gain $G(j\omega_0)$ will thereby approaches infinity). This condition can be satisfied if $\angle H(j\omega_0) = \pi$ and $|H(j\omega_0)| = 1$. However, in order for the oscillation to begin and to be sustained, the loop gain of the negative feedback circuit needs to satisfy the following equations [79]:

$$|H(j\omega_0)| \geq 1 \quad (2.2)$$

$$\angle H(j\omega_0) = \pi \quad (2.3)$$

Oscillators can be realized using Complementary Metal Oxide Semiconductor (CMOS) and Indium Phosphide (InP) technologies. Typically, the output of an oscillator is accompanied by noise. The oscillator noise is a very important design parameter which may effect the overall system performance where oscillators are involve. The description of oscillator noise and its impact on the power spectral density is addressed in the next sections.

2.2 Properties of phase noise in oscillators

In this section, a brief description of oscillator phase noise and its spectral characteristics are given. Important requirements for a good oscillator design include [80]:

- Low phase noise
- Frequency accuracy
- Wide tuning range
- Tuning linearity
- Low power consumption
- Small size
- Integration on chip

All these parameters are important, however, as the data rate of optical communication systems is being pushed higher and higher, phase noise becomes an important system design parameter. This is because as the data rate is increased, the requirements on oscillator phase noise become more stringent [79]. In optical communication systems, uncertainties in switching instants caused by oscillator phase noise lead to synchronization problems and this may lead to increased bit-error-rate. Apart from phase noise, oscillators usually also exhibit amplitude noise. However, for electrical oscillators the effect of amplitude noise can be significantly reduced by using an amplitude-control mechanism that largely suppresses amplitude fluctuations. In most practical cases, the effects of phase noise overshadow by far the effects of amplitude noise for the electrical oscillators.

Phase noise can be described in the time or frequency domain. However, from a practical point of view, the frequency domain description of phase noise is preferable. Usually, the phase noise of an oscillator is characterized from the measurement of power spectral density. Numerous measurements have shown consistently that the power spectral density of the phase noise tends to be well approximated by [80]:

$$W(\omega) \approx \frac{h_4}{\omega^4} + \frac{h_3}{\omega^3} + \frac{h_2}{\omega^2} + \frac{h_1}{\omega} + h_0 \quad (2.4)$$

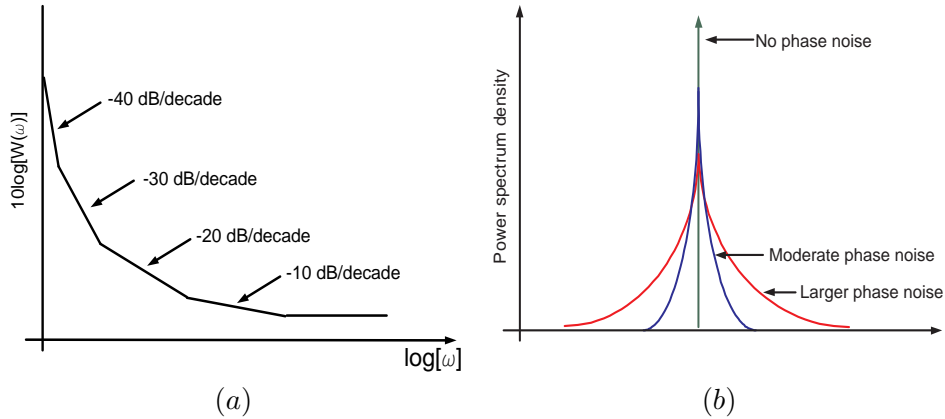


Figure 2.2: (a) Typical power spectral density of an oscillator phase noise. (b) Power spectrum density of an oscillator for increasing values of phase noise.

where ω is angular frequency and h_v are coefficients that are particular to each individual device. In Figure 2.2(a), $\log[W(\omega)]$ versus $\log[\omega]$ is plotted as connected straight line segments.

Each segment is labelled with the log-log slope in dB/decade. The $1/\omega^4$ contribution appears well below 1 Hz and it is normally not an issue for oscillators in phase-locked loops [80]. We are therefore not going to consider h_4/ω^4 term in this report. The other terms are significant. Each term arises from a different source of phase noise. The phase noise terms h_3/ω^3 and h_2/ω^2 arise from flicker¹ and white noise perturbations in the oscillator. The $1/\omega^3$ and $1/\omega^2$ noise spectral components are especially prevalent in electrical oscillators. The effect of different (frequency) noise terms on system performance is dependent on the application, where the oscillators are involved.

The number of publications on phase noise analysis exploded in the 1990s as the phase noise was becoming better recognized as a critical source of system degradation. Moreover, mathematical sophistication has advanced in the engineering community and advanced circuit analysis has been widely deployed to analyze the effects of phase noise. Notable publications include [81]- [92]. The two main (competing) approaches for oscillator phase noise analysis are based on Impulse Sensitivity Function (ISF) [81] and Non-linear Stochastic Differential Equations (NSDE) [89]. Briefly described, the

¹Noise with $1/\omega$ frequency dependence is called flicker noise.

ISF approach quantifies the phase disturbances caused by a noise impulse originating from a specific location in the oscillator circuit at a particular instant in the oscillation cycle. The ISF approach therefore provides information regarding the transformation of additive white and flicker noise into phase noise. An effective ISF is defined with regard to each noise source within the oscillator. The ISF method is applicable to all categories of oscillators: linear or non-linear, resonator-based or not. For a more detailed explanation of ISF approach, see [81]. The phase noise analysis presented in [89] is a non-linear analysis applicable to any oscillator that can be described by stochastic non-linear differential equations. In reference [89], the phase noise is modelled as a stochastic process and shows good agreement with experimental observations. The main outcome of the analysis presented in [89] is the power spectral density of the oscillator in the presence of phase noise. It has been shown that if the spectrum of the additive noise in the oscillator is white, the power spectral density of the oscillator has a Lorentzian shape, i.e. $1/\omega^2$ dependence. In this work, the approach presented in [89] is adopted for phase noise description, moreover, we also treat the systems with noise using the techniques from stochastic calculus. In the following, mathematical description of an oscillator output with phase noise is given.

In the absence of phase noise, the output of an oscillator can be expressed in Fourier series as follows:

$$g(t) = a_0 + \sum_{i=1}^{\infty} a_i \sin[i\omega_0 t] \quad (2.5)$$

where a_i are (real) Fourier coefficients and $\omega_0 = 2\pi f_0$. f_0 is the repetition frequency and i is an integer. If $g(t)$ is Fourier transformed, the power spectral density will contain discrete lines, i.e. delta functions at frequencies $\omega = i\omega_0$. In the presence of phase noise, which we denote $\alpha(t)$, the signal $g(t)$ becomes $g(t + \alpha(t))$ and is expressed in a Fourier series as:

$$g(t + \alpha(t)) = a_0 + \sum_{i=1}^{\infty} a_i \sin[i\omega_0 t + i\omega_0 \alpha(t)] \quad (2.6)$$

The effect of phase noise $\alpha(t)$ on a signal $g(t)$ is to create deviations or jitter in the repetition frequency, f_0 . In other words $\alpha(t)$ will cause spectral dispersion of a signal $g(t)$ and this is illustrated in Figure 2.2(b). Small amounts of phase noise cause small spreading and larger amounts of phase noise cause greater spreading as illustrated in Figure 2.2(b). A randomly

fluctuating phase noise, $\alpha(t)$, can be asymptotically described as a modulated Wiener (stochastic) process [90]:

$$\alpha(t) = \sqrt{c} B(t) \implies \frac{d\alpha}{dt} = \sqrt{c} \Gamma(t) \quad (2.7)$$

where $B(t)$ is described by a 1-D Brownian motion process [93] and c is a constant determining the amount of phase noise associated with the signal and the resulting spectral spreading. We recall that Wiener process is the limiting form of the random walk as time step approaches zero [93]. The constant c will depend on the type and design of the particular oscillator and it can be related to the circuit parameters as shown in references [89,90,94]. $\Gamma(t)$ is a stochastic Langevin noise force which is Gaussian distributed and is fully characterized by its ensemble mean value and correlation function:

$$\langle \Gamma(t) \rangle = 0 \quad \langle \Gamma(t) \Gamma(t') \rangle = \delta(t - t') \quad (2.8)$$

Using equations (2.7) and (2.8) the phase noise can be statistically characterized [95]:

$$\langle \alpha(t) \rangle = 0 \quad \langle |\alpha(t) - \alpha(t')|^2 \rangle = c|t - t'| \quad (2.9)$$

The ensemble average of the phase noise is thereby zero and its mean square value increases linearly with time. The autocorrelation function of $g(t + \alpha(t))$ as $t \rightarrow \infty$ is given by [89]:

$$\begin{aligned} R_{g,g}(t, \tau) &= \langle g(t + \alpha(t)) g^*(t + \tau + \alpha(t + \tau)) \rangle \\ &= \sum_{k,i=-\infty}^{\infty} a_k a_i^* e^{j(k-i)\omega_0 t} e^{-ji\omega_0 \tau} \langle e^{j\omega_0(k\alpha(t) - i\alpha(t+\tau))} \rangle \text{ for } t \rightarrow \infty \\ &= \sum_{i=-\infty}^{\infty} |a_i|^2 e^{-ji\omega_0 \tau} e^{-\frac{1}{2}\omega_0^2 i^2 c |\tau|} \end{aligned} \quad (2.10)$$

For practical applications, we are interested in obtaining the power spectral density of the signal $g(t + \alpha(t))$. The single-sided power spectral density of a signal $g(t + \alpha(t))$ is obtained by Fourier transformation of equation (2.10) [89]:

$$G(\omega) = \sum_{i=-\infty}^{\infty} \frac{|a_i|^2 \omega_0^2 i^2 c^2}{\frac{1}{4} \omega_0^4 i^4 c^2 + (\omega + i\omega_0)^2} \quad (2.11)$$

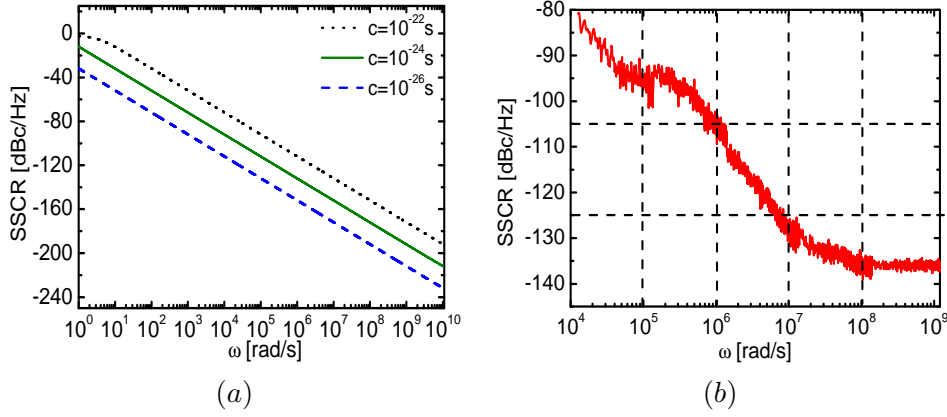


Figure 2.3: (a) Simulation results for the SSCR of a 10 GHz oscillator with phase noise for selected values of the constant c defined in equation (2.7). (b) Measured SSCR of a 10 GHz oscillator.

Equation (2.11) shows that the power spectral density of the signal with phase noise described as Brownian motion phase error will have a Lorentzian spectrum. This is a direct consequence of the driving term $\Gamma(t)$ having a constant power spectral density (white density).

Usually, we are interested in the power spectral density of the oscillator, $G(\omega)$, around the first harmonic, i.e. ω_0 . In practice, the Single-Sideband to Carrier Ratio (SSCR) (in dBc/Hz) is widely used to characterize the noise performance of the oscillators associated with the first harmonic. The SSCR is defined as [89]:

$$SSCR \equiv 10 \log_{10} \left(\frac{G(\omega_0 + \omega)}{|a_1|^2} \right) \quad (2.12)$$

where ω is the offset frequency from the first harmonic. The SSCR gives the amount of phase noise around the first harmonic. Using equation (2.12), the SSCR is shown around the first harmonic in Figure 2.3(a).

As can be seen in Figure 2.3(a), the SSCR is a straight line with a decay of 20 dB/decade which is a direct consequence of $\alpha(t)$ being Brownian motion phase error. It should be emphasized that by representing the phase noise as a Brownian motion phase error only the $1/\omega^2$ spectral component of phase noise is taken into account. The level of the SSCR curves shown in Figure 2.3(a) is fully determined by a constant c , defined in equation (2.7), and as expected the SSCR curves increase for increasing values of c .

In order to compare theoretical results for SSCR, obtained by using equation (2.12), with experimental results, we have measured the SSCR of an oscillator. The measured SSCR is shown in Figure 2.3(b). The experimental set-up was made by Darko Zibar and the measurement were also performed by Darko Zibar. The circuit parameters of the the oscillator were not measured. It is observed that within the frequency range 10^5 rad/s to 10^7 rad/s, the measured SSCR decreases with 20 dB/decade being in good agreement with the theoretical results. Beyond 10^8 rad/s there is a constant level (white noise). Moreover, below 10^5 rad/s, the measured SSCR curve has also a decay of 20 dB/decade, but, the curve is shifted to a lower level compared to the part of the SSCR curve in the 10^5 to 10^7 rad/s frequency range. In conclusion, the measured SSCR shown in Figure 2.3(b) is qualitatively in accordance with the theoretical SSCR curve in Figure 2.3(a) in the frequency range 10^5 rad/s to 10^7 rad/s. Outside this frequency range we do not find correlation between the theoretical and measured SSCR curves. This is because the theoretical SSCR curve only contains $1/\omega^2$ spectral component of phase noise.

2.2.1 Timing jitter

In the previous section, a mathematical description of a signal with phase noise has been given. In this section, we relate the phase noise of a signal to the accumulated timing jitter (time deviation). This is because, in some applications, such as clock generation, and recovery a characterization of the time deviation of signal with phase noise can be of great interest. Time deviation in the clock generation is a measure of the accuracy of the clock signal. The clock generation with large amount of time deviation may lead to system degradation, e.g. bit error rate and inter-symbol-interference.

In general, the phase noise, $\alpha(t)$, creates deviations or jitter in the zero-crossing or transition times of the signal $g(t)$. This is commonly referred to as timing jitter and the effect of jitter on a signal is illustrated in Figure 2.4(a). Alternatively, the deviation of each period from the ideal value can be called jitter.

In addition, we have observed that the variance of phase noise, $\alpha(t)$, shown in equation (2.9) increases linearly with time and this means that the zero-crossings will broaden with time. Using the power density spectrum of the signal shown in equation (2.11), the timing jitter (around the first harmonic) can then be calculated using the Von der Linde method [96]:

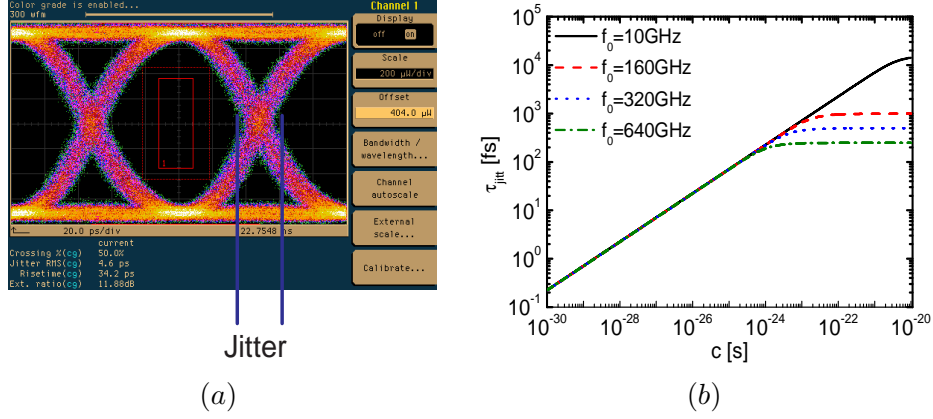


Figure 2.4: (a) Measured eye diagram indicating deviation of zero crossing levels due to the jitter of the signal. The Figure is a courtesy of Professor Michael Green. (b) Timing jitter as a function of a constant c for selected values of repetition frequency, f_0 . Integration range: 1 Hz – $f_0/2$.

$$\tau_{jitt} \equiv \frac{1}{\omega_0} \sqrt{\frac{1}{\pi} \int_{\omega_{min}}^{\omega_{max}} \frac{G(\omega_0 + \omega)}{|a_1|^2} d\omega} \quad (2.13)$$

where $\omega_{min} = 2\pi f_{min}$ and $\omega_{max} = 2\pi f_{max}$. The f_{min} and f_{max} are the lower and upper integration limits, respectively. Inserting $G(\omega_0 + \omega)$ in equation (2.13) and performing the integration (MAPLE 9.5 software is used) around the first harmonic ($i = 1, -1$), the timing jitter of a signal having Brownian motion phase error becomes:

$$\begin{aligned} \tau_{jitt} &= \frac{\sqrt{2}}{\sqrt{\pi}\omega_0} \\ &\times \left[\arctan\left(\frac{\omega_{max}}{\frac{1}{2}\omega_0^2 c}\right) + \arctan\left(\frac{\omega_{max} + \omega_0}{\frac{1}{2}\omega_0^2 c}\right) \right. \\ &\left. - \arctan\left(\frac{\omega_{min}}{\frac{1}{2}\omega_0^2 c}\right) - \arctan\left(\frac{\omega_{min} + \omega_0}{\frac{1}{2}\omega_0^2 c}\right) \right]^{1/2} \quad (2.14) \end{aligned}$$

Equation (2.14) relates the phase noise of the signal to the timing jitter. Using equation (2.14), we can compute the timing jitter of a signal with phase noise as a function of the constant c for the specified integration range $f = [f_{min}; f_{max}]$. In Figure 2.4(b), timing jitter is computed as a

function of the constant c using equation (2.14). The repetition frequency of a signal, f_0 , is varied from 10 GHz to 640 GHz and the corresponding integration range is: $1 \text{ Hz} - f_0/2$. As shown in Figure 2.4(b), the integrated timing jitter, τ_{jitt} , at first increases as a function of c irrespective of the repetition frequency f_0 , whereafter it asymptotically reaches its final value. The final value of τ_{jitt} is dependent on the repetition frequency and the integration range in general.

2.3 Combined effect of phase and amplitude noise in oscillators

In practice, the signal may not only contain phase noise but, it may also contain amplitude noise. It is therefore essential to determine the combined effects of amplitude and phase noise on the power spectral density of a signal. In this way, we will be able to quantify the contributions associated with the amplitude and phase noise to the power spectral density of the signal, and determine which one of the effects is most detrimental for the particular system performance.

The output of an oscillator in the presence of amplitude and phase noise is expressed as:

$$f(t) = [1 + \beta(t)]g(t + \alpha(t)) \quad (2.15)$$

where $\alpha(t)$ and $\beta(t)$ are assumed to be real wide-sense stationary stochastic processes with zero mean. A stochastic process is called wide-sense stationary if its ensemble average is constant ($\langle \alpha(t) \rangle = \langle \beta(t) \rangle = 0$) and its autocorrelation only depends on $\tau = t - t'$ [93]:

$$\langle \alpha(t)\alpha(t') \rangle = R_\alpha(t - t') = R_\alpha(\tau) \quad \langle \beta(t)\beta(t') \rangle = R_\beta(t - t') = R_\beta(\tau) \quad (2.16)$$

The amplitude noise $\beta(t)$ is modelled as a band-limited white noise process (low-pass filtered white noise) with the power spectral density phenomenologically expressed as [97]:

$$S_\beta(\omega) = \frac{\eta}{[1 + (\omega/\omega_R)^2]} \quad (2.17)$$

where $\omega_R = 2\pi f_R$. η is low-frequency amplitude noise spectrum and f_R is a roll-off frequency. In time domain, the amplitude noise, $\beta(t)$, is governed by the following equation:

$$\frac{d\beta}{dt} = -\frac{\beta(t)}{\tau_R} + \frac{\eta}{\tau_R}\Gamma(t) \quad (2.18)$$

where $\tau_R = 1/\omega_R$. The corresponding autocorrelation function of the amplitude noise, $\beta(t)$, is obtained by taking the inverse Fourier transformation of equation (2.17):

$$R_{\beta,\beta}(t, \tau) = \langle \beta(t)\beta^*(t + \tau) \rangle = \frac{1}{2}\eta\omega_R e^{-\omega_R|\tau|} \equiv \sigma_\beta^2 e^{-\omega_R|\tau|} \quad (2.19)$$

where $\sigma_\beta = \frac{1}{2}\eta\omega_R$. In practice, the amplitude and phase noise are expected to be correlated, however, for simplicity, we are going to assume that the amplitude and phase noise are uncorrelated $\langle \alpha(t)\beta(t) \rangle = \langle \alpha(t) \rangle \langle \beta(t) \rangle$. The autocorrelation function of the signal in the presence of amplitude and phase noise is expressed as:

$$\begin{aligned} R_{f,f}(t, \tau) &= \langle g(t)g^*(t + \tau) \rangle \\ &= \langle [1 + \beta(t)]g(t + \alpha(t))[1 + \beta(t + \tau)]g^*(t + \tau + \alpha(t + \tau)) \rangle \\ &= R_{g,g}(t, \tau)[1 + R_{\beta,\beta}(t, \tau)] \end{aligned} \quad (2.20)$$

where $R_{\beta,\beta}(t, \tau)$ is the autocorrelation function of the amplitude noise and $R_{g,g}(t, \tau)$ is the autocorrelation function of the signal with phase noise, see equation (2.10). Inserting equation (2.10) and (2.19) in (2.20), the autocorrelation function of a signal with amplitude and phase noise becomes:

$$\begin{aligned} R_{f,f}(t, \tau) &= \sum_{i=-\infty}^{\infty} |a_i|^2 e^{-ji\omega_0\tau} e^{-\frac{1}{2}\omega_0^2 i^2 c |\tau|} \\ &\quad + \sum_{i=-\infty}^{\infty} |a_i|^2 \sigma_\beta^2 e^{-ji\omega_0\tau} e^{-\left(\frac{1}{2}\omega_0^2 i^2 c - \omega_R\right)|\tau|} \end{aligned} \quad (2.21)$$

The power spectral density of the signal with amplitude and phase noise is obtained by taking the Fourier transformation of equation (2.21):

$$F(\omega) = \sum_{i=-\infty}^{\infty} \frac{|a_i|^2 \omega_0^2 i^2 c^2}{\frac{1}{4}\omega_0^4 i^4 c^2 + (\omega + i\omega_0)^2} + \frac{\sigma_\beta^2 |a_i|^2 (\omega_0^2 i^2 c + 2\omega_R)}{\left(\frac{1}{2}\omega_0^2 i^2 c + \omega_R\right)^2 + (\omega + i\omega_0)^2} \quad (2.22)$$

Equation (2.22) relates amplitude and phase noise of a signal in frequency domain. Moreover, equation (2.22) can be used to illustrate the effects of amplitude and phase noise on the power spectral density of a signal and this is shown in next section with some numerical examples.

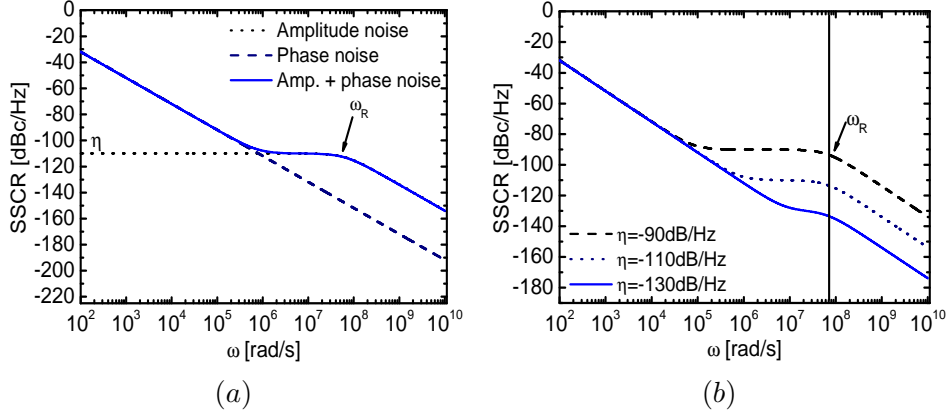


Figure 2.5: (a) SSCR of a signal with amplitude, phase and amplitude and phase noise. (b) SSCR of a signal with amplitude and phase noise for selected values of η .

2.3.1 Power spectrum density of a signal with amplitude and phase noise

In Figure 2.5(a), the SSCR of a signal with amplitude and phase noise is computed around the first harmonic using equation (2.22). In addition, the sole effects of amplitude and phase noise on the SSCR are illustrated as well.

It is observed in Figure 2.5(a) that in the presence of phase noise only, the SSCR decays with 20 dB/decade as expected. In the presence of amplitude noise only, the SSCR contains a frequency independent level (white noise) of magnitude η and beyond $\omega = \omega_R$, the SSCR curve has a decay of 20 dB/decade. In the presence of amplitude and phase noise simultaneously, the SSCR is dominated by phase noise at frequencies well below ω_R and for frequencies beyond ω_R , it is fully dominated by the amplitude noise. In general, we can say that for the frequency range close to the carrier, the power spectral density $F(\omega)$ is dominated by phase noise.

In Figure 2.5(b), the SSCR is plotted for selected values of low-frequency amplitude noise, η , while the roll-off frequency, ω_R , is held constant. For increased values of η , the SSCR becomes dominated by the amplitude noise as expected. However, for relatively low values of $\eta = -130$ dB/Hz, the SSCR is mostly dominated by phase noise. In Figure 2.6, the SSCR is computed for selected values of the roll-off frequency $\omega_R = 2\pi f_R$ while

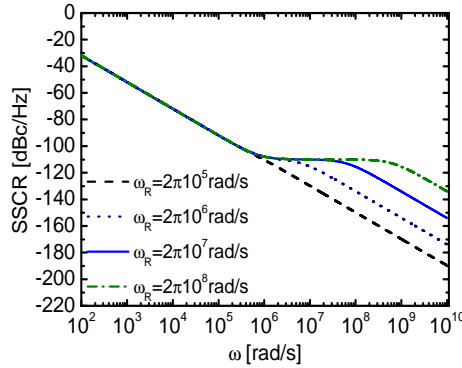


Figure 2.6: Frequency spectrum of a signal with amplitude and phase noise for selected values of roll-off angular frequency ω_R . Low-frequency amplitude noise $\eta = -110$ dB/Hz

η is now held constant. It is observed that as the roll-off frequency is decreased the contribution of amplitude noise to the SSCR decreases. For relatively low values of roll-off frequency ($\omega_R = 2 \cdot \pi \cdot 10^5$ rad/s) the impact of amplitude noise on SSCR is negligible.

2.4 Stochastic differential equations

In sections 2.2 and 2.3, the effects of phase and amplitude noise on the power spectrum density of the signal was mathematically described and illustrated with some numerical examples. In practice, signals with noise are input to a system which then performs various operations on them, e.g. clock recovery, demodulation and analog-to-digital conversion. In addition, the system itself will have various noise sources which will add to the total noise. From a practical point of view, we would like to determine how the input signal noise is transferred to the output of the system, also taking into account the system itself may contribute with the noise. In this and the following sections, mathematical descriptions of systems driven by noisy signals are given.

It is very common and convenient to describe dynamics of a systems with linear models. Among the tools for linear analysis, the Laplace and Fourier transforms are particularly valuable [98]. The related concept of a transfer function which relies on Fourier and Laplace transforms describes a transform domain relation between the input and output of a linear systems.

The concept of transfer functions is an extremely powerful tool and widely used by system designers. However, the concept of a transfer functions can only be applied to linear circuits. Therefore, linearization needs to be performed in order to analyze non-linear systems using the concept of transfer functions. However, this can sometimes yield erroneous results since linearization may only be valid for specific operation conditions. In the presence of a noisy signal and noise from the system itself, the method based on transfer functions can be used, however, noise sources need to be considered as additive. It has also been shown that noise generation (and frequency acquisition) in oscillators and phase-locked loops is inherently a non-linear process and linear noise techniques are not rigorously justified [89, 90, 94]. Moreover, since noise is an integral part of the oscillators output, it may lead to incorrect results to consider noise as an additive source.

Instead of using transfer functions to describe the dynamics of a system, a system of ordinary differential equations can be used as an alternative approach. Moreover, if the noise sources are modelled as stochastic processes they can be easily included in the system of differential equations (stochastic differential equations). Stochastic differential equations are a more general way of describing the dynamics of the system. Furthermore, many approximations associated with the transfer functions approach are avoided by using stochastic differential equations to describe the system. However, the solution of differential equations can under certain conditions be expressed with transfer functions. Mathematical models of various electronic and optoelectronic systems, based on (stochastic) differential equations, have shown an excellent agreement with experimental results [33, 89, 90, 94]. However, the challenge lies in solving the stochastic differential equations as explained below.

In Chapter 3 and 4 of thesis, we are however going to use linearized form of stochastic differential equations. Compared to linear transfer function theory, the advantages of using linearized SDEs are limited. However, using linearized SDEs correlations functions among different noise sources and variables in the considered system can be analytically determined. This enables us to compute power spectral densities and probability density functions. The probability density functions could potentially be used to make a very accurate computations of the BERs. Furthermore, using correlations functions multiplicative noise sources (as in the case of optical regeneration) can be easily handled once the correlation functions are determined.

The dynamics of a time-varying system (phase-locked loop, oscillator or semiconductor laser) without any undesired perturbations can be described by a system of ordinary differential equations [95]:

$$\begin{aligned}\frac{dx_1}{dt} &= F_1(x_1(t), \dots, x_n(t)) \\ &\vdots \\ \frac{dx_n}{dt} &= F_n(x_1(t), \dots, x_n(t))\end{aligned}\tag{2.23}$$

where n represents the order of the system under consideration and $x_n(t)$ represents state variables of the physical system under consideration. We are interested in the response of such system to a state-dependent perturbation (force) $d_n(x_n)\Gamma_n(t)$. $\Gamma_n(t)$ is a stochastic Langevin force with Gaussian distribution and correlation functions described in equation (2.8). The d_n term determines the size of the perturbation force and may, in general, depend on the state variable $x_n(t)$. If d_n is dependent on $x_n(t)$ we say that we have multiplicative noise force and if d_n is not dependent on $x_n(t)$, we have an additive noise force. In the presence of a perturbation by noise, the system of differential equations shown in equation (2.23) is rewritten as:

$$\begin{aligned}\frac{dx_1}{dt} &= F_1(x_1(t), \dots, x_n(t)) + \sum_{j=1}^n d_{1j}(x_1, \dots, x_n)\Gamma_j(t) \\ &\vdots \\ \frac{dx_n}{dt} &= F_n(x_1(t), \dots, x_n(t)) + \sum_{j=1}^n d_{nj}(x_1(t), \dots, x_n(t))\Gamma_j(t)\end{aligned}\tag{2.24}$$

where d_{nj} determine the size of the perturbation force. The system of equations shown in (2.24) is a system of stochastic differential equations, so called Langevin equations. In short, Langevin equations can be written as:

$$\frac{d\mathbf{X}}{dt} = \mathbf{F}(\mathbf{X}) + \mathbf{D}(\mathbf{X})\mathbf{\Gamma}(t)\tag{2.25}$$

where $\mathbf{X} = [x_1, \dots, x_n]^T$, $\mathbf{F} = [F_1, \dots, F_n]^T$, $\mathbf{\Gamma} = [\Gamma_1, \dots, \Gamma_n]$ and \mathbf{D} is a matrix containing d_{nj} as elements. \mathbf{X} , \mathbf{F} and $\mathbf{\Gamma}$ are all assumed to be real vectors. In (2.25), it is assumed that the noise sources $\Gamma_n(t)$ are not correlated and this is expressed as:

$$\langle \Gamma_l(t) \Gamma_k(t') \rangle = \delta_{lk} \delta(t - t') \quad (2.26)$$

where δ_{lk} is the Kronecker δ -function which equals unity for $l = k$ and vanishes otherwise. However, in practice, there will be some degree of correlation and this may be expressed as [99]:

$$\langle \Gamma_n(t) \Gamma_m(t') \rangle = \frac{\varrho}{\tau_c} e^{-|\tau|/\tau_c} \quad (2.27)$$

where $\tau = t - t'$ and ϱ is a constant. τ_c represents the correlation time and if it is much smaller than other time constants in the described system, it is a reasonable approximation to say that noise sources are δ -function correlated. We also note that the phase noise expressed by equation (2.7) is a special case of a one dimensional Langevin equations for $x_1(t) = \alpha(t)$, $\mathbf{F} = 0$ and $d_1 = \sqrt{c}$. The solution of Langevin equations are by no means trivial to obtain. However, in some special cases an analytical solution of the Langevin equations is obtainable as it will be shown in section 2.4.1. Moreover, numerical integration schemes can be used to obtain solutions of Langevin equation and this is demonstrated in section 2.4.2.

2.4.1 Computation of correlation functions for Ornstein-Uhlenbeck process

In this section, it is shown that for an Ornstein-Uhlenbeck process, which is a special case of the Langevin equations, an analytical solution can be obtained. (For a detailed explanation of Ornstein-Uhlenbeck process, see [95].) First, we derive novel expressions for cross-spectral and cross-correlation functions of an Ornstein-Uhlenbeck process, see [59]. The cross-correlation functions are later used to in Chapter 3 and 4 to compute the autocorrelation function of the recovered clock signals and the resulting power spectral density.

If a small signal expansion is performed near a stationary point \mathbf{X}_0 assuming $\mathbf{F}(\mathbf{X}_0) = 0$ then $\mathbf{F}(\mathbf{X}) \approx \mathbf{F}(\mathbf{X}_0) - \mathbf{A}\mathbf{X}$, where \mathbf{A} is a constant matrix. Furthermore, if we assume that the noise drive terms d_{nj} are independent of state variables, the Langevin equation shown in (2.25) is reduced to an Ornstein-Uhlenbeck process:

$$\frac{d\mathbf{X}}{dt} = -\mathbf{A}\mathbf{X} + \mathbf{D}\Gamma(t) \quad (2.28)$$

Equation (2.28) can be solved by Fourier transformation technique. Introducing the Fourier transform $X(\omega)$ of $x(t)$ and $\Gamma(\omega)$ of $\Gamma(t)$:

$$X(\omega) = \int_{-\infty}^{\infty} x(t)e^{-j\omega t} dt \Leftrightarrow x(t) = \frac{1}{2\pi} \int_{-\infty}^{\infty} X(\omega)e^{j\omega t} d\omega \quad (2.29)$$

$$\Gamma(\omega) = \int_{-\infty}^{\infty} \Gamma(t)e^{-j\omega t} dt \Leftrightarrow \Gamma(t) = \frac{1}{2\pi} \int_{-\infty}^{\infty} \Gamma(\omega)e^{j\omega t} d\omega \quad (2.30)$$

and applying the inverse Fourier transformation on equation (2.28), the following is obtained:

$$\begin{aligned} \frac{1}{2\pi} \int_{-\infty}^{\infty} j\omega \mathbf{X}(\omega)e^{j\omega t} d\omega &= \frac{1}{2\pi} \int_{-\infty}^{\infty} (-\mathbf{A}\mathbf{X}(\omega) + \mathbf{D}\Gamma(\omega))e^{j\omega t} d\omega \\ \Rightarrow \frac{1}{2\pi} \int_{-\infty}^{\infty} ((\mathbf{A} + j\omega\mathbf{I})\mathbf{X}(\omega) - \mathbf{D}\Gamma(\omega))e^{j\omega t} d\omega &= 0 \end{aligned} \quad (2.31)$$

where \mathbf{I} is the unity matrix. Equation (2.31) equals zero if $((\mathbf{A} + j\omega\mathbf{I})\mathbf{X}(\omega) - \mathbf{D}\Gamma(\omega))$ equals zero at all time instants. The solution of (2.28) can thereby be expressed as:

$$(\mathbf{A} + j\omega\mathbf{I})\mathbf{X}(\omega) - \mathbf{D}\Gamma(\omega) = 0 \Rightarrow \mathbf{X}(\omega) = (\mathbf{A} + j\omega\mathbf{I})^{-1}\mathbf{D}\Gamma(\omega) \quad (2.32)$$

Next, we want to determine the cross-correlation functions $\langle \mathbf{x}(\tau)\mathbf{x}(0) \rangle$, where we have assumed that $\mathbf{x}(t)$ is a wide-sense stationary process. This is achieved by first computing the cross-spectral densities $\langle \mathbf{X}(\omega)(\mathbf{X}^*(\omega))^T \rangle$ and then transforming to the time domain. However, we first need to determine the spectral properties of Langevin noise terms. According to the Wiener-Khintchine theorem, correlation properties in the frequency domain for Langevin noise are determined using the following expression [100]:

$$\langle \Gamma(\omega)\Gamma^*(\omega') \rangle = 2\pi Y(\omega)\delta(\omega - \omega') \quad (2.33)$$

where $Y(\omega)$ is the power spectral density of the Langevin noise and is a constant equal to 1 (white noise). Now, the cross-spectral densities $\langle \mathbf{X}(\omega)(\mathbf{X}^*(\omega))^T \rangle$ are expressed as:

$$\begin{aligned} \mathbf{S}(\omega) &\equiv \langle \mathbf{X}(\omega)(\mathbf{X}^*(\omega))^T \rangle \\ &= \langle (\mathbf{A} + j\omega\mathbf{I})^{-1}\mathbf{D}\Gamma(\omega)[(\mathbf{A} - j\omega\mathbf{I})^{-1}\mathbf{D}\Gamma^*(\omega)]^T \rangle \\ &= \langle (\mathbf{A} + j\omega\mathbf{I})^{-1}\mathbf{D}\Gamma(\omega)[(\Gamma^*(\omega))^T(\mathbf{A} - j\omega\mathbf{I})^{-1}\mathbf{D}]^T \rangle \\ &= 2\pi(\mathbf{A} + j\omega\mathbf{I})^{-1}\mathbf{D}\mathbf{D}^T(\mathbf{A}^T - j\omega\mathbf{I})^{-1} \end{aligned} \quad (2.34)$$

where we have used $[(\mathbf{A} - j\omega\mathbf{I})^{-1}]^T = [(\mathbf{A}^T - j\omega\mathbf{I})^{-1}]$ [101]. We want to determine the elements, s_{lm} of the matrix $\mathbf{S}(\omega)$, where l and m are integers. Let us define unit vectors $\hat{\mathbf{e}}_1^T = [1 \ \cdots \ 0]$, $\hat{\mathbf{e}}_2^T = [0 \ \cdots 1 \ \cdots \ 0]$ and $\hat{\mathbf{e}}_n^T = [0 \ \cdots \ 1]$. The eigenvectors \mathbf{V}_j corresponding to the eigenvalues λ_j of the matrix \mathbf{A}^T satisfying; $\mathbf{A}^T \mathbf{V}_j = \lambda_j \mathbf{V}_j$. Using the eigenvectors, we define a new basis in which the unit vectors are expressed as:

$$\begin{aligned} \hat{\mathbf{e}}_k &= b_{k_1} \begin{bmatrix} v_{11}^1 \\ v_{12}^1 \\ \vdots \\ v_{1n}^1 \end{bmatrix} + b_{k_2} \begin{bmatrix} v_{11}^2 \\ v_{12}^2 \\ \vdots \\ v_{1n}^2 \end{bmatrix} + \cdots + b_{k_n} \begin{bmatrix} v_{11}^n \\ v_{12}^n \\ \vdots \\ v_{1n}^n \end{bmatrix} \\ &= b_{k_1} \mathbf{V}_1 + b_{k_2} \mathbf{V}_2 + \cdots + b_{k_n} \mathbf{V}_n \end{aligned} \quad (2.35)$$

where $k = 1, \dots, n$ and b_{k_1}, \dots, b_{k_n} are constants. The coefficients s_{lm} of the matrix $\mathbf{S}(\omega)$ can now be expressed as:

$$\begin{aligned} s_{lm} &= \hat{\mathbf{e}}_l^T \mathbf{S} \hat{\mathbf{e}}_m \\ &= 2\pi(b_{l_1} \mathbf{V}_1^T + \cdots + b_{l_n} \mathbf{V}_n^T)(\mathbf{A} + j\omega\mathbf{I})^{-1} \mathbf{D} \mathbf{D}^T \\ &\quad \cdot (\mathbf{A}^T - j\omega\mathbf{I})^{-1} (b_{m_1} \mathbf{V}_1 + \cdots + b_{m_n} \mathbf{V}_n) \\ &= \sum_{i=1}^n \sum_{k=1}^n 2\pi b_{l_i} \mathbf{V}_i^T [(\mathbf{A} + j\omega\mathbf{I})^{-1} \mathbf{D} \mathbf{D}^T \\ &\quad \cdot (\mathbf{A}^T - j\omega\mathbf{I})^{-1}] \mathbf{V}_k b_{m_k} \end{aligned} \quad (2.36)$$

where n is the order of the system, see equation (2.23). We recall that:

$$\begin{aligned} (\mathbf{A}^T \mathbf{V}_j)^{-1} = (\lambda_j \mathbf{V}_j)^{-1} &\Rightarrow (\mathbf{A}^T \mathbf{V}_j)^{-1} \mathbf{V}_j = \frac{1}{\lambda_j} \mathbf{V}_j^{-1} \mathbf{V}_j \\ &\Rightarrow (\mathbf{A}^T)^{-1} \mathbf{V}_j = \frac{1}{\lambda_j} \mathbf{V}_j \end{aligned} \quad (2.37)$$

Furthermore,

$$\begin{aligned}
(\mathbf{A}^T \mathbf{V}_j)^T &= (\lambda_j \mathbf{V}_j)^T \\
\Rightarrow \mathbf{V}_j^T \mathbf{A} &= \lambda_j \mathbf{V}_j^T \Rightarrow (\mathbf{V}_j^T \mathbf{A})^{-1} = (\lambda_j \mathbf{V}_j^T)^{-1} \\
\Rightarrow (\mathbf{V}_j^T)^{-1} \mathbf{A}^{-1} \frac{1}{\lambda_j} &= (\mathbf{V}_j^T)^{-1} \\
\Rightarrow \mathbf{V}_j (\mathbf{V}_j^T)^{-1} \mathbf{A}^{-1} &= \frac{1}{\lambda_j} \mathbf{V}_j (\mathbf{V}_j^T)^{-1} \Rightarrow \mathbf{V}_j^T \mathbf{A}^{-1} = \frac{1}{\lambda_j} \mathbf{V}_j^T
\end{aligned} \tag{2.38}$$

We recall that \mathbf{A} and \mathbf{A}^T have the same eigenvalues [101], and matrices $(\mathbf{A} + j\omega \mathbf{I})$ and $(\mathbf{A}^T - j\omega \mathbf{I})$ have thereby the following eigenvalues $(\lambda_j + j\omega)$ and $(\lambda_j - j\omega)$, respectively. We have therefore the following:

$$\mathbf{V}_i^T (\mathbf{A} + j\omega \mathbf{I})^{-1} = \frac{\mathbf{V}_i^T}{\lambda_i + j\omega} \tag{2.39}$$

$$(\mathbf{A}^T - j\omega \mathbf{I})^{-1} \mathbf{V}_k = \frac{\mathbf{V}_k}{\lambda_k - j\omega} \tag{2.40}$$

Inserting equation (2.39) and (2.40) in equation (2.36), the following is obtained:

$$\begin{aligned}
s_{lm} &= \sum_{i=1}^n \sum_{k=1}^n \frac{2\pi b_{l_i} \mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_k b_{m_k}}{(\lambda_i + j\omega)(\lambda_k - j\omega)} \\
&= \sum_{i=1}^n \sum_{k=1}^n \frac{2\pi b_{l_i} \mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_k b_{m_k}}{(\lambda_i + \lambda_k)} \left(\frac{1}{\lambda_k - j\omega} + \frac{1}{\lambda_i + j\omega} \right)
\end{aligned} \tag{2.41}$$

In order to obtain the end result in equation (2.41), the following decomposition was used:

$$\frac{1}{\lambda_i + j\omega} \frac{1}{\lambda_k - j\omega} = \frac{1}{\lambda_i + \lambda_k} \left(\frac{1}{\lambda_i + j\omega} + \frac{1}{\lambda_k - j\omega} \right) \tag{2.42}$$

Taking the inverse Fourier transformation of equation (2.41), the cross-correlation functions are found as follows:

$$\begin{aligned}
s_{lm}(\tau) &= \langle x_l(\tau)x_m(0) \rangle \\
&= \sum_{i=1}^n \sum_{k=1}^n \frac{b_{l_i} \mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_k b_{m_k}}{(\lambda_i + \lambda_k)} \left(e^{\lambda_k \tau} \mu(-\tau) + e^{-\lambda_i \tau} \mu(\tau) \right)
\end{aligned} \tag{2.43}$$

where $\mu(\cdot)$ is the unit step function. Since the matrix $\mathbf{D} \mathbf{D}^T$ is symmetric, we have: $\mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_k = (\mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_k)^T = \mathbf{V}_k^T \mathbf{D} \mathbf{D}^T \mathbf{V}_i$. When $l = m$, equation (2.43) can then be expressed as:

$$\begin{aligned}
s_{mm}(\tau) &= \sum_{i=1}^n \left(\frac{b_{m_i} \mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_i b_{m_i}}{2\lambda_i} + \sum_{j \neq i, j=1}^n \frac{b_{m_i} \mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_j b_{m_j}}{\lambda_i + \lambda_j} \right) \cdot e^{-\lambda_i |\tau|} \\
&= \sum_{i=1}^n \nu_i^{mm} e^{-\lambda_i |\tau|}
\end{aligned} \tag{2.44}$$

Furthermore, when $l \neq m$, $s_{lm}(\tau) + s_{ml}(\tau)$ can be expressed as:

$$\begin{aligned}
s_{lm}(\tau) + s_{ml}(\tau) &= 2 \sum_{i=1}^n \left(\frac{b_{l_i} \mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_i b_{m_i}}{2\lambda_i} + \sum_{j \neq i, j=1}^n \frac{b_{l_i} \mathbf{V}_i^T \mathbf{D} \mathbf{D}^T \mathbf{V}_j b_{m_j}}{\lambda_i + \lambda_j} \right) \\
&\quad \cdot e^{-\lambda_i |\tau|} = 2 \sum_{i=1}^n \nu_i^{lm} e^{-\lambda_i |\tau|}
\end{aligned} \tag{2.45}$$

The constants b_{l_i} , where l and i are integers, appearing in expressions (2.44) and (2.45) are the elements of a vector \mathbf{B}_l and are determined by solving the following matrix equation:

$$\mathbf{B}_l = \mathbf{V}^{-1} \hat{\mathbf{e}}_l, \quad \mathbf{V} = \begin{bmatrix} v_{11} & v_{21} & \dots & v_{n1} \\ v_{12} & v_{22} & \dots & v_{n2} \\ \vdots & \vdots & \dots & \vdots \\ v_{1n} & v_{2n} & \dots & v_{nn} \end{bmatrix} \equiv \begin{bmatrix} v_{11}^1 & v_{11}^2 & \dots & v_{11}^n \\ v_{12}^1 & v_{12}^2 & \dots & v_{12}^n \\ \vdots & \vdots & \dots & \vdots \\ v_{1n}^1 & v_{1n}^2 & \dots & v_{1n}^n \end{bmatrix} \tag{2.46}$$

where $\hat{\mathbf{e}}_l$ are unit vectors.

The correlation functions presented in equations (2.44) and (2.45) are used in Chapter 3 and 4 to determine the dynamics of optoelectronic phase-locked loop based clock recovery and optical regenerator.

2.4.2 Numerical solution of Langevin equations

In the previous section, an analytical solution for the Ornstein-Uhlenbeck process was given and the cross-correlation functions were determined. However, the Ornstein-Uhlenbeck process is based on a linearization of the Langevin equations and this approach may not always be valid. For instance, the Ornstein-Uhlenbeck process cannot capture the non-linear dynamics of the locking process of a phase-locked loop [90, 94]. Furthermore, we cannot determine the amount of noise which would bring the phase-locked loop out of a lock. It is therefore of great importance to solve the Langevin equations in their general form and thereby describe the non-linear dynamics of the systems under consideration.

In general, there is a great interest in numerical methods to solve stochastic differential equations. Extending numerical integration techniques, which are widely used for ordinary differential equations, would be of great importance. The main approach behind numerical solutions of the Langevin equations is the following. First the stochastic Langevin force needs to be simulated. Usually, a random number generator is used to produce random numbers (e.g. in MATLAB) having a Gaussian distribution. The next step would be to integrate the stochastic differential equations. At the end, an average over a large number of realizations should be taken. It is important to have a large number of realizations of long time intervals. In this section, a simple stochastic integration scheme, a so called forward Euler scheme, is employed for solving the Langevin equations (2.24) [95].

The integration is started and ended at time 0 and T_f , respectively, using the initial condition $\mathbf{X}(0) = \mathbf{X}_0$. The time interval, $[0; T_f]$, is divided into N_t small steps of length dt , that is:

$$dt = \frac{T_f}{N_t}, \quad t_p = p dt, \quad p = 0, 1, 2, \dots, N_t \quad (2.47)$$

The forward Euler scheme, for the i th state variable, x_i , in equation (2.24) at a point $p + 1$ is given by the following expression [95]:

$$x_{i,p+1} = x_{i,p} + D_i^{(1)}(x_p, t_p)dt + \sum_{j=1}^n d_{ij}w_{jp}\sqrt{\frac{1}{2}}\sqrt{dt} \quad (2.48)$$

where n is the order of the system in shown in equation (2.24), x_{i_p} is the solution at a point p and d_{ij} are noise term coefficients, see equation (2.24). w_{jp} are (numerically generated) random numbers. $D_i^{(1)}$ are the first order Kramers-Moyal expansion coefficients [95]:

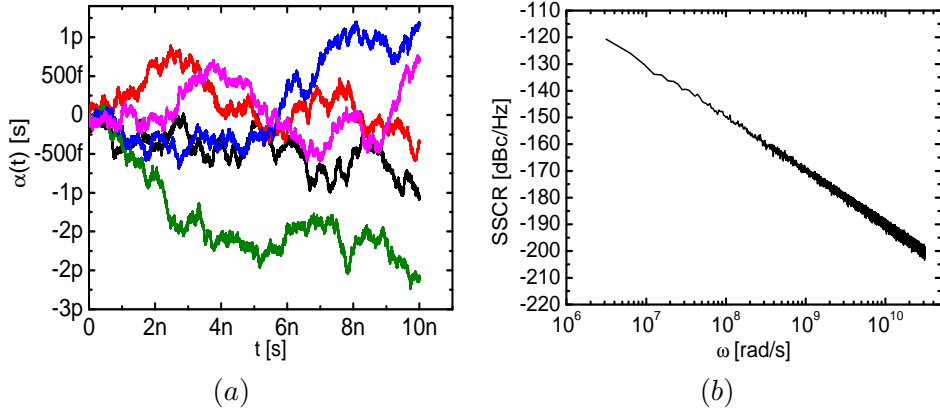


Figure 2.7: (a) Five different realizations of phase noise as a function of time. (b) SSCR of a 10 GHz signal with phase noise obtained by stochastic numerical integration.

$$D_i^{(1)} = F_i(x_1(t), \dots, x_n(t)) + \sum_{k=1}^n \sum_{j=1}^n d_{kj} \frac{\partial d_{ij}}{\partial x_k} \quad (2.49)$$

The random numbers w_{jp} are distributed according to [95]:

$$\langle w_{jp}(t) \rangle = 0 \quad \langle w_{jp} w_{kp'} \rangle = \delta_{jk} \delta_{pp'} \quad (2.50)$$

where δ_{ij} is the Kronecker delta function. The forward Euler scheme, equation (2.48), can then be used to simulate stochastic differential equations. By performing numerical integration, correlation functions can also be determined which would then be used to estimate the spectra. However, a large number of stochastic process realizations need to be taken into account in order to improve the accuracy. Nonetheless, the stochastic Euler scheme is helpful in obtaining some estimate of the solution of the Langevin equations.

In Figure 2.7(a), five different realizations of phase noise, $\alpha(t)$, are shown. We have used the numerical scheme shown in equation (2.48) to solve the phase noise equation (2.7) and the numerical scheme is expressed as:

$$\alpha_{p+1} = \alpha_p + \sqrt{c_{in}} w_p \sqrt{\frac{1}{2} dt} \quad (2.51)$$

where w_p are random numbers. Each of the five realizations in Figure 2.7(a) have different paths due to the stochastic nature of phase noise. However, the ensemble average taken over many realizations should go to zero. In Figure 2.7(b), the SSCR of a 10 GHz signal with a phase noise obtained by using equation (2.51) is plotted. As can be seen from Figure 2.7(b), the SSCR is a straight line with decay of 20 dB/decade which is in accordance with the theoretical and experimental results in Figure 2.3.

2.5 Summary

In this chapter, we have reviewed oscillator fundamentals and phase noise theory. Furthermore, an analytical expression for a power spectral density of a signal with amplitude and phase noise was derived. Using the analytical expression, the effects of amplitude and phase noise on the power spectral density of a signal have been quantified. We have shown that the power spectral density is dominated by phase noise around the carrier frequency. Beyond the roll-off frequency, the power spectral density of a signal is fully dominated by amplitude noise. For very small values of roll-off frequency, the effect of amplitude noise was negligible. Furthermore, we have presented Langevin equations and the corresponding Ornstein-Uhlenbeck process. We have derived analytical expressions for cross-correlation functions for the Ornstein-Uhlenbeck process and this result will be used in Chapter 3 and 4 to analyze the dynamics of clock recovery and regenerator.

Chapter 3

Clock Recovery Based on an Optoelectronic Phase-Locked Loop

In digital communication systems, clock recovery is an integral part of the data-recovery section. The purpose of clock recovery is to provide information about the sampling time to the decision circuit and thereby synchronize the decision process. The output of a clock recovery circuit is a signal with a frequency inversely proportional to the bit-rate or a fraction of the bit-rate of a data signal.

In this chapter, a brief review of important clock recovery parameters is at first given. Thereafter, a review of the most typical clock recovery techniques, used in optical communication systems, is presented. We also explain advantages and disadvantages of the reviewed clock recovery techniques, and we chose to focus on an Optoelectronic Phase-Locked Loop (OPLL) based clock recovery.

The Langevin equations describing the optoelectronic phase-locked loop based clock recovery, with noise sources, are thereafter derived on which we base our novel numerical model. We use correlation functions derived in Chapter 2 to derive the autocorrelation functions of the recovered clock signals and compute novel analytical expressions for timing jitter. Moreover, we investigate how the phase noise, loop gain, loop filter bandwidth, time delay and laser characteristic knee frequency influence the timing jitter of the recovered clock signal. A simple experimental set-up is constructed to verify the numerical model. In addition, experimental results for clock

recovery operating at 320 Gb/s are presented.

3.1 Review of clock recovery parameters

Before we go into the details of different clock recovery schemes, we review important clock recovery parameters which are:

- Lock-in range
- Lock-in time
- Hold-in range
- Jitter (jitter transfer, jitter generation and jitter tolerance)

The lock-in range is the range of frequencies within which an unlocked clock recovery can acquire a lock. The lock-in range should be large enough in order to accommodate any frequency drift of the data signal. Lock-in time is the time needed to obtain a lock. For applications such as packet switching very fast locking time (~ 1 ps) is needed [102].

The hold-in range is the range of frequencies within which (locked) clock recovery can statically maintain phase tracking when frequency drift or other disturbances occur in the data signal. For instance, if the data signal has a relatively long range of zeros and the clock recovery is not losing its lock, then we say that the clock recovery has a large hold-in range.

Clock recovery jitter is another very important parameter. Since the clock recovery provides sampling time for a decision circuit, it is important that the recovered clock does not have a lot of jitter. If the clock recovery has a lot of jitter then this may lead to incorrect bit identification by the decision circuit which would then result in bit-error-rate [103]. The jitter transfer function of a clock recovery circuit describes how jitter from the input signal is transferred to the jitter of the output signal (recovered clock signal). The clock recovery must follow slow frequency drifts (low frequency jitter components) in order to ensure phase tracking of the input data signal. Clock recovery should on the other hand not track fast drifts (high frequency jitter components) since this may lead to erroneous detection of the bits [79]. A typical jitter transfer function of a clock recovery circuit is shown in Figure 3.1 [79].

Figure 3.1 shows that clock recovery should exhibit low-pass characteristics. The bandwidth of the jitter transfer function corresponds to the bandwidth

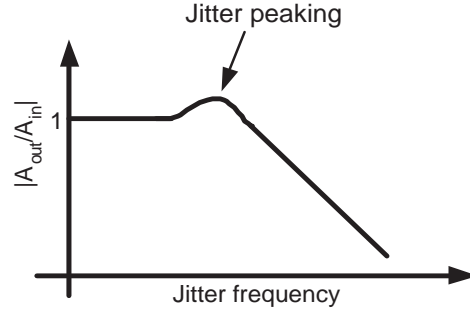


Figure 3.1: Typical jitter transfer function of a clock recovery [79]. The A_{in} is the amplitude of input jitter frequency components and A_{out} is the amplitude of output jitter frequency components.

of the clock recovery circuit. Another characteristic of the jitter transfer function is jitter peaking. The jitter peaking is a ripple in the jitter transfer function of the clock recovery as illustrated in Figure 3.1. The bandwidth of the clock recovery and the allowed jitter peaking depends on the application where clock recovery is involved. For instance for Synchronous Optical NETwork (SONET), the jitter transfer function should have a 120 kHz bandwidth and the jitter peaking should be less than 0.1 dB [79]. Having such a low loop bandwidth is very challenging since it may compromise the stability of the clock recovery as shown in [80].

Jitter generation refers to the jitter produced by a clock recovery circuit itself when the input data has no jitter. Jitter generation is thereby dependent on how much jitter different components in the clock recovery generate. In general, jitter generation is very dependent on the clock recovery topology.

Jitter tolerance specifies how much input jitter a clock recovery must tolerate without increasing the bit-error-rate. This specification is typically described by the mask function and it is shown in Figure 3.2. The mask function also exhibits a low-pass behavior which is a consequence of the jitter transfer function having a low-pass characteristics. For a satisfactory performance of the clock recovery, the jitter tolerance curve should be above the reference curve, see Figure 3.2. Once again specifications on jitter tolerance is dependent on the application where the clock recovery is involved. In general, a clock recovery should be able to tolerate large amounts of low frequency jitter while the jitter tolerance decreases as the

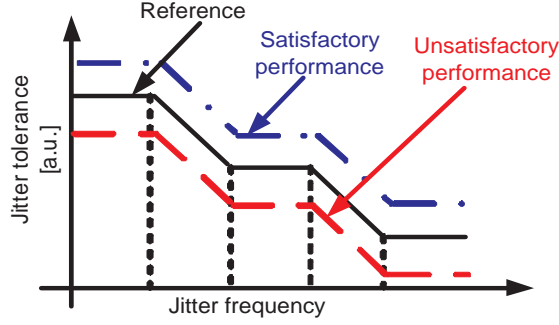


Figure 3.2: Jitter tolerance curve of a clock recovery.

frequency is increased as illustrated in Figure 3.2.

Since the focus of this thesis is on noise in clock recovery systems, only clock recovery parameters such as jitter generation and jitter transfer are treated. The hold-in range, lock-in range and lock-in time are thereby not treated partly because they fall out of the scope of the thesis and partly because they may require nonlinear model of clock recovery. This thesis will not cover nonlinear clock recovery models. However, for the reference on nonlinear clock recovery models see [107, 114, 123].

3.2 Review of clock recovery schemes for optical communication systems

In this section, a brief review of clock recovery schemes in optical communication systems is given. We start by considering ETDM optical communication systems. It should be mentioned that in ETDM systems, prior to clock extraction, an electro-optical conversion is performed using a photodiode in order to convert the optical data signal into electrical domain.

In simple terms, clock recovery is nothing else than a bandpass filter with a high Q -value. The simplest way to perform a clock recovery in ETDM communications systems is to use an electrical bandpass filter and isolate the desired frequency component from the incoming data signal [104]. However, this approach assumes that the data signal has the desired frequency component. The advantage of this clock recovery scheme is apart from its simplicity a very fast lock-in time. Using this type of clock recovery scheme lock-in times in the range of ps can be obtained. Furthermore, if the electri-

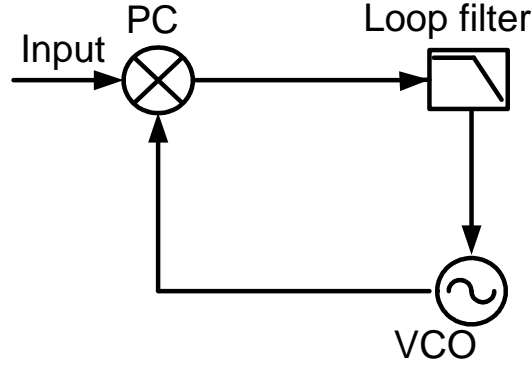


Figure 3.3: Basic phase lock loop. PC: phase comparator, VCO: voltage controlled oscillator.

cal bandpass filter is made from passive components, it is possible to obtain low jitter generation. The disadvantage of this scheme is low lock-in and hold-in range. This is because the pass-band of this type of clock recovery is pre-set and there is thereby very limited flexibility. If the data signal undergoes some frequency drift, the clock recovery may not be able to follow it and will thereby lose the lock. Another issue with this technique is how well the pass-band is designed. Any ripple in the pass-band will have an undesired effect on the jitter transfer function. Moreover, since the bandwidth is finite this may cause significant jitter transfer [105]. We would need a high Q-value in order to have a narrow bandwidth, such that the 3 dB frequency of the jitter transfer function is sufficiently low.

Typically, in high-speed ETDM transmission systems, clock recovery is performed by the use of electrical phase-locked loops, see [80, 106]. Before considering the PLL based clock recovery, the basic principles of a PLL are first described. A diagram of a basic PLL is shown in Figure 3.3.

A PLL synchronizes an output signal, which is locally generated in the loop, in frequency as well as in phase to an input signal. In the synchronized (locked) state the phase error between the input signal and locally generated signal is zero or very small. If the total phase error (which includes relative phase and frequency) begins to build up, the feedback mechanism acts on the local oscillator in such a way that the phase error is brought down to zero again. The loop has thereby re-obtained a lock.

The PLL principally consists of three components:

1. A phase comparator (mixer).

2. A loop filter.
3. A Voltage Controlled Oscillator (VCO).

The Phase Comparator (PC) mixes the incoming data signal with the locally generated clock signal from the VCO. The frequency content of the incoming data signal is thus mixed with the frequency content of the locally generated clock signal. Since the process of mixing corresponds to mathematical multiplication, the output of the PC is a signal which includes the phase error, i.e. the *error signal*, plus the signals at higher frequencies. The output of the PC is then passed through the loop filter, in order to cancel high frequency terms, and is subsequently fed back to the VCO. The output from the VCO is the (desired) recovered clock signal.

The PLL based clock recovery is fully determined by its loop bandwidth which is directly proportional to the closed loop gain of the system [80]. The advantage of the PLL based clock recovery is its large lock-in and hold-in range which can be tuned using the loop bandwidth. This translates into very stable operation of PLL based clock recovery. Furthermore, this type of clock recovery scheme is very flexible since the performance can be easily adjusted using the loop bandwidth.

Decreasing the loop bandwidth will decrease the 3 dB frequency of the jitter transfer function. A clock recovery circuit based on a PLL can thereby provide a clock signal with low timing jitter and high stability. However, decreasing the loop bandwidth may result in peaking of jitter transfer function due to positioning of the poles and zeros of PLL transfer function. Careful attention is required to avoid this effect [80].

A disadvantage of the PLL based clock recovery is that it has relatively high lock-in time [107]. However, a lock-in time can be reduced by optimizing the loop bandwidth and it has been shown that a lock-in time as low as 15 ns is obtainable [107]. An additional issue with the PLL based clock recovery is jitter generation. Loop components such as VCO and electronics will contribute with noise which will be translated into jitter. The loop components of the PLL based clock recovery need therefore to have very low jitter, and as it will be shown later in this chapter, VCO has a significant role on the jitter generation of this type of clock recovery.

Third type of electronic clock recovery scheme is injection locking of a free-running electrical oscillator [108, 109]. The main principle behind this technique is that the internal oscillator can be locked in frequency if the input data signal is applied. This approach is a mixture of band-pass filter

approach and a PLL based clock recovery. If the free-running oscillator has very low-noise, then the jitter performance of this type of clock recovery can be significantly improved compared to the PLL based clock recovery. In this approach there are no loop components (loop filter and amplifier) reducing the overall noise performance. The disadvantage of this type of clock recovery is its limited bandwidth. The limited bandwidth may, as mentioned earlier, translate into low lock-in and hold-in ranges.

As we move away from ETDM towards high-speed OTDM transmission systems clock recovery becomes more challenging. This is partly because of the very high speeds of OTDM transmission systems and partly because the data signal has only frequency components at line rate (aggregate bit-rate).

The recovered clock signal in OTDM systems can be used for either retiming or demultiplexing. For retiming purposes, clock signal at the line rate of OTDM data signal is needed. For demultiplexing purposes, a recovered clock signal at the base rate is needed, so called pre-scaled clock recovery. Recovering a base rate clock from OTDM data signal is very challenging since the OTDM data signal has no base rate frequency component. The clock recovery therefore needs to lock to the line rate of the OTDM data signal. Since the speed of the OTDM data signal can easily exceed 160 Gb/s [110] using electronics to achieve base rate clock extraction may be out of reach for now [15,111] and therefore an alternative way of performing clock recovery is desirable.

Many all-optical or optoelectronic clock recovery techniques have been demonstrated for OTDM transmission systems and they are mostly based on:

- All-optical filtering ([60]- [61] and references therein).
- Injection locking in mode-locked fibre/semiconductor lasers or electro-absorption modulators ([62]- [67] and references therein).
- Optoelectronic phase-locked loops ([68]- [78] and references therein)

The above mentioned techniques for clock recovery in OTDM systems all basically rely on modified versions of clock recovery schemes for ETDM systems. The operational principles are therefore similar to those described earlier for ETDM systems. In addition, the pros and cons described for ETDM clock recovery schemes are thereby also the same for the mentioned OTDM based clock recovery schemes.

For regeneration purposes, all-optical clock recovery techniques based on optical filtering and injection locking are advantageous since they provide a clock at the line rate [60]- [67]. Recently, a mode-locked semiconductor laser has been locked to a 160 Gb/s input OTDM data signal and generated 160 GHz recovered optical clock signal [65]. The recovered 160 GHz clock signal had low jitter and 3 dB bandwidth of the jitter transfer function was relatively. Also, a clock recovery based on quantum dot self-pulsating semiconductor laser was shown to provide very low jitter (190 fs) line rate clock extraction at 40 Gb/s [62]. Furthermore, it was demonstrated that this particular type of clock recovery could satisfy the ITU-T recommendations for jitter transfer function [62].

For demultiplexing operations a recovered clock at the base rate is needed. In most cases, if the clock recovery is performed using all-optical filtering or injection locking techniques, some extra processing is needed in order to obtain base rate clock signal [112]. As a result, PLL based optoelectronic clock recovery is the dominant choice for demultiplexing operations. Using an electrical PLL based clock recovery, the highest data rate from which a base rate clock has been extracted is 100 Gb/s [15]. The main bottleneck in the electrical PLL based clock recovery is the mixer and photodiode. It is very difficult to make high-frequency mixers with high frequency efficiency. An electrical mixer becomes too slow for clock extraction from OTDM systems operating above 100 Gb/s and an optoelectronic PLL must instead be used. The central component in the optoelectronic PLL is the optical mixer [23,68,75]. Non-linear optical effects can be used to achieve mixing in a Semiconductor Optical Amplifier (SOA). Non-linear effects such as Cross Gain Modulation (XGM) [75], Four Wave Mixing (FWM) [23], Cross Phase Modulation (XPM) [77] and filtering-assisted XPM [68] have been used. The advantages of using an optical mixer is that we can avoid implementing a high frequency electrical mixer and a fast photodiode, both of which are difficult to manufacture, and at the same time reach higher bit rates. The output of the optical mixer is passed through the optical bandpass filter in order to extract the modulated optical signal containing the error signal. The rest of the loop remains the same as in the electric PLL where only low-speed and simple electronics are required. The VCO is used to control the optical clock signal generating laser whose pulses are then injected into the optical mixer.

The highest data bit rates from which a pre-scaled base rate clock has been extracted have been achieved with an optoelectronic PLL (400 Gb/s) [23]. In addition, pre-scaled clock extraction of a 10 GHz clock signal

from a 320 Gb/s data signal should also be mentioned [68, 76]. The clock extraction reported reference [68] was achieved using filtering-assisted XPM in optoelectronic PLL. The approaches based on FWM and filtering-assisted XPM therefore seem to be good candidates for applications at very high bit rates (beyond 160 Gb/s). This is because the non-linear processes such as FWM and filtering-assisted XPM can be used to achieve mixing at very high bit rates and thereby generate the error signal to control the optoelectronic PLL. It is therefore believed that the OPLL based clock recovery has a great potential for ultra-high bit rate OTDM systems.

In summary, clock recovery based on an optoelectronic phase-locked loop can offer very high speed operations while maintaining all the benefits of the PLL based clock recovery. We therefore choose to concentrate on the optoelectronic PLL based clock recovery in this thesis.

3.3 Optoelectronic phase-locked loop – a brief descriptions

In this and following sections we will focus on a *balanced* optoelectronic PLL for clock extraction from high-speed data signals, and analyze its stability and noise properties.

The block diagram of the balanced OPLL is shown in Figure 3.4. The phase comparator mixes the optical high-speed data signal (e.g. 160 Gb/s) with a locally generated optical clock signal at the base rate frequency (e.g. 10 GHz) producing an error signal. The mixing process corresponds to a mathematical multiplication with a mixer gain, G . The mixer output is a slowly varying signal proportional to the sine of the phase difference between the data and the optical clock signal. The optical error signal is then converted into the electrical domain.

The balanced (low bandwidth) photodetection provides the subtraction of the DC level from the error signal, which results in a bipolar error signal. This subtraction also helps to stabilize the error signal against fluctuations in the input signal power levels. The signal is then low pass filtered and fed back to the VCO, which controls the frequency of the optical clock generating laser. In an optoelectronic PLL based clock recovery, a considerable time delay is very likely to occur, since the loop length of an OPLL is often much longer than in an electrical PLL. Time delays in an OPLL may typically arise from the presence of an Erbium Doped Fibre Amplifier (EDFA) and/or a pulse compression stage in the loop. An EDFA may

phase noise of the recovered clock signal is filtered by the laser transfer function, with a characteristic knee frequency, f_{ch} , and thus this feature must also be included in the model equations.

In the following sections, a detailed phase noise analysis of an optoelectronic PLL based clock recovery is performed.

3.4 Derivation of Langevin equations for OPLL

In order to study the dynamical behavior of the optoelectronic PLL presented in Figure 3.4 stochastic differential equations, describing the phase error between the input data signal and the local oscillators, need to be derived. The intensity of the input data signal in the presence of phase noise, $\alpha_{in}(t)$, originating from the pulse source at the transmitter, regenerators and in-line amplifiers, is expressed in Fourier series as [100]:

$$P_{in}(t + \alpha_{in}(t)) = s_0 + 2 \sum_{k=1}^{\infty} s_k \sin[2\pi k f_0 t + 2\pi k f_0 \alpha_{in}(t)] \quad (3.1)$$

where k is a positive integer. The constants s_0 and s_k are (real) Fourier coefficients and f_0 is the frequency of the aggregate bit-rate (e.g. 40 GHz, 160 GHz, 320 GHz etc.) [115]. The input data signal phase noise, $\alpha_{in}(t)$ is described by a 1-D Brownian motion stochastic process and c_{in} is a constant determining the amount of phase noise associated with the input signal, as explained in section 2.2, equation (2.7). The intensity of a locally generated optical clock signal is similarly expressed by a Fourier series as [100]:

$$P_{clk}(t + \alpha_{clk}(t)) = c_0 + 2 \sum_{q=1}^{\infty} c_q \cos[2\pi q (f'_0/m) t + 2\pi q (f'_0/m) \alpha_{clk}(t)] \quad (3.2)$$

where q is a positive integer. The constants c_0 and c_q are (real) Fourier coefficients. The repetition frequency of the optical clock signal is f'_0/m , where m is an integer. The frequency f'_0/m (e.g. 10 GHz or 40 GHz) corresponds to the free-running frequency of the optical clock signal generating pulse source and it is chosen so that it is close to the base rate frequency, f_{base} (e.g. 10 GHz, 40 GHz), of the optical input data signal [115]. $\alpha_{clk}(t)$ is the phase noise of the optical clock signal and is modelled as a band-limited Brownian motion stochastic process, as later shown in equation (3.9). The

output of the phase comparator (mixer), see Figure 3.4, is defined to be a product between the optical clock signal in equation (3.2) and the optical data signal in equation (3.1) with a gain G . Phase comparators are frequently modelled as multipliers, partly for analytical convenience and partly because many practical phase comparators are good approximations to multipliers [80]. The optical signal after the mixing contains low frequency components as well as high frequency components, which exceed the aggregate bit-rate ($> f_0$). A slow photodiode with bandwidth $BW \ll f_{base}$ is used to convert the signal from the optical to the electrical domain and is also used to filter out high frequency components. It is assumed that the responsivity of the photodiode is constant in frequency. The fundamental frequency component of the optical data signal f_0 ($k = 1$) interacts with the m 'th frequency component ($q = m$) of the optical clock signal, resulting in the frequency component $\Delta f = (f'_0 - f_0)$. When the aggregate bit rate of the optical data signal is 160 Gb/s and the base rate is 10 Gb/s, we have $m = 16$. The frequency component Δf is smaller than the bandwidth of the photodiode and can therefore be detected and forms the fundamental frequency component. Furthermore, following Figure 3.4, it can be observed that part of the optical data signal is first attenuated, in order to match the signal powers, and then passed through the second photodiode which is identical to the first one. Since the photodiode is slow, only the DC frequency component of the optical data signal passes through. The balanced photodetection (BW in the MHz range) thereby provides a subtraction of the DC level from the error signal. After balanced photodetection the error signal, $e(t)$, is expressed as:

$$\begin{aligned} e(t) &= RGs'_0c'_0 + 2RG \sum_{i=1}^{\infty} s_i c_{i \cdot m} \sin[i\Phi_e(t)] \\ \Phi_e(t) &= \Delta\omega t + 2\pi f'_0 \alpha_{clk}(t) - 2\pi f_0 \alpha_{in}(t) \end{aligned} \tag{3.3}$$

where i is a positive integer and R is the responsivity of the photodiode. $s'_0c'_0$ is the remaining DC level in the error signal after the subtraction. Even though the balanced photodetection provides a subtraction of the DC levels, cancellation may not be complete due to imperfections in the electronics.

In order to perform phase noise analysis of the system, the locally generated optical clock signal must be synchronized to the input data signal,

i.e. $f_0 = f'_0$. We choose to operate with a normalized total phase difference, $\phi_e(t) \equiv \Phi_e(t)/(2\pi f_0)$. The advantage of using the normalized form of $\Phi_e(t)$ is that the phase noise analysis will not be affected by the frequency difference between the input signal and the locally generated clock signal. The normalized phase difference, $\phi_e(t)$, is given by:

$$\phi_e(t) = \Delta\omega_n t + \alpha_{clk}(t) - \alpha_{in}(t) \quad (3.4)$$

where $\Delta\omega_n = \Delta\omega/(2\pi f_0)$. The normalized error signal $e_n(t) \equiv RGs'_0c'_0 + 2RG\sum_{i=1}^{\infty} s_i c_{i \cdot m} \sin[i\phi_e(t)]$ is now applied to the loop filter, a Proportional Integrator (PI) filter. In the time domain the output of the PI filter $\psi_e(t)$ is defined as [116]:

$$\tau_1 \frac{d\psi_e}{dt} = A \left[e_n(t) - \tau_2 \frac{de_n}{dt} \right] \quad (3.5)$$

where A is the gain of the electrical amplifier in the loop. τ_1 represents the integration time of the filter (inversely proportional to the PI filter bandwidth) and τ_2 determines the DC gain of the PI filter. The reason that we concentrate on a PI loop filter, is because it is more tolerant to long loop lengths compared to the Low Pass (LP) and Active Lag (AL) filters [114]. The output signal of the VCO is written as:

$$v(t) = V_0 \cos[2\pi f_0'' t + 2\pi f_0'' \alpha_{vco}(t)] \quad (3.6)$$

where f_0'' is the free-running frequency of the VCO, also chosen close to the base rate of the optical data signal, and V_0 is the amplitude of the VCO signal. $\alpha_{vco}(t)$ is the phase noise associated with the VCO. It has been shown by Mehrotra [94] that the phase noise of the oscillator in the presence of the applied signal is governed by the following stochastic differential equation:

$$\frac{d\alpha_{vco}}{dt} = \nu^T(t + \alpha_{vco}(t))\Gamma_p(t) + \nu_{ctrl}(t + \alpha_{vco}(t))\psi_e(t) \quad (3.7)$$

where $\Gamma_p(t)$ is a vector of p uncorrelated white noise sources and $\nu(\cdot)$ is a periodic function which depends on the noise source intensities and the response of the linearized oscillator circuit [94]. $\nu_{ctrl}(\cdot)$ is the component of $\nu(\cdot)$ which corresponds to a unit noise source present at the control node of the VCO [94]. In order to perform noise analysis of the PLL we must assume that the loop is locked and we are therefore only interested in the asymptotic behavior of the VCO phase noise. Asymptotically, equation (3.7) reduces to [94]:

$$\frac{d\alpha_{vco}}{dt} = K_{vco}\psi_e(t) + \sqrt{c_{vco}} \Gamma_{vco}(t) \quad (3.8)$$

where c_{vco} is a constant determining the amount of phase noise associated with the free-running VCO and K_{vco} is the average gain of the VCO. $\Gamma_{vco}(t)$ is a white noise source. From the diagram of the OPLL, Figure 3.4, we observe that the signal from the VCO is directly modulating the optical pulse source (laser). Under the assumption that the VCO and the laser are synchronized, the phase noise of the VCO, $\alpha_{vco}(t)$, will be filtered by the laser transfer function, with a characteristic knee frequency of f_{ch} [117, 118]. The characteristic knee frequency, f_{ch} , denotes the bandwidth around the laser's center frequency within which phase noise is transferred from the VCO to the laser [119]. The phase noise, $\alpha_{clk}(t)$, of the laser is thus expressed as:

$$\frac{d\alpha_{clk}}{dt} = -\frac{\gamma_e(t)}{\tau_{ch}} + \sqrt{c_{clk}} \Gamma_{clk}(t) \quad (3.9)$$

where $\tau_{ch} = 1/(2\pi f_{ch})$ and c_{clk} is a constant describing the magnitude of the phase noise of the free-running laser described by the white noise source $\Gamma_{clk}(t)$. $\gamma_e(t)$ is the phase noise difference between the VCO and the optical pulse source, i.e. $\gamma_e(t) = \alpha_{clk}(t) - \alpha_{vco}(t)$. The stochastic differential equation describing the phase noise difference between the VCO and the optical pulse source is then obtained by differentiating $\gamma_e(t)$ and using equation (3.8) and (3.9):

$$\frac{d\gamma_e}{dt} = -\frac{\gamma_e(t)}{\tau_{ch}} - K_{vco}\psi_e(t) + \sqrt{c_{clk}} \Gamma_{clk}(t) - \sqrt{c_{vco}} \Gamma_{vco}(t) \quad (3.10)$$

The effect of a time delay, τ_d , see Figure 3.4, is taken into account by incorporating a delay in $\alpha_{clk}(t)$ and thereby equation (3.4). Therefore, $\phi_e(t)$ in equation (3.4) changes to $\phi_e(t) = \Delta\omega_n t + \alpha_{clk}(t - \tau_d) - \alpha_{in}(t)$. By using equation (3.10), the stochastic differential equation describing the total phase error in the loop is obtained by differentiating $\phi_e(t)$ and is expressed as:

$$\frac{d\phi_e(t)}{dt} = \Delta\omega_n - \frac{\gamma_e(t - \tau_d)}{\tau_{ch}} + \sqrt{c_{clk}} \Gamma_{clk}(t - \tau_d) - \sqrt{c_{in}} \Gamma_{in}(t) \quad (3.11)$$

where $d\alpha_{in}/dt = \sqrt{c_{in}} \Gamma_{in}(t)$ and $\Gamma_{in}(t)$ is a white noise source associated with the input data signal. Inserting equation (3.11) in (3.5), the output from the loop filter becomes:

$$\begin{aligned}
\frac{d\psi_e}{dt} = & DC + \frac{1}{\tau_1} \sum_{i=1}^{\infty} \zeta_i \sin[i\phi_e(t)] + \frac{\tau_2}{\tau_1} \left(\sum_{i=1}^{\infty} \zeta_i i \cos[i\phi_e(t)] \right) \\
& \times \left(-\Delta\omega_n + \frac{\gamma_e(t - \tau_d)}{\tau_{ch}} - \sqrt{c_{clk}} \Gamma_{clk}(t - \tau_d) + \sqrt{c_{in}} \Gamma_{in}(t) \right)
\end{aligned} \tag{3.12}$$

The dynamical behavior of the optoelectronic PLL in the presence of noise is governed by equations (3.10), (3.11) and (3.12) where $DC = \frac{ARGs'_0c'_0}{\tau_1}$ and $\zeta_i = 2ARGs_i c_{i \cdot m}$. ζ_i describes the overall gain in the loop. s_i is the i th Fourier coefficient of the input data signal in equation (3.1) and $c_{i \cdot m}$ is the $(i \cdot m)$ th Fourier coefficient of the optical clock signal in equation (3.2).

3.5 Linearization of Langevin equations describing the OPLL

3.5.1 Zero time delay

In this subsection it is assumed the time delay is set to zero, i.e. $\tau_d = 0$. We want to linearize the Langevin equations, (3.10) – (3.12), by performing a small-signal expansion near a stationary point; $(\bar{x}, \bar{y}, \bar{z}) \equiv (\bar{\phi}_e, \bar{\gamma}_e, \bar{\psi}_e) = 0$. For convenience we introduce:

$$\phi_e(t) = \bar{x} + \Delta x(t) \tag{3.13}$$

$$\gamma_e(t) = \bar{y} + \Delta y(t) \tag{3.14}$$

$$\psi_e(t) = \bar{z} + \Delta z(t) \tag{3.15}$$

where $\Delta x(\cdot)$, $\Delta y(\cdot)$ and $\Delta z(\cdot)$ are small disturbances around the stationary points $(\bar{\phi}_e, \bar{\gamma}_e, \bar{\psi}_e)$. In order to get a dynamic response of the loop, we need to derive the differential equations describing the disturbances Δx , Δy and Δz . The expressions in equations (3.13) – (3.15) are inserted in (3.10) – (3.12). After linearization, the following is obtained¹:

¹In practise $s_1 c_m \gg s_i c_{i \cdot m}$ for $i \in [2; \infty]$, and only $i = 1$ is considered. This is observed by considering the pulse width requirements for the high-speed OTDM data signal [45].

$$\begin{aligned}
\begin{bmatrix} \dot{\Delta x} \\ \dot{\Delta y} \\ \dot{\Delta z} \end{bmatrix} &= \begin{bmatrix} 0 & -\frac{1}{\tau_{ch}} & 0 \\ 0 & -\frac{1}{\tau_{ch}} & -K_{vco} \\ \frac{\zeta_1}{\tau_1} & \frac{\tau_2 \zeta_1}{\tau_1 \tau_{ch}} & 0 \end{bmatrix} \begin{bmatrix} \Delta x \\ \Delta y \\ \Delta z \end{bmatrix} \\
&+ \begin{bmatrix} 0 & \sqrt{c_{clk}} & -\sqrt{c_{in}} \\ -\sqrt{c_{vco}} & \sqrt{c_{clk}} & 0 \\ 0 & -\frac{\tau_2 \zeta_1}{\tau_1} \sqrt{c_{clk}} & \frac{\tau_2 \zeta_1}{\tau_1} \sqrt{c_{in}} \end{bmatrix} \begin{bmatrix} \Gamma_{vco}(t) \\ \Gamma_{clk}(t) \\ \Gamma_{in}(t) \end{bmatrix}
\end{aligned} \tag{3.16}$$

To obtain the characteristic equation describing the eigen (natural) solution of the system (without the driving noise terms) we assume solutions of the form: $\Delta x = \Delta x_0 e^{\lambda t}$, $\Delta y = \Delta y_0 e^{\lambda t}$ and $\Delta z = \Delta z_0 e^{\lambda t}$. Δx_0 , Δy_0 and Δz_0 are amplitudes of the disturbances and λ are eigenvalues. Inserting Δx , Δy and Δz in (3.16), the expression for eigenvalues, λ , can be obtained. The eigenvalues are then governed by the following characteristic equation:

$$\lambda^3 + \frac{1}{\tau_{ch}} \lambda^2 + \frac{K_{vco} \zeta_1 \tau_2}{\tau_1 \tau_{ch}} \lambda - \frac{\zeta_1 K_{vco}}{\tau_1 \tau_{ch}} = 0 \tag{3.17}$$

3.5.2 Non-zero time delay

In the presence of time delay, the equations describing the dynamics of the OPLL include a delay term as shown in equations (3.10) – (3.12). The delayed differential equations are harder to deal with and we would therefore like to transform the delayed differential equations into ordinary differential equations. The method which we use is described in [120] and is valid for relatively small values of the ratio τ_d/τ_{ch} , i.e. $\tau_d/\tau_{ch} < 500$ ns [121]. Without the driving terms caused by noise, the system of equations (3.10) – (3.12) can be written in form:

$$\begin{aligned}
\dot{\mathbf{w}} &= f(\mathbf{w}) \\
\mathbf{w} &= [w_1(t), w_2(t), w_3(t), w_4(t)]^T \equiv [\phi_e(t), \gamma_e(t), \gamma_e(t - \tau_d), \psi_e(t)]^T
\end{aligned} \tag{3.18}$$

For each of the variables in $\mathbf{w} = [w_1(t), w_2(t), w_3(t), w_4(t)]^T$, a first order differential equation needs to be derived in order to get the total response of the loop. The variable $w_3(t)$ is a delayed version of $w_2(t)$, i.e. $w_3(t) = w_2(t - \tau_d)$. Using the first order Pade(1,1) approximation and following the

procedure described in [120] and [121], a differential equation describing $w_3(t)$ is obtained:

$$\frac{dw_3}{dt} = \frac{2}{\tau_d} \left[w_2(t) - w_3(t) \right] - \frac{dw_2}{dt} \quad (3.19)$$

Using equation (3.19), the differential equations in (3.10) – (3.12) are rewritten as (without the noise driving terms):

$$\frac{dw_1}{dt} = \Delta\omega_n - \frac{w_3(t)}{\tau_{ch}} \quad (3.20)$$

$$\frac{dw_2}{dt} = -\frac{w_2(t)}{\tau_{ch}} - K_{vco}w_4(t) \quad (3.21)$$

$$\frac{dw_3}{dt} = \frac{2}{\tau_d} \left[w_2(t) - w_3(t) \right] + \frac{w_2(t)}{\tau_{ch}} + K_{vco}w_4(t) \quad (3.22)$$

$$\begin{aligned} \frac{dw_4}{dt} &= DC \\ &+ \frac{1}{\tau_1} \sum_{i=1}^{\infty} \zeta_i \sin[iw_1(t)] + \frac{\tau_2}{\tau_1} \left(\sum_{i=1}^{\infty} \zeta_i i \cos[iw_1(t)] \right) \cdot \left(-\Delta\omega_n + \frac{w_3(t)}{\tau_{ch}} \right) \end{aligned} \quad (3.23)$$

Next, we want to linearize the Langevin equations in (3.20) – (3.23), by performing a small signal expansion near a stationary point; $(\bar{x}, \bar{y}, \bar{q}, \bar{z}) = (\bar{w}_1, \bar{w}_2, \bar{w}_3, \bar{w}_4) = 0$. Once again, as in (3.13)-(3.15), we introduce: $w_1(t) = \bar{x} + \Delta x(t)$, $w_2(t) = \bar{y} + \Delta y(t)$, $w_3(t) = \bar{q} + \Delta q(t)$, $w_4(t) = \bar{z} + \Delta z(t)$. After linearization and including the noise terms, the following is obtained:

$$\begin{aligned} \begin{bmatrix} \dot{\Delta x} \\ \dot{\Delta y} \\ \dot{\Delta q} \\ \dot{\Delta z} \end{bmatrix} &= \begin{bmatrix} 0 & 0 & -\frac{1}{\tau_{ch}} & 0 \\ 0 & -\frac{1}{\tau_{ch}} & 0 & -K_{vco} \\ 0 & \frac{2}{\tau_d} + \frac{1}{\tau_{ch}} & -\frac{2}{\tau_d} & K_{vco} \\ \frac{\zeta_1}{\tau_1} & 0 & \frac{\tau_2 \zeta_1}{\tau_1 \tau_{ch}} & 0 \end{bmatrix} \begin{bmatrix} \Delta x \\ \Delta y \\ \Delta q \\ \Delta z \end{bmatrix} \\ &+ \begin{bmatrix} 0 & 0 & \sqrt{c_{clk}} & -\sqrt{c_{in}} \\ -\sqrt{c_{vco}} & \sqrt{c_{clk}} & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & 0 & -\frac{\tau_2 \zeta_1}{\tau_1} \sqrt{c_{clk}} & \frac{\tau_2 \zeta_1}{\tau_1} \sqrt{c_{in}} \end{bmatrix} \begin{bmatrix} \Gamma_{vco}(t) \\ \Gamma_{clk}(t) \\ \Gamma_{clk}(t - \tau_d) \\ \Gamma_{in}(t) \end{bmatrix} \end{aligned} \quad (3.24)$$

Following the same principle as in section 3.5.1, the eigenvalues are then governed by the following characteristic equation:

$$\begin{aligned} \lambda^4 &+ \frac{2\tau_1\tau_{ch} + \tau_1\tau_d}{\tau_d\tau_1\tau_{ch}}\lambda^3 + \frac{2\tau_1 - K_{vco}\zeta_1\tau_2\tau_d}{\tau_d\tau_1\tau_{ch}}\lambda^2 \\ &+ \frac{2K_{vco}\zeta_1\tau_2 - \zeta_1K_{vco}\tau_d}{\tau_d\tau_1\tau_{ch}}\lambda - \frac{2\zeta_1K_{vco}}{\tau_d\tau_{ch}\tau_1} = 0 \end{aligned} \quad (3.25)$$

Characteristic equations (3.17) and (3.25), expressing the eigenvalues, are very useful since they can be used to perform the stability analysis of the loop with and without the time delay, respectively [114].

3.6 Computation of correlation functions for OPLL based clock recovery

In section 3.4, we have derived a set of stochastic non-linear differential (Langevin) equations, describing the phase error in the loop, and linearized them in section 3.5. Moreover, algebraic equations expressing the eigenvalues for the loop with and without the time delay have been derived. In this section, using the results derived in section 3.5 and 2.4.1, we determine the correlation functions, which will be subsequently used to compute the power density spectrum of the recovered clock signals and the resulting timing jitter.

In short notation, the system of equations in (3.16) and (3.24) can be expressed as an Ornstein-Uhlenbeck process, (see equation (2.28)), namely:

$$\frac{d\mathbf{X}}{dt} = -\mathbf{A}\mathbf{X} + \mathbf{D}\mathbf{\Gamma}(t) \quad (3.26)$$

where \mathbf{A} and \mathbf{D} are constant matrices, $\mathbf{\Gamma}$ denotes a vector of white noise sources with vanishing cross-correlation functions and \mathbf{X} is a vector of state variables. \mathbf{A} , \mathbf{D} , \mathbf{D} and \mathbf{X} are defined in equation (3.16) or equation (3.24) depending on whether the loop with or without the time delay is considered.

Next, we want to determine the correlation function $\langle \alpha_{in}(\tau)\mathbf{x}(0) \rangle$. This is because, the correlations function $\langle \alpha_{in}(\tau)\mathbf{x}(0) \rangle$ is later used to determine the autocorrelation function of the recovered clock signal as shown in section 3.7. In order to compute the correlation function, $\langle \alpha_{in}(\tau)\mathbf{x}(0) \rangle$, we use the same procedure as described in section 2.4.1. According to section 2.4.1, we first need to begin by determining the cross-spectral densities

$\langle \alpha_{in}(\omega) \mathbf{X}^*(\omega) \rangle$. Since $d\alpha_{in}(t)/dt = \sqrt{c_{in}} \Gamma_{in}(t)$, the solution in frequency domain using an inverse Fourier transformation technique is:

$$\alpha_{in}(\omega) = \frac{\sqrt{c_{in}} \Gamma_{in}(\omega)}{j\omega} \quad (3.27)$$

Cross-spectral density of the input data signal phase noise $\langle \alpha_{in}(\omega) \alpha_{in}^*(\omega) \rangle$ is expressed as:

$$\langle \alpha_{in}(\omega) \alpha_{in}^*(\omega) \rangle = \frac{2\pi c_{in}}{\omega^2} \quad (3.28)$$

where we set $\delta(\omega - \omega') = 1$ for $\omega' = \omega$ [95]. The correlation function of the input data signal phase noise is obtained by taking Fourier transformation of equation (3.28):

$$\langle \alpha_{in}(\tau) \alpha_{in}(0) \rangle = -\frac{1}{2} c_{in} |\tau| \quad (3.29)$$

Next, cross-spectral densities $\langle \alpha_{in}(\omega) \mathbf{X}^*(\omega) \rangle$ are determined using equations (3.27), (2.32) and (2.33) as follows:

$$\begin{aligned} \langle \alpha_{in}(\omega) \mathbf{X}^*(\omega) \rangle &= \left\langle \frac{\sqrt{c_{in}} \Gamma_{in}(\omega)}{j\omega} (\mathbf{A} - j\omega \mathbf{I})^{-1} \mathbf{D} \Gamma^*(\omega) \right\rangle \\ &= \frac{2\pi \sqrt{c_{in}}}{j\omega} (\mathbf{A} - j\omega \mathbf{I})^{-1} \mathbf{D} \hat{\mathbf{e}}_n \end{aligned} \quad (3.30)$$

where $\hat{\mathbf{e}}_n^T = [0 \ \cdots \ 1]$ is a unit vector defined in section 2.4.1 and n is an integer representing the order of the system in equation (3.26). By combining equations (3.30) and (2.39), the cross-spectral densities, $\langle \alpha_{in}(\omega) x_l^*(\omega) \rangle$, where $x_l^*(\omega)$ are the elements of $\mathbf{X}^*(\omega)$, are expressed as:

$$\begin{aligned} \langle \alpha_{in}(\omega) x_l^*(\omega) \rangle &= \frac{2\pi \sqrt{c_{in}}}{j\omega} \hat{\mathbf{e}}_l^T (\mathbf{A} - j\omega \mathbf{I})^{-1} \mathbf{D} \hat{\mathbf{e}}_n \\ &= \sum_{i=1}^n \frac{2\pi \sqrt{c_{in}} b_{li} \mathbf{V}_i^T (\mathbf{A} - j\omega \mathbf{I})^{-1} \mathbf{D} \hat{\mathbf{e}}_n}{j\omega} \\ &= \sum_{i=1}^n \frac{2\pi \sqrt{c_{in}} b_{li} \mathbf{V}_i^T \mathbf{D} \hat{\mathbf{e}}_n}{\omega^2 + j\omega \lambda_i} \end{aligned} \quad (3.31)$$

where b_{li} (l, i are integers) are vector elements determined by equation (2.46). Similarly, it can be shown that $\langle x_l(\omega) \alpha_{in}^*(\omega) \rangle$ is given by:

$$\langle x_l(\omega)\alpha_{in}^*(\omega) \rangle = \sum_{i=1}^n \frac{2\pi\sqrt{c_{in}}b_{l_i}\mathbf{V}_i^T\mathbf{D}\hat{\mathbf{e}}_n}{\omega^2 - j\omega\lambda_i} \quad (3.32)$$

As will be shown in section 3.7, the correlation function $\langle \alpha_{in}x_l^*(\omega) \rangle$ always contains the pair $\langle x_l(\omega)\alpha_{in}^*(\omega) \rangle$ when computing the autocorrelation function of the recovered clock signal. It is therefore convenient to determine the Fourier transformation of $\langle \alpha_{in}(\omega)x_l^*(\omega) \rangle + \langle x_l(\omega)\alpha_{in}^*(\omega) \rangle$. This is found to be:

$$\begin{aligned} \langle \alpha_{in}(\tau)x_l(0) \rangle + \langle x_l(\tau)\alpha_{in}(0) \rangle &= \sum_{i=1}^n \frac{\sqrt{c_{in}}b_{l_i}\mathbf{V}_i^T\mathbf{D}\hat{\mathbf{e}}_n}{\lambda_i} e^{-\lambda_i|\tau|} \\ &= \sum_{i=1}^n \mu_i^l e^{-\lambda_i|\tau|} \end{aligned} \quad (3.33)$$

3.7 Computation of the autocorrelation functions of the recovered clock signals

In this section, the autocorrelation functions of the recovered electrical and optical clock signals are computed using the correlation functions derived in section 2.4.1 and 3.6. The intensity of a locally generated optical clock signal in the presence of phase noise, equation (3.2), can be expressed as:

$$P_{clk}(t) = \sum_{q=-\infty}^{\infty} c_q e^{jq\omega'_0 t} e^{jq\omega'_0 \alpha_{clk}(t)} = \sum_{q=-\infty}^{\infty} c_q e^{jq\omega'_0 (t + \alpha_{clk}(t))} \quad (3.34)$$

where $\omega'_0 = 2\pi(f'_0/m)$. The autocorrelation function, $\langle P_{clk}(t)P_{clk}^*(t + \tau) \rangle$, of the optical clock signal is expressed as:

$$\langle P_{clk}(t)P_{clk}^*(t + \tau) \rangle = \sum_{q=-\infty}^{q=\infty} \sum_{k=-\infty}^{\infty} c_q c_k^* e^{j(q-k)\omega'_0 t} e^{-jk\omega'_0 \tau} \langle e^{j\omega'_0 \varphi_{qk}(t, \tau)} \rangle \quad (3.35)$$

where $\varphi_{qk}(t, \tau) = q\alpha_{clk}(t) - k\alpha_{clk}(t + \tau)$. The term $\langle e^{j\omega'_0 \varphi_{qk}(t, \tau)} \rangle$ corresponds to the characteristic function of the stochastic process $q\alpha_{clk}(t) - k\alpha_{clk}(t + \tau)$. It has been shown that the characteristic function of the zero mean Gaussian distributed random variable Γ , $N(0, \sigma_\Gamma^2)$, equals: $\langle e^{j\omega\Gamma} \rangle = e^{-\frac{1}{2}\omega^2\sigma_\Gamma^2}$ [93]. As

stated earlier, the phase noise of the optical clock signal is asymptotically a zero mean wide-sense stationary Gaussian process and therefore the following is valid: $\langle e^{j\omega'_0 \varphi_{q,k}(t,\tau)} \rangle = e^{-\frac{1}{2}\omega_0'^2 \sigma_\varphi^2(t,\tau)}$. The variance $\sigma_\varphi^2(t, \tau)$ is expressed as:

$$\begin{aligned}\sigma_\varphi^2(t, \tau) &= \langle [q\alpha_{clk}(t) - k\alpha_{clk}(t + \tau)]^2 \rangle - \langle [q\alpha_{clk}(t) - k\alpha_{clk}(t + \tau)] \rangle^2 \\ &= q^2 \langle \alpha_{clk}^2(t) \rangle - 2qk \langle \alpha_{clk}(t) \alpha_{clk}(t + \tau) \rangle + k^2 \langle \alpha_{clk}^2(t + \tau) \rangle\end{aligned}\quad (3.36)$$

Taking the stationarity of the phase noise into consideration:

$$\langle \alpha_{clk}(\tau + t) \alpha_{clk}(t) \rangle = \langle \alpha_{clk}(\tau) \alpha_{clk}(0) \rangle \quad (3.37)$$

$$\langle \alpha_{clk}^2(t) \rangle = \langle \alpha_{clk}^2(0) \rangle = \langle \alpha_{clk}(0) \alpha_{clk}(0) \rangle \quad (3.38)$$

$$\langle \alpha_{clk}^2(t + \tau) \rangle = \langle \alpha_{clk}^2(\tau) \rangle = \langle \alpha_{clk}^2(0) \rangle \quad (3.39)$$

the variance, $\sigma_\varphi^2(t, \tau)$, in equation (3.36) is thereby expressed as:

$$\sigma_\varphi^2(t, \tau) = (q^2 + k^2) \langle \alpha_{clk}(0) \alpha_{clk}(0) \rangle - 2qk \langle \alpha_{clk}(\tau) \alpha_{clk}(0) \rangle \quad (3.40)$$

In order to determine the autocorrelation function of the optical clock signal the autocorrelation function of the phase noise, $\langle \alpha_{clk}(\tau) \alpha_{clk}(0) \rangle$, needs to be calculated. By using the Wiener-Khintchine theorem [95], the autocorrelation function $\langle \alpha_{clk}(\tau) \alpha_{clk}(0) \rangle$ can be expressed in terms of the spectral density:

$$\langle \alpha_{clk}(\tau) \alpha_{clk}(0) \rangle = \frac{1}{2\pi} \int_{-\infty}^{\infty} \frac{\langle \alpha_{clk}(\omega) \alpha_{clk}^*(\omega) \rangle}{2\pi} e^{j\omega\tau} d\omega \quad (3.41)$$

Using equation (3.4), we find that $\alpha_{clk}(\omega) = \phi_e(\omega) + \alpha_{in}(\omega)$ and it can therefore be shown that the spectral density of the optical clock signal phase noise $\langle \alpha_{clk}(\omega) \alpha_{clk}^*(\omega) \rangle$ becomes:

$$\begin{aligned}\langle \alpha_{clk}(\omega) \alpha_{clk}^*(\omega) \rangle &= \langle \alpha_{in}(\omega) \alpha_{in}^*(\omega) \rangle + \langle \alpha_{in}(\omega) \phi_e^*(\omega) \rangle \\ &+ \langle \phi_e(\omega) \alpha_{in}^*(\omega) \rangle + \langle \phi_e(\omega) \phi_e^*(\omega) \rangle\end{aligned}\quad (3.42)$$

In order to compute the autocorrelation function of the optical clock signal phase noise, $\langle \alpha_{clk}(\tau) \rangle$

$\alpha_{clk}(0)\rangle$, equation (3.42) is inserted into equation (3.41) and we furthermore use equations (3.27), (2.44) and (3.33). After inverse Fourier transformation one gets:

$$\begin{aligned}
\langle \alpha_{clk}(\tau) \alpha_{clk}(0) \rangle &= \left(\langle \alpha_{in}(\tau) \alpha_{in}(0) \rangle + \langle \alpha_{in}(\tau) \phi_e(0) \rangle \right. \\
&\quad \left. + \langle \phi_e(\tau) \alpha_{in}(0) \rangle + \langle \phi_e(\tau) \phi_e(0) \rangle \right) \\
&= -\frac{1}{2} c_{in} |\tau| + \sum_{i=1}^n (\mu_i^1 + \nu_i^{11}) e^{-\lambda_i |\tau|} \quad (3.43)
\end{aligned}$$

The variance of the phase noise, $\sigma_\varphi^2(t, \tau)$, is obtained by inserting equation (3.43) into equation (3.40):

$$\sigma_\varphi^2(t, \tau) = qk c_{in} |\tau| + (q^2 + k^2) \sum_{i=1}^n (\mu_i^1 + \nu_i^{11}) - 2qk \sum_{i=1}^n (\mu_i^1 + \nu_i^{11}) e^{-\lambda_i |\tau|} \quad (3.44)$$

Inserting the expression for the variance, equation (3.44), into equation (3.35) and observing that only terms corresponding to $k = q$ are non-zero for $t \rightarrow \infty$, the autocorrelation function of the optical clock signal $\langle P_{clk}(t) P_{clk}^*(t + \tau) \rangle$ can be expressed as:

$$\begin{aligned}
\langle P_{clk}(t) P_{clk}^*(t + \tau) \rangle &= \sum_{q=-\infty}^{\infty} |c_q|^2 e^{-jq\omega'_0 \tau} \cdot \exp \left(-\frac{1}{2} q^2 \omega_0'^2 \left[c_{in} |\tau| \right. \right. \\
&\quad \left. \left. + 2 \sum_{i=1}^n (\mu_i^1 + \nu_i^{11}) (1 - e^{-\lambda_i |\tau|}) \right] \right) \quad (3.45)
\end{aligned}$$

Next, using equations (3.27), (2.44), (2.45) and (3.33), we determine the autocorrelation function of the VCO signal; $\langle v(t) v^*(t + \tau) \rangle$ enabling a comparison between the extracted optical and electrical clock signal. Following the procedure above, and expressing; $\alpha_{vco}(t) = \alpha_{in}(t) + \phi_e(t) - \gamma_e(t)$, the autocorrelation function of the VCO signal becomes:

$$\begin{aligned}
\langle v(t)v^*(t+\tau) \rangle &= \sum_{p=-1}^1 \frac{V_0^2}{4} e^{-jp\omega'_0\tau} \exp \left(-\frac{1}{2}p^2\omega_0'^2 \left[c_{in}|\tau| \right. \right. \\
&\quad \left. \left. + 2 \sum_{i=1}^n (\mu_i^1 - \mu_i^2 + \nu_i^{11} + \nu_i^{22} - 2\nu_i^{12}) (1 - e^{-\lambda_i|\tau|}) \right] \right)
\end{aligned} \tag{3.46}$$

The power spectral density of the extracted optical and electrical clock signal can now be obtained by taking the Fourier transformation of equations (3.45) and (3.46) [94]:

$$\begin{aligned}
S_{P_{clk}, P_{clk}}(\omega) &= \sum_{q=-\infty}^{\infty} \sum_{k_1, \dots, k_n=0}^{\infty} 2|c_q|^2 \exp \left[-q^2\omega_0^2 \sum_{i=1}^n (\mu_i^1 + \nu_i^{11}) \right] \\
&\times \frac{\prod_{i=1}^n [q^2\omega_0^2 (\mu_i^1 + \nu_i^{11})]^{k_i} \left[\left(\frac{1}{2}\omega_0^2 q^2 c_{in} + \sum_{i=1}^n k_i \lambda_i \right) \right]}{\left[\prod_{i=1}^n k_i! \right] \left[\left(\frac{1}{2}(\omega_0^2 q^2 c_{in} + \sum_{i=1}^n k_i \lambda_i) \right)^2 + (q\omega_0 + \omega)^2 \right]}
\end{aligned} \tag{3.47}$$

and

$$\begin{aligned}
S_{v,v}(\omega) &= \sum_{p=-1}^1 \sum_{k_1, \dots, k_n=0}^{\infty} \frac{V_0^2}{2} \exp \left[-p^2\omega_0'^2 \sum_{i=1}^n (\mu_i^1 - \mu_i^2 + \nu_i^{11} + \nu_i^{22} - 2\nu_i^{12}) \right] \\
&\times \frac{\prod_{i=1}^n [p^2\omega_0'^2 (\mu_i^1 - \mu_i^2 + \nu_i^{11} + \nu_i^{22} - 2\nu_i^{12})]^{k_i}}{\left[\prod_{i=1}^n k_i! \right] \left[\left(\frac{1}{2}(\omega_0'^2 p^2 c_{in} + \sum_{i=1}^n k_i \lambda_i) \right)^2 + (p\omega_0' + \omega)^2 \right]} \\
&\cdot \left[\left(\frac{1}{2}\omega_0'^2 p^2 c_{in} + \sum_{i=1}^n k_i \lambda_i \right) \right]
\end{aligned} \tag{3.48}$$

The SSCR of the recovered optical and electrical clock signal can be computed using equation (2.12) in combination with equation (3.47) or equation (3.48), respectively. The corresponding timing jitter of the recovered optical and electrical clock signal is obtained by performing integration of

equations (3.47) and (3.48) and inserting the solution in equation (2.13). (The integration is performed using MAPLE 9.5 software package.) The timing jitter, around the first harmonic, of the recovered optical clock signal, $\tau_{jitt}^{P_{clk}}$, and the recovered electrical clock signal, τ_{jitt}^v , is expressed in equation (3.49) and (3.50), respectively:

$$\begin{aligned} \tau_{jitt}^{P_{clk}} = & \frac{1}{\omega_0} \left[\sum_{k_1, \dots, k_n}^n \frac{2\omega_0^2 \sum_{i=1}^n (\mu_i^1 + \nu_i^{11})^{k_1} e^{-\omega_0^2 \sum_{i=1}^n (\mu_i^1 + \nu_i^{11})}}{\pi \prod_{i=1}^n k_i!} \right. \\ & \cdot \left[\text{atan}\left(\frac{\omega_{max}}{\frac{1}{2}\omega_0^2 c_{in} + \sum_{i=1}^n k_i \lambda_i}\right) + \text{atan}\left(\frac{(\omega_{max} + 2\omega_0)}{\frac{1}{2}\omega_0^2 c_{in} + \sum_{i=1}^n k_i \lambda_i}\right) \right. \\ & \left. \left. - \text{atan}\left(\frac{\omega_{min}}{\frac{1}{2}\omega_0^2 c_{in} + \sum_{i=1}^n k_i \lambda_i}\right) - \text{atan}\left(\frac{(\omega_{min} + 2\omega_0)}{\frac{1}{2}\omega_0^2 c_{in} + \sum_{i=1}^n k_i \lambda_i}\right) \right] \right]^{1/2} \end{aligned} \quad (3.49)$$

and

$$\begin{aligned} \tau_{jitt}^v = & \frac{1}{\omega'_0} \left[\sum_{k_1, \dots, k_n}^n \frac{2\omega_0'^2 \sum_{i=1}^n (\mu_i^1 - \mu_i^2 + \nu_i^{11} + \nu_i^{22} - 2\nu_i^{12})^{k_1} e^{-\omega_0'^2 \sum_{i=1}^n (\mu_i^1 - \mu_i^2 + \nu_i^{11} + \nu_i^{22} - 2\nu_i^{12})}}{\pi \prod_{i=1}^n k_i!} \right. \\ & \times \left[\text{atan}\left(\frac{\omega_{max}}{\frac{1}{2}\omega_0'^2 c_{in} + \sum_{i=1}^n k_i \lambda_i}\right) + \text{atan}\left(\frac{(\omega_{max} + 2\omega'_0)}{\frac{1}{2}\omega_0'^2 c_{in} + \sum_{i=1}^n k_i \lambda_i}\right) \right. \\ & \left. \left. - \text{atan}\left(\frac{\omega_{min}}{\frac{1}{2}\omega_0'^2 c_{in} + \sum_{i=1}^n k_i \lambda_i}\right) - \text{atan}\left(\frac{(\omega_{min} + 2\omega'_0)}{\frac{1}{2}\omega_0'^2 c_{in} + \sum_{i=1}^n k_i \lambda_i}\right) \right] \right]^{1/2} \end{aligned} \quad (3.50)$$

where $\omega_{min} = 2\pi f_{min}$ and $\omega_{max} = 2\pi f_{max}$. The f_{min} and f_{max} are the lower and the upper integration limits. Having the analytical expression for timing jitter of the recovered clock signals is a very powerful tool. Using the analytical expressions, we can very quickly get an estimate of the timing jitter of the recovered clock signal and if necessary perform the optimization in terms of the system parameters. Moreover, it is possible compute the timing jitter very close to the carrier.

3.8 Probability density function of the recovered clock signal amplitude

Using the autocorrelation functions determined in the previous section, we determine the Probability Density Function (PDF) of the extracted clock signal. In general, given a stochastic process $x(t)$, we form another stochastic process, $y(t)$, by the following definition:

$$y(t) = T[x(t)] \quad (3.51)$$

The process $y(t)$ is completely specified in terms of the operator T and $x(t)$. The probability density function of $y(t)$, $(p_y(y; t))$, can be determined in terms of the probability density function of $x(t)$ namely $p_x(x; t)$ [93]:

$$p_y(y; t) = \sum_n \frac{p_x(x_n; t)}{|y'(x_n; t)|} \quad (3.52)$$

where n is a positive integer and x_n are the roots of the equation: $y(t) - T[x_n] = 0$. The n represents the total number of roots. Let us now consider the stochastic process $\alpha_{clk}(t)$ and the corresponding transformation:

$$y(t) = \sum_{k=-\infty}^{\infty} A_0 e^{-\left[\frac{t_k + \alpha_{clk}(t)}{T_0}\right]^2} \quad (3.53)$$

where A_0 is a constant, $t_k = t + kT_p$ and $T_p = \frac{1}{f_0}$. Equation (3.53) represents a Gaussian pulse train (optical clock signal) with phase noise. By using equation, (3.52) we will determine the probability density function of the optical clock signal, i.e. $p_y(y; t)$. Since $\alpha_{clk}(t)$ is (asymptotically) a zero mean wide-sense stationary Gaussian process, the probability density function, $p_{\alpha_{clk}}(\alpha_{clk}; t)$, is expressed as:

$$p_{\alpha_{clk}}(\alpha_{clk}; t) = \frac{1}{\sqrt{2\pi}\sigma_{\alpha_{clk}}} e^{-\frac{\alpha_{clk}^2}{2\sigma_{\alpha_{clk}}^2}} \quad (3.54)$$

where the variance $\sigma_{\alpha_{clk}}^2$ is determined from the autocorrelation function (3.43) by setting τ to zero, i.e. $\sigma_{\alpha_{clk}}^2 = \sum_{i=1}^n (\mu_i^1 + \nu_i^{11})$. The roots, $x_n \equiv \alpha_{clk_n}$, are obtained by solving the following equation:

$$y(t) - \sum_{k=-\infty}^{\infty} A_0 e^{-\left[\frac{t_k + \alpha_{clk_n}}{T_0}\right]^2} = 0 \quad (3.55)$$

We want to determine the solution for $0 < y(t) \leq A_0$. Equation (3.55) is easily solved numerically, however, it would also be convenient to have an analytical solution. For an isolated Gaussian pulse, by setting $k = 0$ in equation (3.53), the roots, $\alpha_{clk_{1,2}}$, are easily determined:

$$y(t) = A_0 e^{-\left[\frac{t+\alpha_{clk_n}}{T_0}\right]^2} \Rightarrow \alpha_{clk_{1,2}} = -t \pm T_0 \sqrt{\ln\left(\frac{A_0}{y(t)}\right)} \quad (3.56)$$

If we assume that the T_{FWHM} of the optical clock pulse signal satisfies²: $T_{FWHM} \ll T_p$, then the roots α_{clk_n} of equation (3.55) can be expressed as:

$$\alpha_{clk_n} = -(t + kT_p) \pm T_0 \sqrt{\ln\left(\frac{A_0}{y(t)}\right)} \quad (3.57)$$

Now, the probability density function of an optical clock signal can be determined by using equations (3.52) and (3.57):

$$p_y(y; t) = \sum_{k=-\infty}^{\infty} \frac{T_0}{2\sqrt{2\pi}\sigma_{\alpha_{clk}} y \sqrt{\ln(A_0/y)}} \cdot \left(e^{-\frac{(-t_k + T_0 \sqrt{\ln(A_0/y)})^2}{2\sigma_{\alpha_{clk}}^2}} + e^{-\frac{(-t_k - T_0 \sqrt{\ln(A_0/y)})^2}{2\sigma_{\alpha_{clk}}^2}} \right) \quad (3.58)$$

The probability density function, calculated in equation (3.58) shows that, even though the phase noise $\alpha_{clk}(t)$ has a Gaussian distribution, the PDF of the optical clock signal is not Gaussian. Next, the mean of the optical clock signal is computed. In general, the mean of the stochastic process $y(t)$ specified in equation (3.51) is given by the following integral [93]:

$$\langle y(t) \rangle = \int_{-\infty}^{\infty} T[\alpha_{clk}(t)] p_{\alpha_{clk}}(\alpha_{clk}; t) d\alpha_{clk} \quad (3.59)$$

In order to compute the mean value of the optical clock signal we therefore need to solve:

²In practise this condition will be satisfied. Typically, the repetition frequency of the optical clock signal will be 10 GHz or 40 GHz. The corresponding period time T_p is therefore 25 ps or 100 ps and the typical values of the T_{FWHM} are in the range from 1 - 5 ps.

$$\langle y(t) \rangle = \frac{A_0}{\sqrt{2\pi}\sigma_{\alpha_{clk}}} \sum_{k=-\infty}^{k=\infty} \int_{-\infty}^{\infty} e^{-\left[\frac{t_k + \alpha_{clk}}{T_0}\right]^2} e^{-\frac{\alpha_{clk}^2}{2\sigma_{\alpha_{clk}}^2}} d\alpha_{clk} \quad (3.60)$$

The integral in equation (3.60) is easily solved and the mean of the optical clock signal is thereby given as:

$$\langle y(t) \rangle = \sum_{k=-\infty}^{k=\infty} \frac{A_0 T_0}{\sqrt{(T_0^2 + 2\sigma_{\alpha_{clk}}^2)}} e^{-\frac{t_k^2}{(T_0^2 + 2\sigma_{\alpha_{clk}}^2)}} \quad (3.61)$$

Having computed the mean value of the extracted optical clock signal the corresponding variance is computed by setting τ to zero in equation (3.45) and using equation (3.61). By using the same procedure one can obtain the probability density function and mean of the extracted electrical clock signal.

3.9 Experimental set-up

In order to verify the model of the OPLL based clock recovery, derived in section 3.4-3.7, a simplified experimental set-up is constructed. The experimental set-up is shown in Figure 3.5. The experimental set-up was build by Darko Zibar and Leif K. Oxenløwe from discrete components available to COM•DTU. However, the loop filter in the experimental set-up shown in Figure 3.5 was made by Leif K. Oxenløwe. The measurements were performed by Darko Zibar under guidance of Leif K. Oxenløwe.

In this simple characterization set-up, an electrical mixer is used to derive an error signal from a 10 GHz data signal and the 10 GHz VCO. To investigate the effect of time delay, an electro-absorption modulator (EAM) is used to E/O-convert the VCO signal so it may be transmitted over a variable length of single mode fibre (SMF) before being O/E-converted, amplified and injected into the electrical mixer. The experimental set-up will be characterized in terms of spectral phase noise and resulting timing jitter.

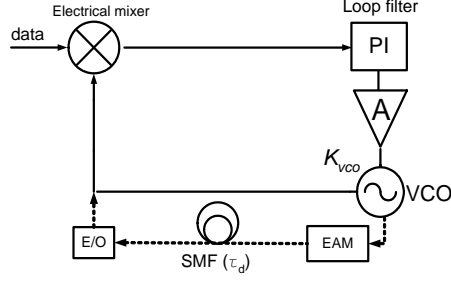


Figure 3.5: Experimental set-up of PLL based clock recovery. EAM: electro-absorption modulator. SMF: Single mode fibre.

3.10 Simulation and experimental results

In this section, detailed characterization, of the clock recovery based on an optoelectronic phase-locked loop, is performed in terms of the SSCR and integrated timing jitter of the recovered clock signals. We use equations (3.47) – (3.50) to perform numerical simulations, and thereby compute the corresponding SSCR and timing jitter. Moreover, the numerical result will be compared to the experimental result obtained using the experimental set-up in Figure 3.5.

In Table I, we have summarized the key system parameters that are held fixed throughout the numerical simulations.

In the following subsections, we are going to investigate how the integrated timing jitter of the recovered clock signal scales with loop gain, time delay and input data signal jitter. We want to investigate the bandwidth in which the input phase noise is transferred to the recovered clock signal (\sim jitter transfer function) and how this can be controlled. In addition, we want to investigate how the VCO affects jitter generation.

3.10.1 Sanity check of the OPLL model

In this section, the results obtained by the newly developed OPLL based clock recovery model will be compared to reported observations in the literature. Due to simplicity noise only associated with the input signal is considered to begin with, i.e. $c_{in} \neq 0$. Noise associated with the free-running VCO and the laser is set to zero, i.e. $c_{vco} = c_{clk} = 0$.

We first start by writing-up open-loop transfer function, $G_{opll}(s)$, of the

Input data signal frequency	160 GHz
Input data signal power	15 dBm
Input data signal FWHM:	2.5 ps
Free-running laser frequency	10 GHz
Laser signal power	15 dBm
Laser signal FWHM	1 ps
Free-running VCO frequency	10 GHz
VCO signal amplitude	1 V
VCO gain	10^5 Hz/V
Photodiode responsivity	0.95 A/W

Table 3.1: Parameters of the OPLL based clock recovery that are held constant throughout the numerical simulations.

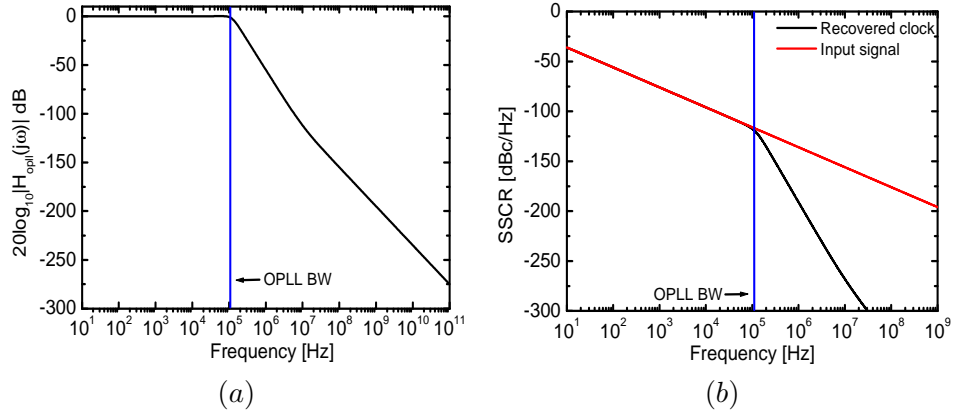


Figure 3.6: $\zeta_1 \cdot K_{vco} = 1.12 \cdot 10^5$ radA/sV, $\tau_1 = \tau_2 = (1/(2\pi 10^7))$ s, $\tau_{ch} = (1/(2\pi 10^4))$ s and $c_{in} = 10^{-22}$ s. (a) The amplitude response of transfer function of the OPLL as a function of ω . (b) SSCR of the recovered clock signal obtain by the theoretical model.

OPLL model in Figure 3.4. Open-loop transfer function of the OPLL shown in Figure 3.4 can be expressed as [80]:

$$G_{opll}(s) = \frac{\zeta_1(1 + s\tau_2)}{s\tau_1} \cdot \frac{K_{vco}}{s} \cdot \frac{1}{1 + s\tau_{ch}} \quad (3.62)$$

where $s = j\omega$. The transfer function of the OPLL, $H_{opll}(s)$, is then expressed as [80]:

$$H_{opll}(s) = \frac{G_{opll}(s)}{1 + G_{opll}(s)} \quad (3.63)$$

The amplitude response of the transfer function, $|H_{opll}(j\omega)|$, is then plotted in Figure 3.6 (a) for the specific set of parameters. Figure 3.6 (a) shows that the bandwidth of the OPLL is approximately 10^5 Hz for the considered set of parameters. According to the method relying on transfer function theory, see [80], the recovered clock signal should then follow input signal noise up to 10^5 Hz.

Next, the SSCR of the recovered 10 GHz clock signal is computed around the first harmonic using equation (3.47) for the same set of parameters as $|H_{opll}(j\omega)|$, see Figure 3.6 (b). In addition, the SSCR of a 10 GHz input signal is plotted using equation (2.12) for $c = c_{in}$. We observe in Figure 3.6 (b) that within the OPLL bandwidth, the SSCR of the recovered clock signal follows the SSCR of the input signal, whereafter beyond the OPLL bandwidth the SSCR of the recovered clock signal is filtered by the OPLL bandwidth. This is in accordance with the results reported in [80, 94].

In Figure 3.7, we also plot the SSCR of the recovered 10 GHz clock signal using equation (3.47), however in this case phase noise associated with the laser in the loop is taken into consideration. In the same Figure, we also plot the SSCR of the input signal and the free-running laser using equation (2.12) for $c = c_{in}$ or $c = c_{clk}$. Phase noise associated with the VCO is still zero. It is observed in Figure 3.7 that beyond the OPLL bandwidth the SSCR of the recovered clock signal follows the SSCR of the laser. This is also in accordance with observations reported in [80, 94].

3.10.2 Timing jitter as a function of loop gain

In this section, it is assumed that the laser characteristic knee frequency, f_{ch} , is large enough such that phase noise of the VCO is completely transferred to the laser, i.e. $\alpha_{clk}(t) = \alpha_{vco}(t)$ and thereby $\gamma_e(t) = 0$. We therefore only need to consider the phase noise associated with the input signal, $\alpha_{in}(t)$, and the VCO, $\alpha_{vco}(t)$. Initially, we assume that the VCO signal contains more phase noise than the input data signal, i.e. $c_{vco} > c_{in}$ and

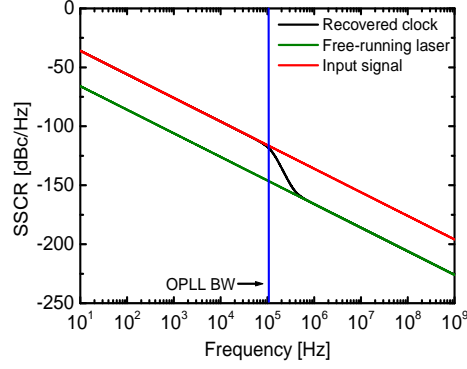


Figure 3.7: SSCR of the recovered clock signal obtain by the theoretical model when noise associated with the input signal and laser are considered. $\zeta_i \cdot K_{vco} = 1.12 \cdot 10^5$ radA/sV, $\tau_1 = \tau_2 = (1/(2\pi 10^7))$ s, $\tau_{ch} = (1/(2\pi 10^4))$ s, $c_{in} = 10^{-22}$ s and $c_{clk} = 10^{-25}$ s.

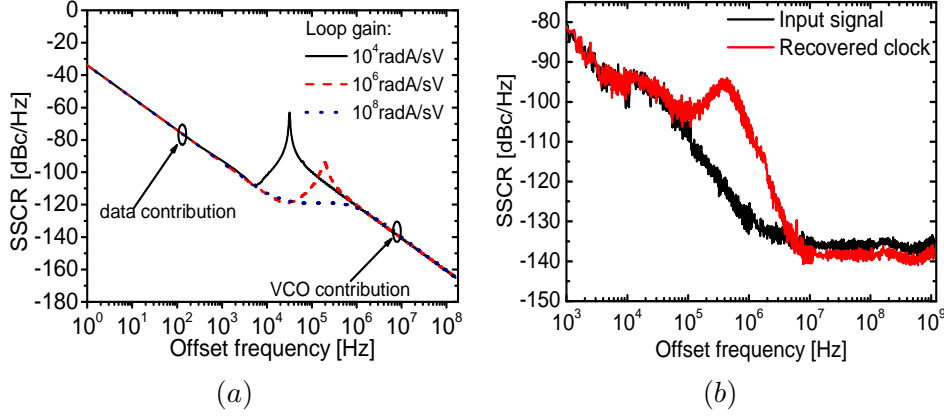


Figure 3.8: (a) Simulation results for SSCR of the extracted clock signal for selected values of the overall loop gain. The VCO jitter is 2.25 ps and PI filter bandwidth is 20 MHz. (b) Measured SSCR of the recovered electrical clock signal.

the time delay is set to zero. The timing jitter of the input data signal and the free-running VCO is computed using equation (2.14) for $c = c_{in}$ or $c = c_{vco}$.

In Figure 3.8(a), the SSCR of the extracted 10 GHz electrical clock signal is computed, around the first harmonic (i.e. 10 GHz), when the overall loop

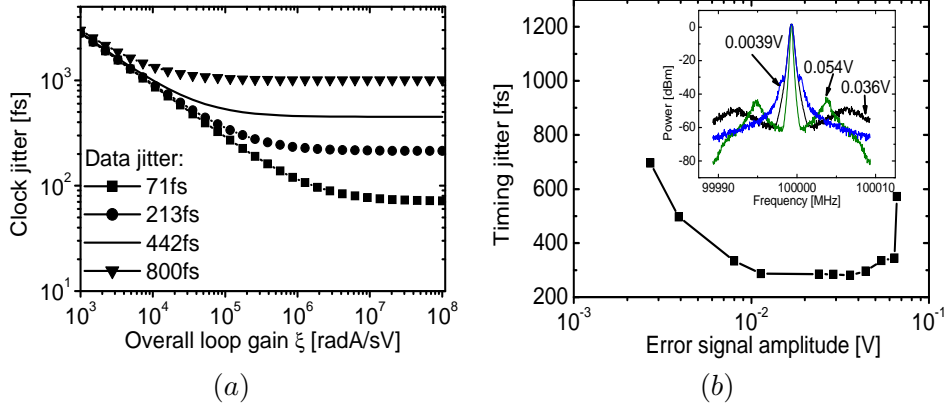


Figure 3.9: (a) Simulation results for timing jitter of the extracted clock signal as a function of the overall loop gain. The VCO jitter is 2.25 ps and PI filter bandwidth is 20 MHz. (b) Measured timing jitter of the recovered clock signal as a function of the error signal amplitude. Inset: measured power spectral density of the recovered clock signal for selected values of the error signal amplitude.

gain, $\xi = \zeta_1 K_{vco}$, is varied from 10^4 radA/sV to 10^8 radA/sV. These values of the loop gain are obtainable in practical realizations of the optoelectronic PLL using standard components [122]. For low frequencies the clock SSCR follows the input data signal SSCR and for higher frequencies it follows the SSCR of the VCO which is in accordance with the observations reported in [80, 94]. The offset frequency, at which the SSCR of the clock signal makes a transition to the SSCR of the VCO, corresponds to the bandwidth of the OPLL. This implies that the low frequency timing jitter is directly transferred from the input data signal to the clock signal, while the high frequency timing jitter of the clock signal originates from the VCO. This means that the jitter generation originating from the VCO will affect the high frequency components of the recovered clock signal.

In general, we are interested in reducing the high frequency jitter from the clock signal since it may result in a penalty when using the clock signal for optical gating. As the overall loop gain, ξ , is increased from 10^4 radA/sV to 10^8 radA/sV in Figure 3.8(a), the SSCR of the clock signal becomes less and less influenced by the VCO signal. The sideband is pushed down and away. For sufficiently large values of the overall loop gain, the SSCR of the clock signal will approach the SSCR of the input data signal.

Using the experimental set-up shown in Figure 3.5, we have measured the SSCR of the recovered electrical clock signal and plotted it in Figure 3.8(b). We want to emphasize that since we were not able to measure many loop parameters in the experimental set-up shown in Figure 3.5, the loop parameters used for the simulations are therefore not the same as the loop parameters in the experimental set-up. This is valid for Figure 3.8, 3.9 and 3.10. The idea behind is just to make a qualitative comparison between trends observed in the simulations and experiments.

It is observed that the measured SSCR follows the input signal SSCR until the PLL bandwidth is exceeded whereafter high frequency noise from the PLL and the VCO is added to the SSCR. This is qualitatively in accordance with the simulation result shown in Figure 3.8(a). It should be observed from the measured SSCR, Figure 3.8(b), that the PLL bandwidth is relatively small (~ 100 kHz). Moreover, we observe in Figure 3.8(b) that the $1/f^2$ frequency component of the VCO phase noise cannot be identified. The reason for this may be that we hit the noise floor in our measurements or a white noise frequency component of the VCO is reached.

In Figure 3.9(a), timing jitter of the extracted clock signal (jitter integration range: 1 Hz - 5 GHz) is computed as a function of the overall loop gain for an input data signal jitter varying from 71 fs to 800 fs (jitter integration range: 1 Hz - 80 GHz). Increasing the loop gain thus reduces the timing jitter of the extracted clock signal when $c_{vco} > c_{in}$, see Figure 3.9(a). As the overall loop gain is increased sufficiently, the clock timing jitter approaches its minimum value. It is worth remarking that this minimum value corresponds to the input data signal jitter. However, when the input data signal jitter is relatively large, i.e. 800 fs, the minimum obtainable jitter of the clock signal is approximately 1 ps. In practise, the PLL overshoots if the loop gain is increased unconditionally. However, since the optoelectronic PLL is modelled in the small signal regime (Ornstein-Uhlenbeck process), the overshooting can therefore not be observed in the analytical results [123].

In Figure 3.9(b), the dependence of the timing jitter of the recovered clock signal is measured as a function of the amplitude of the error signal. It should be mentioned that the loop gain is directly proportional to the amplitude of the error signal. From the power spectral density measurement, inset in Figure 3.9(b), it is observed that as the amplitude of the error signal is increased the sidebands are pushed down and away from the carrier. This is in accordance with numerical simulations shown in Figure 3.8(a). Moreover, Figure 3.9(b) shows how the clock jitter decreases as the

amplitude of the error signal is increased. For sufficiently large values of the error signal, the measured clock signal jitter flattens out and reaches its minimum jitter (285 fs). (A similar trend is observed from numerical results shown in Figure 3.9(a)) As the error signal is increased further the PLL starts to overshoot, which leads to jitter increase as shown in Figure 3.9(b).

3.10.3 Timing jitter in the presence of time delay

Until now, we have assumed that the time delay in the loop was zero. The influence of a time delay on the timing jitter of the extracted clock signal is investigated by plotting the SSCR of the extracted clock signal for zero and 300 ns time delay (corresponds to loop length of ~ 60 m), see Figure 3.10(a). Notice that the SSCR of the extracted clock signal increases, around the resonant peak, when the time delay is 300 ns compared to the zero time delay case. An increase in time delay from 0 to 300 ns, furthermore results in an increased timing jitter from 188 fs to 621 fs. In Figure 3.10(b), the measured SSCR for a short and a long loop is shown. The long loop includes EAM and some additional SMF fibre, see Figure 3.5. The long loop is approximately 10 m long and the short loop is about 2 m long. It is observed from Figure 3.10(b) that the measured SSCR of the extracted clock signal increases and becomes more pronounced around the resonant peak as the loop length is increased. The similar trend is observed using numerical simulations as shown in Figure 3.10(a). Increasing the loop length in the experimental set-up results in an increase of the extracted clock jitter from 285 fs to 430 fs. It is thereby confirmed numerically and experimentally that increasing the loop length results in a jitter increase of the recovered clock signal.

It has been shown previously that in the presence of time delay, the behavior of the loop is very much dependent on the PI filter bandwidth [114]. We therefore need to investigate the timing jitter dependency of the time delay as the PI filter bandwidth is varied. This is shown in Figure 3.11(a). Here, we observe that the clock jitter increases as the time delay increases. However, this is most pronounced for the PI filter bandwidth, f_{bw} , of 3 MHz and 5 MHz. The loop's dynamical behavior becomes more unstable resulting in increased timing jitter as the time delay approaches its critical value ($\tau_d = 1/2\pi f_{bw}$) [114]. The impact of time delay on timing jitter is negligible as long as we are far away from the critical value.

Next, we investigate how the timing jitter of the extracted clock signal is

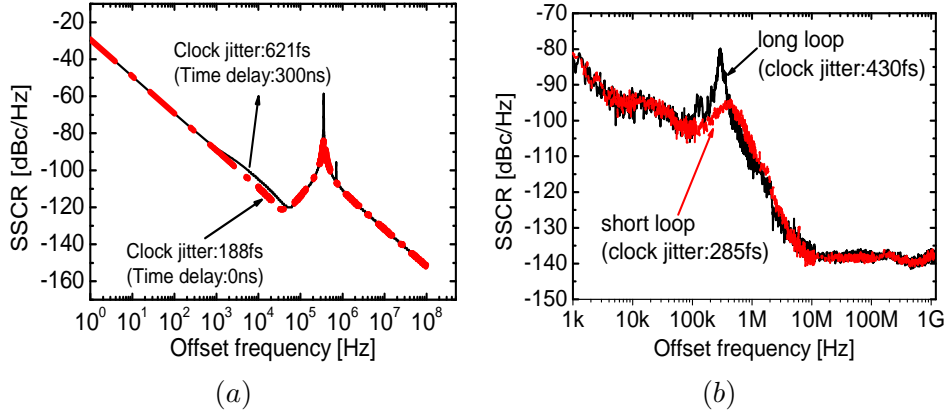


Figure 3.10: (a) Simulation results for SSCR of the extracted clock signal for zero and 300 ns time delay. (b) Measured SSCR of the extracted clock signal for short (2 m) and long (10 m) loop.

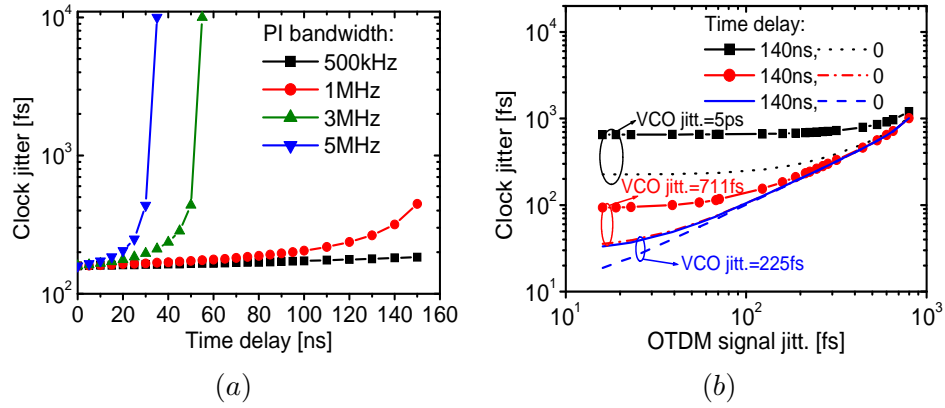


Figure 3.11: (a) Clock jitter as a function of time delay for selected PI filter bandwidths. (b) Integrated jitter of the recovered clock signal as a function of input signal jitter for zero and 140 ns time delay. PI filter bandwidth: 1 MHz.

affected by a time delay as we vary the input data and VCO signal timing jitter. In Figure 3.11(b), the timing jitter of the extracted clock signal is plotted as a function of input OTDM data signal jitter for selected values of VCO jitter (225 fs, 711 fs and 5 ps). The computations are made for zero and 140 ns time delay.

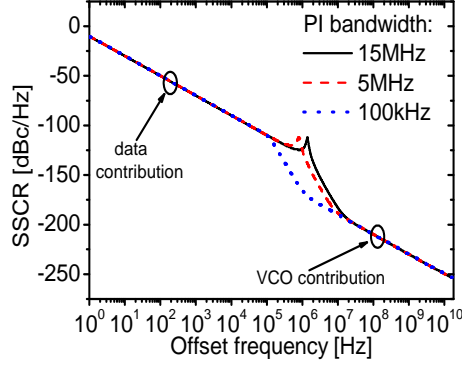


Figure 3.12: Simulation results for SSCR of the extracted clock signal for selected values of the PI filter bandwidths. Input signal jitter: 990 fs. VCO signal jitter: 70 fs.

For the relatively large value of VCO jitter (5 ps), a large increase in clock jitter is observed as the time delay is increased from zero to 140 ns. Furthermore, it should be noted that the clock timing jitter is almost constant over a large range of input jitter values. The timing jitter of the VCO is large and therefore dominates the clock jitter. Reducing the timing jitter of the VCO reduces the impact of the time delay on the clock jitter as seen in Figure 3.11(b). For a relatively low VCO timing jitter of 225 fs, the impact of time delay on the clock jitter becomes negligible for input data signal jitter above approximately 100 fs. Furthermore, reducing the VCO jitter, the extracted clock jitter becomes more dependent on the input data signal jitter. The clock jitter approximately assumes the values of the input data signal jitter when the VCO jitter is 225 fs and the time delay is zero.

So far, we have assumed that the VCO signal contained more phase noise than the input data signal. Now, let us consider a case in which the input signal is more noisy than the VCO signal. This case is shown in Figure 3.12 for selected values of the PI filter bandwidth: 100 kHz, 5 MHz and 15 MHz.

As explained earlier, within the bandwidth of the PLL the clock signal follows the input data signal and for frequencies exceeding the PLL bandwidth, the extracted clock signal couples to the SSCR of the VCO signal. Since the free-running VCO signal contains less timing jitter than the input signal, the recovered optical clock signal will thus exhibit lower timing jitter than the input data signal. In the considered case we want to decrease the

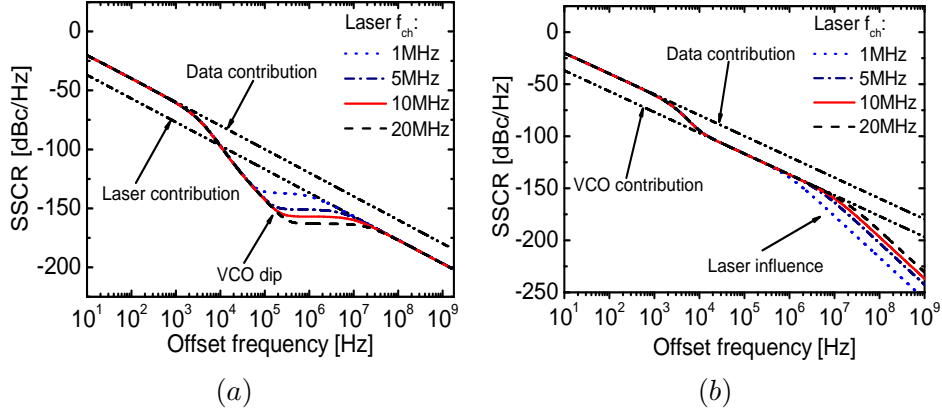


Figure 3.13: Simulation results for SSCR of the recovered optical clock signal for selected values of the laser's characteristic knee frequency, f_{ch} . Input data signal jitter: 160 fs. (a) Free-running laser jitter: 100 fs and free-running VCO jitter: ≈ 0 fs. (b) Free-running laser jitter ≈ 0 fs and VCO jitter 100 fs.

PI filter bandwidth since the coupling frequency to the SSCR of the VCO signal decreases, as observed from Figure 3.12, yielding an overall lower SSCR. However, even though the free-running VCO is less noisy than the input data signal, we may end up with a case where high-frequency noise is added to the extracted clock signal due to other noisy loop components. We may, however, conclude that the minimum requirement, in order for a clock signal to exhibit less timing jitter than the input signal, is to have a VCO which contains less jitter than the input signal, as well as having a low PLL bandwidth.

3.10.4 Phase noise contributions from the laser and the VCO

In this section, we investigate how the SSCR of the recovered optical clock signal (at the laser output) is affected by the free-running laser and VCO phase noise. The input data signal is assumed to have higher jitter than the local oscillators (VCO and laser). The optoelectronic PLL bandwidth is chosen to be relatively low in Figure 3.13 in order to illustrate the coupling from the input data signal SSCR to the VCO and the laser SSCR.

In Figure 3.13(a), the SSCR of the recovered 10 GHz optical clock signal is shown. The laser characteristic knee frequency, f_{ch} , is increased from 1 MHz to 20 MHz. The phase noise of the free-running VCO is negligible

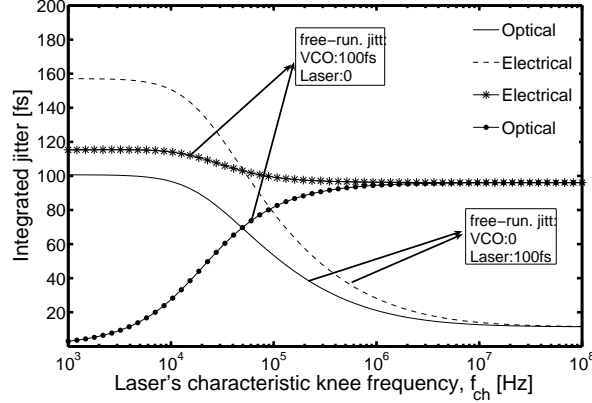


Figure 3.14: Integrated timing jitter of the recovered optical and electrical clock signal as a function of the laser's characteristic knee frequency, f_{ch} . Input jitter: 160 fs. PLL bandwidth: 5 MHz. Jitter integration range: 20 kHz - $f_0/2$.

and the timing jitter of the free-running laser is 100 fs. Figure 3.13(a) shows that within the PLL bandwidth, f_{pll} , the SSCR of the recovered optical clock signal follows the SSCR of the input data signal. The laser characteristic knee frequency, f_{ch} , determines the frequency at which the SSCR of the optical clock couples to the SSCR of the free-running laser. The SSCR of the optical clock signal is dominated by the VCO phase noise in the frequency range $f_{pll} - f_{ch}$. As f_{ch} is increased, the SSCR of the optical clock signal is coupled to the VCO in a larger bandwidth, resulting in a bigger dip.

In Figure 3.13(b), we have assumed that the phase noise associated with the laser is negligible and the free-running VCO has a timing jitter of 100 fs. It is observed that as f_{ch} is decreased, the SSCR is suppressed. By keeping f_{ch} low, the VCO jitter is not transferred to the noise-free laser signal. Furthermore, the high frequency jitter contribution (> 100 MHz) is reduced from the recovered optical clock signal, as observed in Figure 3.13(b). It is therefore of great advantage to use a laser with low phase noise.

In Figure 3.14, the integrated timing jitter of the recovered optical (laser output) and electrical (VCO output) clock signal is computed as a function of f_{ch} . The PLL bandwidth is set to the more realistic value of 5 MHz, allowing for a reasonable locking time [107]. When the jitter of the

free-running laser is 100 fs and the corresponding jitter of the VCO is set to be negligible, the timing jitter of the recovered optical clock signal reduces as f_{ch} increases. This is in accordance with Figure 3.13(a). When the jitter associated with the free-running laser is negligible and the VCO free-running jitter is 100 fs it is observed that the timing jitter of the recovered optical clock signal increases as f_{ch} increases and this is in accordance with Figure 3.13(b). For the extracted electrical clock signal, in both cases the integrated timing jitter reduces as the laser's characteristic knee frequency increases. Furthermore, the recovered optical clock signal exhibits less timing jitter than the electrical clock signal in the range: 1 kHz - 2 MHz.

3.10.5 Reduction of timing jitter

Optical regeneration is potentially a key technology for bit-rates above 40 Gb/s. For optical regeneration, clock signal extraction from a distorted data signal is needed and the extracted clock signal must exhibit lower timing jitter than the degraded data signal [59]. In order for a recovered clock signal to have less jitter than the input signal, the jitter of the free-running local oscillator must be less than that of the input signal as shown in section 3.10.3. An advantage of using an OPLL-based clock recovery is that one has access to both a recovered electrical and optical clock signal. In this section, we describe the requirements to an OPLL to achieve reduced timing jitter.

In Figure 3.15, the contour lines correspond to a constant timing jitter of 112 fs ($\sim 30\%$ jitter reduction compared to the input) of the recovered optical clock signal as a function of the free-running jitter of the VCO and the laser, when the laser's characteristic knee frequency, f_{ch} , is varied from 100 kHz to 20 MHz. The input data signal jitter is 160 fs. In other words, Figure 3.15 shows the requirements for the free-running jitter of the laser and the VCO in order to obtain a recovered optical clock signal with 30% jitter reduction compared to the input, for specific values of f_{ch} .

Figure 3.15 illustrates that as f_{ch} is increased the timing jitter requirement for the laser becomes more relaxed as the integrated clock signal jitter becomes less affected by the laser jitter. The VCO jitter requirements become more demanding as the laser's characteristic knee frequency, f_{ch} , is increased. However, this is insignificant when f_{ch} is above 500 kHz. It should be observed that even though the laser's free-running jitter exceeds 160 fs, an optical clock signal with lower timing jitter than at the input is

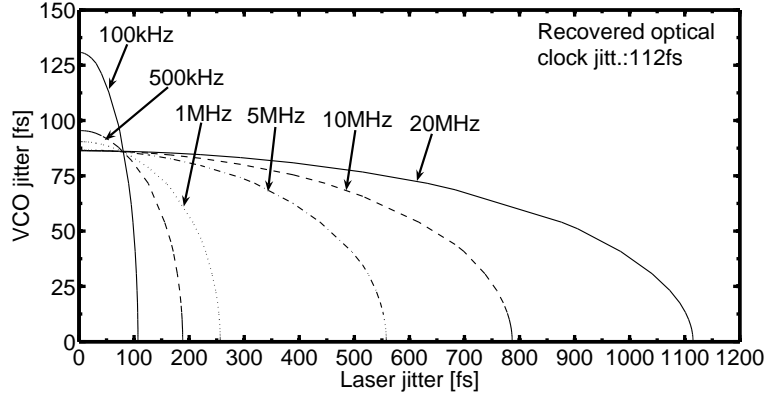


Figure 3.15: Contour lines for a constant timing jitter of 112 fs of the recovered *optical clock signal* as a function of the VCO's and laser's free-running jitter when f_{ch} is varied in the range 100 kHz - 20 MHz. Input jitter: 160 fs. PLL bandwidth: 5 MHz. Jitter integration range: 20 kHz - $f_0/2$.

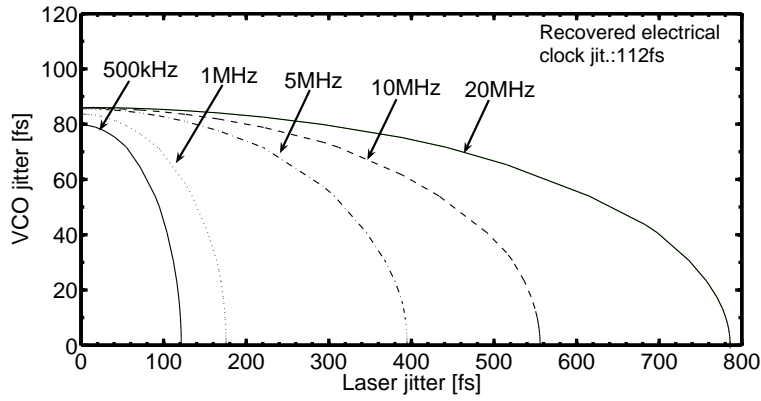


Figure 3.16: Contour lines for a constant timing jitter of 112 fs of the recovered *electrical clock signal* as a function of the VCO's and laser's free-running jitter when f_{ch} is varied in the range 500 kHz - 20 MHz. Input jitter: 160 fs. PLL bandwidth: 5 MHz. Jitter integration range: 20 kHz - $f_0/2$.

obtainable if the jitter of the free-running VCO is low enough.

Figure 3.16 depicts similar contour lines as Figure 3.15 using the recovered electrical clock signal. Again, the contour lines illustrate the requirements

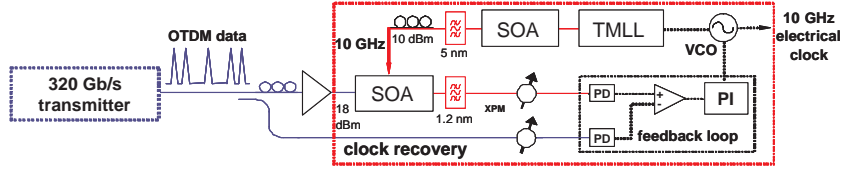


Figure 3.17: Experimental set-up of a clock recovery based on a balanced optoelectronic PLL. TMLL: tunable mode-locked laser, PD: photodiode. PI: proportional integrator, SOA: semiconductor optical amplifier. The Figure is a courtesy of Leif K. Oxenløwe.

for the free-running jitter of the laser and the VCO for specified values of f_{ch} in order to obtain recovered electrical clock signal with 30% jitter reduction compared to the input. Again, we observe that as f_{ch} is increased the timing jitter requirement for the laser and for the VCO become more relaxed. By comparing Figures 3.15 and 3.16 it is observed that the timing jitter requirements for the laser and the VCO are more demanding for the extracted electrical clock signal.

3.11 Experimental demonstration of clock recovery at 320 Gb/s

The results presented in this section were obtained by using a clock recovery set-up build by Leif K. Oxenløwe [124,125] with whom measurements presented here were performed in close collaboration. Since we did not measure loop parameters for the experimental set-up shown in Figure 3.17 comparison between the experimental and simulations results is not possible performed.

3.11.1 Experimental set-up

The experimental set-up for clock extraction of 10 GHz from a 320 Gb/s data signal is shown in Figure 3.17. The experimental set-up in Figure 3.17 was optimized based on the theoretical guidelines obtained in section 3.10.

At the transmitter an erbium glass-crystal-based external cavity mode-locked laser (ERGO-PGL), producing 2 ps pulses at 1555 nm, is used. The output pulses from the ERGO laser (10 GHz repetition rate) are first data modulated, then compressed to 1.3 ps and optically time division multi-

plexed up to 320 Gb/s. An EDFA is used to boost the power of the optical OTDM data signal before injecting the data signal into the clock recovery. A semiconductor optical amplifier acts as a mixer between the OTDM data signal and a locally generated optical clock signal producing an error signal. The locally generated optical clock signal has a repetition rate of 10 GHz and is generated from a tunable semiconductor mode-locked laser. The mode-locked laser is driven by a VCO with an output frequency proportional to the error signal. The wavelength of the mode-locked laser is 1560 nm and the pulse width is approximately 2.2 ps.

Since the data rate of the OTDM signal is 320 Gb/s, ultra-fast effects in the semiconductor optical amplifier need to be used in order to obtain efficient mixing between the data signal and the clock signal. Filtering-assisted XPM is therefore used in order to obtain an error signal which is then used to control the PLL [126]. As the data pulses travel through the SOA, they give rise to changes in the carrier density due to ultra-fast carrier dynamics such as spectral hole burning and carrier heating. This results in gain as well as refractive index changes, which in turn results in an amplitude and phase modulation of the clock pulses – they get chirped. In this experiment, a 1.2 nm filter is placed with its slope on the clock wavelength and its peak on the red shifted side of the clock ~ 1562 nm. Using an optical bandpass filter after the SOA, the phase modulation is transferred to intensity modulation to control the loop. For a more detailed explanation of filtering-assisted XPM, see [126].

3.11.2 Power spectrum density of clock signal

In Figure 3.18(a), an autocorrelation function of a 320 Gb/s data signal is portrayed. The pulse width of the data signal is 1.3 ps which is sufficient for a 320 Gb/s OTDM data signal. Furthermore, an autocorrelation of a 10 GHz optical clock signal with 2.2 ps pulse width is shown.

In Figure 3.18(b), the error signal is measured for selected values of the OTDM data signal rate. The amplitude of the error signal decreases as the bit rate increases. As the data rate of the OTDM signal increases, the efficiency of the phase comparator deteriorates partly because of the slow gain recovery in the SOA and partly due to the slightly too wide pulse width of the optical clock signal. However, we are still able to obtain a control signal which is used to control the optoelectronic PLL.

In Figure 3.19(a), the power spectral density of the recovered 10 GHz clock

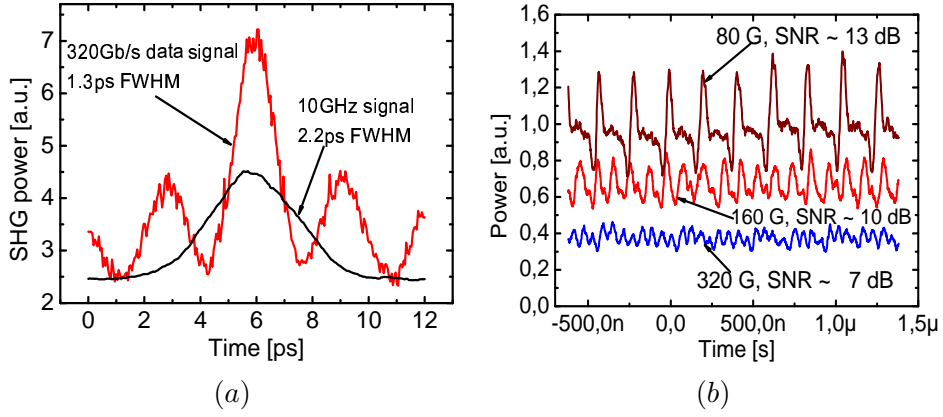


Figure 3.18: (a) Measured autocorrelation function of a 320 Gb/s OTDM data and a 10 GHz optical clock signal. (b) Measured error signal as a function of time for selected values of the data rates of the OTDM signal.

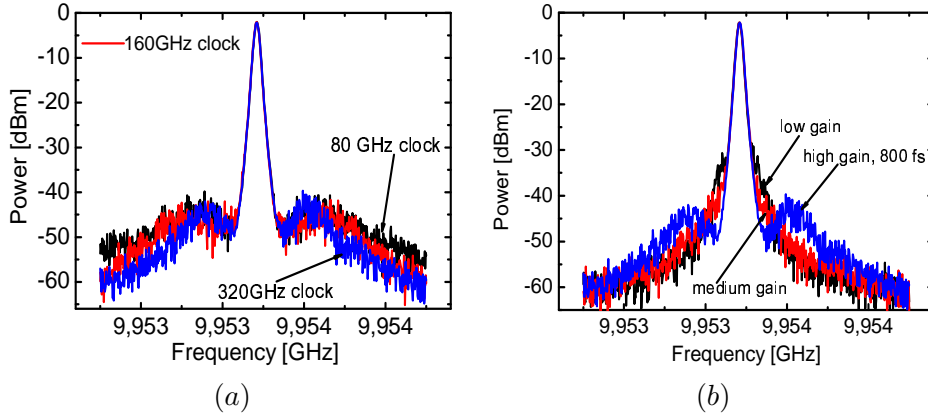


Figure 3.19: Measured power spectral density of the recovered 10 GHz clock signal. (a) The OTDM signal data rate is varied. (b) The loop gain is varied.

signal is portrayed as the OTDM data signal rate is varied from 80 Gb/s to 320 Gb/s. According to Figure 3.19(a), locking of a 10 GHz clock signal to a 320 Gb/s OTDM data signal is obtained. In Figure 3.19(b), the power spectral density of a 10 GHz clock signal recovered from 320 Gb/s is shown as a function of loop gain. As the loop gain is increased, the sidebands are pushed down and away. This is also in agreement with Figure 3.8(a) and

(b). The measured timing jitter is 800 fs for the highest tolerable value of the loop gain, before the loop starts to overshoot.

The measured timing jitter of 800 fs of the recovered clock signal is too high for OTDM transmission systems operating at 320 Gb/s. The reason why we obtained such a high value of the timing jitter is partly because of the long loop length (~ 20 m), high TMLL jitter and also due to the limited loop gain. We recall that the loop gain is directly proportional to the error signal and the amplitude of the error signal is relatively small at 320 Gb/s. One way of improving the timing jitter performance would be to use a shorter loop and an optical clock signal with a very narrow pulse width and low jitter.

3.12 Summary

A detailed phase noise analysis of a clock recovery circuit based on an opto-electronic PLL was presented. The phase noise associated with the signals, the effects of loop time delay and laser transfer function, were all been taken into account. Using a small-signal analysis the Langevin equations describing the corresponding system were linearized to an Ornstein-Uhlenbeck process and the correlation functions of the Ornstein-Uhlenbeck process were obtained by inverse Fourier transformation technique. Furthermore, the probability density function of the recovered clock signal was computed. The noise analysis have illustrated that the minimum recovered clock signal jitter approaches the input data signal jitter if the input data signal has less jitter than the VCO and the laser. Increasing the loop length resulted in an increase in timing jitter of the recovered clock signal as the time delay approached its critical value. The impact of time delay on the clock jitter was reduced by using a low noise VCO and a low PI filter bandwidth. A simple experimental set-up of the electrical PLL was constructed in order to verify the model. Numerical results were observed qualitatively to be in good agreement with experimental results. In general, experimental and numerical results seemed to follow the same trend.

Furthermore, we have numerically investigated the influence of noisy electrical and optical local oscillators on the timing jitter of the recovered clock signal. Timing jitter requirements for the laser could be significantly relaxed when using a large laser characteristic knee frequency with a low-noise VCO. If the laser had more jitter than the VCO it is preferable to use a large laser characteristic knee frequency and if the laser had less jitter than

the VCO it is preferable to use a small laser characteristic knee frequency. We also showed that the recovered optical clock signal exhibited lower timing jitter than the recovered electrical clock signal for low values of the laser characteristic knee frequency.

Using filtering-assisted XPM in a semiconductor optical amplifier, a 10 GHz clock signal was successfully extracted from a 320 Gb/s OTDM data signal. A stable operation of 320 Gb/s clock recovery was thereby achieved.

Chapter 4

The Effects of Timing Jitter on 3R Regenerator and Demultiplexer

In ideal optical communication systems, the pulses of the digital data stream would arrive at times that are integer multiples of the data signal repetition period. However, during the data signal transmission through the fibre-optic channel, the data signal will experience different types of degradation which will alter data signal (pulse) arrival time at the receiver. To be more specific, oscillator phase and amplitude noise at the transmitter, plus Amplified Spontaneous Emission (ASE), originating from optical in-line amplification, will cause the pulses to arrive at times that differ from integers of the data signal repetition period. The effect of phase and amplitude noise plus ASE will thus result in a jittery data signal. As the data signal is propagated through the transmission channel, the total jitter will accumulate and increase with the transmission distance [104]. At some point in the transmission link, the accumulated jitter will become so large leading to incorrect decisions and loss of information. The accumulated jitter will thereby limit the maximum transmission distance. In addition, transmission channel (fibre) non-linearities will enhance jitter coupling among different noise sources resulting in increased accumulated jitter. Optical transmission systems therefore use regenerators to limit the accumulated jitter and thereby increase the transmission distance.

A brief review of regeneration schemes in optical communication systems is at first presented in this chapter. We describe a traditional electrical regeneration scheme and list some of its advantages and disadvantages.

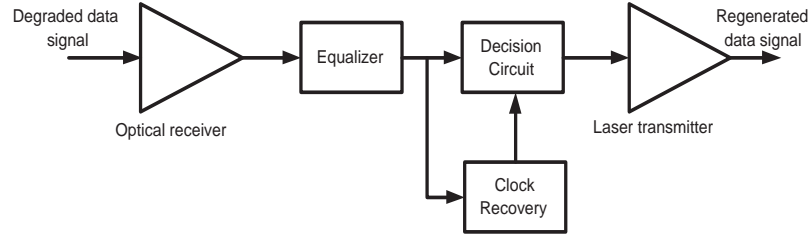


Figure 4.1: Block diagram of a conventional fibre optic regenerator [104].

Furthermore, an optical regeneration scheme, which is very promising for ultra high speed optical communication systems, is also described.

We model the optical regenerator as an Ornstein-Uhlenbeck process and derive a novel analytical expression for the power spectral density of the regenerated optical data signal, in the presence of the optoelectronic PLL based clock recovery. Using the analytical expression, we investigate the effects of the data and clock signal timing jitter, pulse shape and pulse width on the timing jitter of the regenerated data signal. These investigations are performed at 40 Gb/s and 160 Gb/s.

Thereafter, the focus is shifted from optical regeneration to optical demultiplexing. For high-speed optical communication systems timing jitter is a crucial parameter for switching operations between the data and control signal. This is especially valid for the demultiplexer. The effect of timing jitter becomes very important as the bit-rate of the data signal increases beyond 100 Gb/s and it is therefore essential to determine its effect. The impact of gating timing jitter on a 160 Gb/s demultiplexer is investigated experimentally using two pulse sources with different timing jitter properties. We also investigate experimentally the interplay between the control signal pulse width and timing jitter in order to achieve error-free performance. In addition, we investigate how the impact of timing jitter on the optical demultiplexer can be reduced.

4.1 Regenerators in optical communication systems

Regenerators can be divided into conventional (electrical) regenerators and optical regenerators. In Figure 4.1, we have shown a conventional regenerator used in optical communication systems. The regenerator shown in

Figure 4.1 is based on the so-called Optical-Electrical-Optical (O-E-O) conversion, so the regenerator is not transparent to the optical data signal. An optical receiver is used to convert a jittery optical data signal into an electrical signal. The electrical signal is then passed through an equalizer to improve the data signal quality. After the equalizer, one part of the signal is sent to the clock recovery circuit and the other part of the signal is sent to the decision circuit. The clock recovery, typically based on a phase-locked loop, extracts the clock signal which is then used to trigger the decision circuit [104]. The clock recovery must have a low bandwidth in order not to allow a lot of jitter transfer from the degraded data signal. The data signal is then sampled and the regenerated output of the decision circuit modulates a laser.

In general, the regenerator will act as a low pass filter with a transfer function similar to the one shown in Figure 3.1. The goal is to achieve a regenerated output data signal with less jitter than the input data signal. The regenerator will improve the quality of the signal, such that it can be transmitted to longer distances compared to the non-regenerated signal. Conventional O-E-O regenerators are widely deployed in ETDM communication systems operating at 10 Gb/s per single channel [127]. Also for the next generation of ETDM systems operating at 40 Gb/s per single channel, O-E-O regenerators could fully be realized using commercially available components [128]. Even though conventional O-E-O regenerators have many benefits such as integration, low cost, mass production, etc., there are some issues that arise when the capacity of the optical communication system is significantly increased in terms of number of WDM channels and single channel bit-rate [16].

In a WDM system each channel needs an independent O-E-O regenerator and if the number of WDM channels is significantly increased this will lead to an increased power consumption and cost [105]. Moreover, as the line rate of a single channel is increased beyond 100 Gb/s, the challenge is to ensure satisfactory performance of the electronics at such high bit-rates [8, 9]. An alternative approach would be to use all-optical signal regeneration which has been investigated over the last years. When using all-optical regeneration, the signal regeneration is performed in the optical domain. In this way O-E-O conversion is avoided enabling a transparent operation.

In Figure 4.2, the basic configuration of an all-optical 3R regenerator is shown. Depending on the number of regenerative functionalities, optical

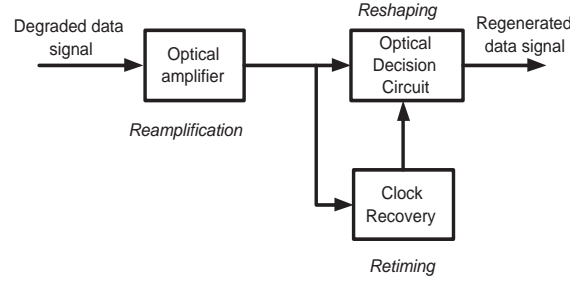


Figure 4.2: Basic configuration of an all-optical 3R regenerated [16].

regenerators can be classified as 1R, 2R and 3R, where the "R's" represent reamplification, reshaping and retiming, correspondingly. Optical amplifiers like EDFA can be used to perform the 1R function. The retiming and reshaping is achieved by first performing a (optical) clock recovery at the line rate from the degraded data signal. Clock recovery can be performed using various techniques as explained in Chapter 3. Thereafter, the optical clock signal pulses are switched out in the optical decision gate by the data signal. Physically this means that "1" and "0" from the degraded jittery data signal are transferred to the clean recovered optical clock signal. It is therefore imperative that the recovered optical clock signal must have less jitter than the degraded input data signal.

There are many different configurations of all-optical 3R regenerators exploiting different optical effects to achieve non-linear gating (optical decision gate). The optical decision gate can be realized using SOA in Mach-Zender interferometer configuration [47–49] (operation at 40 Gb/s and 80 Gb/s was demonstrated), electro-absorption modulators [50–52], non-linear optical loop mirrors [53] and SPM in highly non-linear optical fibres [54]. Furthermore, using a Kerr switch in highly non-linear optical fibre signal regeneration at 160 Gb/s has been demonstrated [55]. In order not to have a bit-rate dependency, the optical decision gate needs to be very fast. SOA based non-linear gates are limited in speed by carrier recovery time [49]. Furthermore, ASE will add some extra noise to the signal. Fibre based regenerators are promising solutions for high-speed optical signals exceeding 100 Gb/s due to the ultra-fast response (in terms of fs) of the optical fibres [55].

Ideally, the optical decision gate has a role of a limiter and should have

a step-like transfer function. In this way noise at "0" and "1" can be significantly compressed. Higher contrasts of the optical decision gate will lead to a better reshaping of the signal. An important issue with all-optical regeneration is that the wavelength of the regenerated signal should be the same as that of the input data signal. This is in order to make the design of the transmission link and switching nodes simple. Moreover, performance of the all-optical regenerator should not depend on the pulse shape and data pattern.

Even though a lot of work has been done on all-optical 3R regeneration little attention has been given to the retiming in the presence of the recovered clock signal. Retiming properties of the regenerator in terms of the recovered optical clock signal jitter have not been characterized. However, the results presented in references [55] and [129] suggest that optical data signal pulses need to have rectangular shape (i.e. flat-top pulses) in order to significantly reduce the jitter of the degraded data signal. In this chapter, we will focus on the retiming properties of 3R regenerator in the presence of the recovered optical clock signal. Using the results on clock recovery, presented in Chapter 3, we want to determine how a 3R regenerator filters out the accumulated jitter from the incoming data signal using the low-noise recovered clock signal. Furthermore, the effects of pulse shape and width on the retiming properties are determined.

4.2 Power spectrum density of regenerated data signal

The model set-up of an all-optical 3R regenerator for which we are going to investigate retiming properties is shown Figure 4.3. The clock recovery is based on an optoelectronic PLL, shown in Figure 3.4 and analyzed in Chapter 3. The intensity of the input data signal in the presence of the accumulated phase noise, $\alpha_{in}(t)$, originating from the transmitter and transmission link properties, as explained in section 4.1, is expressed in equation (3.1). If we assume that the pulse shaper in Figure 4.3 does not contribute with phase noise, the output of the pulse shaper can be expressed as:

$$P_{ps}(t + \alpha_{in}(t)) = \sum_{k=-\infty}^{\infty} b_k e^{jk\omega_0(t + \alpha_{in}(t))} \quad (4.1)$$

where b_k are Fourier coefficients and $\omega_0 = 2\pi f_0$. The Fourier coefficients b_k

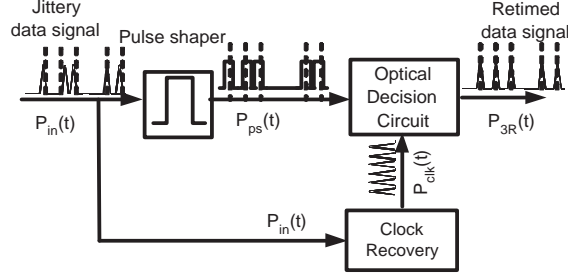


Figure 4.3: Model set-up of an all-optical 3R regenerator with a pulse shaper.

are determined by the pulse shape of the data signal after the pulse shaper. Similarly, the recovered optical clock signal is expressed by equation (3.34):

$$P_{clk}(t + \alpha_{clk}(t)) = \sum_{q=-\infty}^{\infty} c_q e^{jq\omega'_0(t + \alpha_{clk}(t))} \quad (4.2)$$

Following Figure 4.3, the output signal from the pulse shaper, $P_{ps}(t)$, is incident on the optical decision gate (2R). The recovered optical clock signal, $P_{clk}(t)$, is used to trigger the optical decision gate. If we assume that the optical decision gate transfer function is fully determined by the optical clock signal pulse shape, the operation of the optical decision gate can be approximated by the mathematical operation of multiplication [130]. The regenerated (retimed) output optical data signal, $P_{3R}(t)$, can thereby be expressed as:

$$\begin{aligned} P_{3R}(t) &= P_{ps}(t + \alpha_{in}(t)) \cdot P_{clk}(t + \alpha_{clk}(t)) \\ &= \sum_{k,q=-\infty}^{\infty} F(k, q) e^{jk\omega_0(t + \alpha_{in}(t))} e^{jq\omega'_0(t + \alpha_{clk}(t))} \end{aligned} \quad (4.3)$$

where $F(k, q) = b_k c_q$. In order to characterize the jitter and thereby retiming properties of the all-optical regenerator, shown in Figure 4.3, we would like to determine the power spectral density of the retimed optical data signal, $P_{3R}(t)$. This can be done by following the procedure described in section 3.7.

We begin by determining the autocorrelation function of the regenerated optical data signal, $P_{3R}(t)$. The autocorrelation function of the retimed optical data signal, $\langle P_{3R}(t) P_{3R}^*(t + \tau) \rangle$, can be expressed as:

$$\begin{aligned}
\langle P_{3R}(t)P_{3R}^*(t+\tau) \rangle &= \sum_{k,l,q,m=-\infty}^{\infty} F(k,l)F^*(q,m) \\
&\cdot e^{j(k-l)\omega_0 t} e^{-jl\omega_0 \tau} e^{j(q-m)\omega'_0 t} e^{-jm\omega'_0 \tau} \\
&\times \langle e^{j(\omega_0(k\alpha_{in}(t)-l\alpha_{in}(t+\tau))+\omega'_0(q\alpha_{clk}(t)-m\alpha_{clk}(t+\tau)))} \rangle
\end{aligned} \tag{4.4}$$

The term $\Phi(\omega_0, \omega'_0) \equiv \langle e^{j(\omega_0(k\alpha_{in}(t)-l\alpha_{in}(t+\tau))+\omega'_0(q\alpha_{clk}(t)-m\alpha_{clk}(t+\tau)))} \rangle$ corresponds to the joint characteristic function of the stochastic processes $\omega_0(k\alpha_{in}(t) - l\alpha_{in}(t + \tau))$ and $\omega'_0(q\alpha_{clk}(t) - m\alpha_{clk}(t + \tau))$. We recall from Chapter 3, that the input phase noise, $\alpha_{in}(t)$, and the phase noise of the recovered clock signal, $\alpha_{clk}(t)$, are wide-sense stationary zero mean Gaussian processes. Furthermore, if we assume that $\alpha_{in}(t)$ and $\alpha_{clk}(t)$ are two jointly normal processes, the joint characteristic function, $\Phi(\omega_0, \omega'_0)$, is expressed as [93]:

$$\Phi(\omega_0, \omega'_0) = e^{\frac{1}{2}(\omega_0^2 \sigma_\theta^2(t, \tau) + 2r \sigma_\theta^2(t, \tau) \sigma_\vartheta^2(t, \tau) \omega_0 \omega'_0 + \omega_0'^2 \sigma_\vartheta^2(t, \tau))} \tag{4.5}$$

where $\sigma_\theta^2(t, \tau)$ and $\sigma_\vartheta^2(t, \tau)$ are variances of $\theta_{k,l}(t, \tau) \equiv k\alpha_{in}(t) - l\alpha_{in}(t + \tau)$ and $\vartheta_{q,m}(t, \tau) \equiv q\alpha_{clk}(t) - m\alpha_{clk}(t + \tau)$, respectively. The $r = \langle (k\alpha_{in}(t) - l\alpha_{in}(t + \tau))(q\alpha_{clk}(t) - m\alpha_{clk}(t + \tau)) \rangle$. Using equations (3.29) and (3.43), the variances $\sigma_\theta^2(t, \tau)$ and $\sigma_\vartheta^2(t, \tau)$ are expressed as:

$$\begin{aligned}
\sigma_\theta^2(t, \tau) &= \langle [k\alpha_{in}(t) - l\alpha_{in}(t + \tau)]^2 \rangle - \langle [k\alpha_{in}(t) - l\alpha_{in}(t + \tau)] \rangle^2 \\
&= k^2 \langle \alpha_{in}^2(t) \rangle - 2kl \langle \alpha_{in}(t) \alpha_{in}(t + \tau) \rangle + l^2 \langle \alpha_{in}^2(t + \tau) \rangle \\
&= -klc_{in}|\tau|
\end{aligned} \tag{4.6}$$

$$\begin{aligned}
\sigma_\vartheta^2(t, \tau) &= \langle [q\alpha_{clk}(t) - m\alpha_{clk}(t + \tau)]^2 \rangle - \langle [q\alpha_{clk}(t) - m\alpha_{clk}(t + \tau)] \rangle^2 \\
&= qmc_{in}|\tau| + (q^2 + m^2) \sum_{i=1}^n (\mu_i^1 + \nu_i^{11}) - 2qm \sum_{i=1}^n (\mu_i^1 + \nu_i^{11}) e^{-\lambda_i |\tau|}
\end{aligned} \tag{4.7}$$

Inserting the expression for the joint characteristic function, equation (4.5), into equation (4.4), we observe that only terms corresponding to $l = k$ and $m = q$ are non-zero for $t \rightarrow \infty$. For $l = k$ and $m = q$, the correlation factor r in equation (4.5) is given by (stationarity condition is used):

$$\begin{aligned}
r &= \langle (k\alpha_{in}(t) - l\alpha_{in}(t + \tau))(q\alpha_{clk}(t) - m\alpha_{clk}(t + \tau)) \rangle \\
&= -kq[\langle \alpha_{clk}(\tau)\alpha_{in}(0) \rangle + \langle \alpha_{in}(\tau)\alpha_{clk}(0) \rangle] \\
&+ kq[\langle \alpha_{clk}(0)\alpha_{in}(0) \rangle + \langle \alpha_{in}(0)\alpha_{clk}(0) \rangle]
\end{aligned} \tag{4.8}$$

From equation (3.4) we recall that $\alpha_{clk}(t) = \phi_e(t) + \alpha_{in}(t)$ when the optoelectronic PLL is locked. Therefore $\langle \alpha_{clk}(\tau)\alpha_{in}(0) \rangle$ and $\langle \alpha_{in}(\tau)\alpha_{clk}(0) \rangle$ can be expressed as:

$$\langle \alpha_{clk}(\tau)\alpha_{in}(0) \rangle = \langle \phi_e(\tau)\alpha_{in}(0) \rangle + \langle \alpha_{in}(\tau)\alpha_{in}(0) \rangle \tag{4.9}$$

$$\langle \alpha_{in}(\tau)\alpha_{clk}(0) \rangle = \langle \alpha_{in}(\tau)\phi_e(0) \rangle + \langle \alpha_{in}(\tau)\alpha_{in}(0) \rangle \tag{4.10}$$

Combining equations (4.9), (4.10), (3.29) and (3.33) we have following:

$$\begin{aligned}
&[\langle \alpha_{clk}(\tau)\alpha_{in}(0) \rangle + \langle \alpha_{in}(\tau)\alpha_{clk}(0) \rangle] \\
&= \langle \phi_e(\tau)\alpha_{in}(0) \rangle + \langle \alpha_{in}(\tau)\phi_e(0) \rangle + 2\langle \alpha_{in}(\tau)\alpha_{in}(0) \rangle \\
&= -c_{in}|\tau| + \sum_{i=1}^n \mu_i^1 e^{-\lambda_i|\tau|}
\end{aligned} \tag{4.11}$$

and similarly:

$$\langle \alpha_{clk}(0)\alpha_{in}(0) \rangle + \langle \alpha_{clk}(0)\alpha_{in}(0) \rangle = \sum_{i=1}^n \mu_i^1 \tag{4.12}$$

The correlation factor, r , in equation (4.8) is thus given by:

$$\begin{aligned}
r &= \langle (k\alpha_{in}(t) - l\alpha_{in}(t + \tau))(q\alpha_{clk}(t) - m\alpha_{clk}(t + \tau)) \rangle \\
&= kq \left[c_{in}|\tau| + \sum_{i=1}^n \mu_i^1 (1 - e^{-\lambda_i|\tau|}) \right]
\end{aligned} \tag{4.13}$$

Combining equation (4.5), (4.6), (4.7) and (4.13), the autocorrelation function, equation (4.4), of the regenerated output data signal is expressed as:

$$\begin{aligned}
\langle P_{3R}(t)P_{3R}^*(t+\tau) \rangle &= \sum_{k,q=-\infty}^{\infty} F(q,k)F^*(q,k)e^{-j\omega_0 k\tau}e^{-j\omega'_0 q\tau} \\
&\times \exp\left(-\frac{1}{2}\left[(k^2\omega_0^2c_{in} + q^2\omega_0'^2c_{in} + 2kq\omega_0\omega'_0c_{in})|\tau| \right. \right. \\
&\left. \left. + 2\sum_{i=1}^n(q\omega_0'^2(\mu_i^1 + \nu_i^{11}) + kq\omega_0\omega'_0\mu_i^1)(1 - e^{-\lambda_i|\tau|})\right]\right)
\end{aligned} \tag{4.14}$$

Since the autocorrelation function in equation (4.14) is on the same form as the autocorrelation function of the recovered clock signal presented in reference [94], the power spectral density of the regenerated output data signal, $P_{3R}(t)$ is derived as following:

$$\begin{aligned}
S_{3R}(\omega) &= \sum_{k,q=-\infty}^{\infty} \sum_{k_1,\dots,k_n=0}^{\infty} F(k,q)F^*(k,q) \\
&\exp\left[-\sum_{i=1}^n(k^2\omega_0^2(\mu_i^1 + \nu_i^{11}) + kq\omega_0\omega'_0\mu_i^1)\right] \\
&\cdot \frac{[\prod_{i=1}^n k_i]}{\prod_{i=1}^n [k^2\omega_0^2(\mu_i^1 + \nu_i^{11}) + kq\omega_0\omega'_0\mu_i^1]^{k_i}} \\
&\times \frac{1}{\left[\left(\frac{1}{2}(\omega_0^2c_{in}(k^2 + q^2) + 2kq\omega_0\omega'_0c_{in} + \sum_{i=1}^n k_i)\right)^2 + (k\omega_0 + q\omega'_0 + \omega)^2\right]} \\
&\cdot \left[\frac{1}{2}(\omega_0^2k^2c_{in} + \omega_0'^2q^2c_{in} + 2kq\omega_0\omega'_0c_{in} + \sum_{i=1}^n k_i\lambda_i)\right]
\end{aligned} \tag{4.15}$$

By performing an integration of equation (4.15), the timing jitter of the regenerated optical data signal can be computed.

4.2.1 Numerical investigations of the effect of pulse shape and width on the retiming properties of 3R

Using the analytical expression for the power spectral density of the retimed data signal, equation (4.15), we will investigate the retiming properties of the 3R regenerator in Figure 4.3. The pulse shape of the input data signal $P_{in}(t+\alpha_{in}(t))$ is assumed Gaussian. Furthermore, we assume that the pulse

shaper shapes the input data signal pulses into flat-top pulses (rectangular pulses).

In Figure 4.4(a), the SSCR of the input data signal, $P_{in}(t)$, the recovered optical clock signal, $P_{clk}(t)$, and the regenerated data signal, $P_{3R}(t)$, is computed using equations (2.11), (3.47) and (4.15). The input signal data rate is set to 40 Gb/s and the FWHM of the data signal pulses is 11 ps. The FWHM of the recovered optical clock signal is set to 1 ps. For simplicity, timing jitter of the free-running VCO and laser is set to zero. Figure 4.4(a), illustrates the effect of a pulse shaper for the considered case. When the pulse shaper is used to shape the input data signal pulses into flat-top pulses, it is observed in Figure 4.4(a) that the SSCR of the regenerated data signal closely follows the SSCR of the recovered optical clock signal up to 2 MHz offset frequency. By using the pulse shaper, a significant reduction in the phase noise can be achieved. On the contrary, Figure 4.4(a) shows that in the absence of the pulse shaper, the SSCR of the regenerated data signal lies just below the SSCR curve of the input data signal and very small reduction in the phase noise is achieved for the considered case.

One way to sanity check the 3R model is to look at the SSCR of the regenerated data signal, $P_{3R}(t)$, obtained using equation (4.15) and compare it to the SSCR of the input signal and recovered clock signal obtained using equation (2.11) and (3.47). This is illustrated in Figure 4.4(a). To begin with let's look at the SSCR of the regenerated data signal when the pulse shaper is used so that the input pulses to the regenerator have rectangular shape. Since the regenerated data signal and the input data signal will be synchronized, see Figure 4.3, the SSCR curve of the regenerated data signal should follow the SSCR of the input data signal within the clock recovery bandwidth. This is observed in Figure 4.4(a). Beyond the clock recovery bandwidth, the phase noise of the regenerated data signal should be filtered, since noise associated with the oscillators in the clock recovery is set to zero. In Figure 4.4(a), we observe that the SSCR of the regenerated data signal is filtered compared to the input SSCR, i.e. the SSCR of the regenerated data signal follow the SSCR of the recovered clock signal up to approximately 2 MHz. The reason why the SSCR curve of the regenerated data signal includes additional noise beyond 2 MHz compared to the SSCR of the recovered clock signal may be because some of the phase noise from the input signal is converted to the amplitude noise in the regeneration process.

Next, we would like to perform a detailed analysis of how the integrated jitter of the regenerated data signal scales as the recovered optical clock signal FWHM is varied. This is illustrated in Figure 4.4(b). The reference input data signal and the recovered optical clock signal jitter are also plotted in Figure 4.4(b). The average power of the optical clock signal is held constant at 8 dBm. Figure 4.4(b) shows that the integrated jitter of the recovered optical clock signal decreases as the clock signal FWHM is increased. This is because the average optical clock signal power is held constant as the clock signal pulse width is increased and this leads to decreased frequency components of the clock signal. Decreased clock signal frequency components lead to decreased loop gain of the clock recovery and thereby decreased jitter since the VCO has less jitter than the input signal. Moreover, it is observed in Figure 4.4(b) that when the pulse shaper is used to obtain flat-top pulses, the integrated jitter of the regenerated data signal closely follows the recovered clock signal jitter for relatively narrow clock signal FWHM (up to 3 ps). Thereafter, as the clock signal FWHM is increased, the integrated jitter of the regenerated data signal increases as well. In summary, flat-top signal pulses will not allow for jitter transfer from the jittery input data signal to the regenerated data signal if the clock signal is sufficiently narrow.

It should be furthermore noted that according to Figure 4.4(b), the regenerated data signal will also contain less jitter than the input signal if the pulse shaper is not used. However, the amount of the reduced jitter is much smaller compared to the case when the pulse shaper is used. Figure 4.4(b) shows that if the pulse shaper is not used, it is not possible for the regenerated data signal to match the jitter of the recovered optical clock signal. Furthermore, we observe in Figure 4.4(b) that the integrated jitter of the regenerated data signal decreases and flattens out as the clock signal FWHM is sufficiently increased. This is because as the FWHM is increased the pulse top becomes more broader (approaches flat-top) such that the jitter transfer from the degraded data signal to the regenerated data signal is limited.

In summary, Figure 4.4 shows that much stronger jitter suppression for the regenerated data signal can be obtained when rectangular input signal pulse shape is used. This is in qualitatively good agreement with the experimental results reported in [55] where it is demonstrated that the system performance of the 3R regenerator significantly improves when rectangular input signal pulse shape is used compared to the case when the Gaussian input signal pulse shape is used. However, one thing which is remarkable

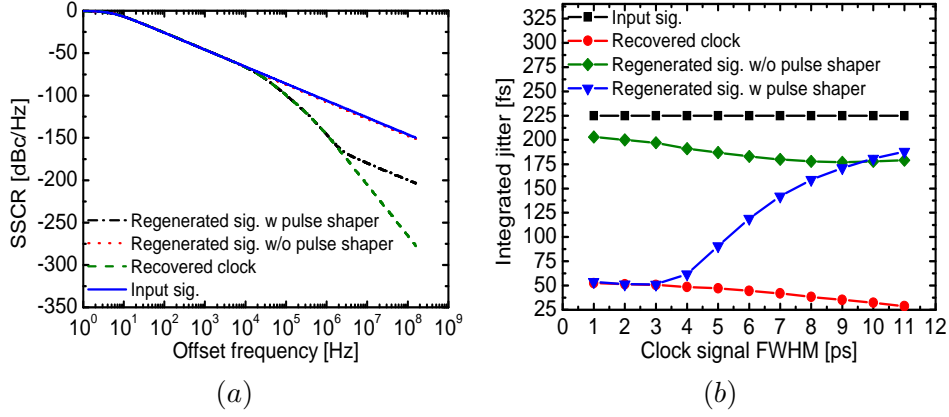


Figure 4.4: (a) SSCR as a function of offset frequency. FWHM of input data signal: 11 ps, FWHM of the recovered optical clock signal: 1 ps. (b) Integrated jitter as a function of recovered optical clock signal FWHM. Gaussian input signal pulse shape is assumed in (a) and (b).

in Figure 4.4 is that very large difference in jitter suppression is achieved when rectangular input signal pulses are used compared to the Gaussian input signal pulses.

In the following we will try to clarify this. Even though only phase noise associated with input data and recovered clock signal is considered, the regenerated data signal will contain the amplitude noise. This is because the phase noise from the input signal is converted to the amplitude noise in the regeneration process via equation (4.3). The amplitude noise is therefore inherently included in equation (4.15) expressing the power spectral density of the regenerated data signal. Since integrated jitter in Figure 4.4(b) is computed by integrating equation (4.15), the integrated jitter in Figure 4.4(b) contains both the contribution from amplitude and phase noise. Now, rectangular pulses are more efficient in limiting the transfer from the phase to amplitude noise due to their flat-top compared to the Gaussian pulses and this may be the reason why more jitter suppression is obtained when rectangular input pulses are used.

So far, we have assumed that the free-running VCO and laser, used in the clock recovery set-up, did not have any jitter. In Figure 4.5(a), integrated jitter of the regenerated data signal and the recovered clock signal is computed as a function of free-running jitter of the VCO for selected values of

laser free-running jitter (0 and 70 fs) when the pulse shaper is used. FWHM of the recovered optical clock signal is 1 ps. It is observed in Figure 4.5(a) that the regenerated data signal jitter coincides with the recovered clock signal jitter and this behavior is independent on the free-running jitter of the VCO and the laser.

Next, we want to investigate the effect of the recovered optical clock signal pulse width (FWHM) on the regenerated data signal jitter as the input data signal FWHM is varied. This is illustrated in Figure 4.5(b). The input data and the recovered optical clock signal have integrated jitter of 225 fs and 110 fs, respectively. In general, the slope of the regenerated data signal curve increases as the input data signal FWHM is decreased. Figure 4.5(b) shows that in order for the regenerated clock signal jitter to approach the recovered clock signal jitter, the requirements on the clock signal FWHM become more stringent as the input data signal FWHM is decreased. For the input data signal FWHM of 5 ps, the clock signal FWHM needs to be approximately 1ps in order to achieve the same (low) jitter values as for the recovered optical clock signal. This suggests that it may be difficult to obtain jitter reduction for high bit-rate OTDM data signals (> 100 Gb/s), where the data signal pulse width is relatively narrow. In order to analyze the properties of 3R regenerator for smaller input data signal pulse widths, the bit-rate of the input data signal is increased to 160 Gb/s and the corresponding FWHM of the data signal pulses is set to 2.8 ps.

In Figure 4.6(a), the integrated jitter of the regenerated data signal is computed as a function of input signal jitter (jitter transfer function) for various values of the recovered clock signal FWHM when the pulse shaper is used. (For simplicity, we assume the characteristic knee frequency of the laser to be sufficiently large to ensure that the jitter from the VCO is fully transferred to the laser signal). It is observed in Figure 4.6(a) that the integrated jitter of the regenerated 160 Gb/s data signal strongly depends on the FWHM of clock signal pulses in accordance with Figure 4.4(b). The integrated jitter of the regenerated optical clock signal has a linear dependency on the input signal jitter with the slope determined by clock signal FWHM. A general trend is that the effect of the regeneration is stronger for larger input signal jitter values. However, for only very short clock signal pulses (0.5 ps), the regenerated data signal jitter coincides with the recovered clock jitter. This is valid for the whole range of considered input data signal jitter.

In Figure 4.6(b), the pulse shaper is removed and the integrated jitter of the

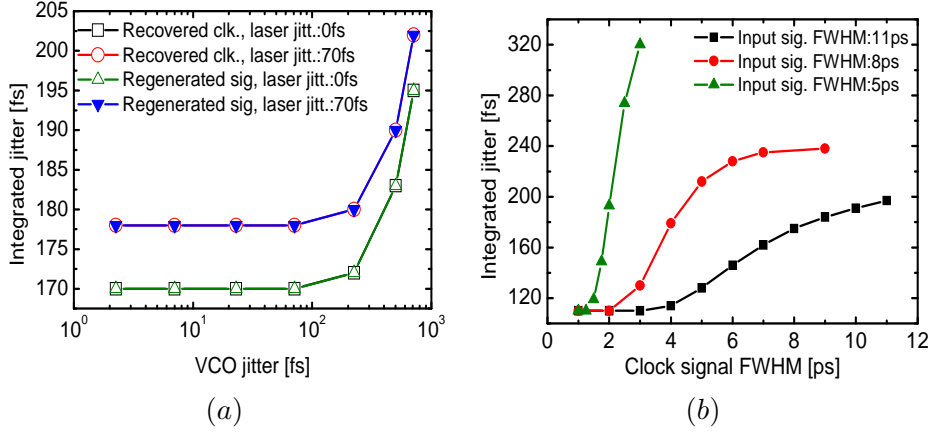


Figure 4.5: (a) Integrated jitter of the regenerated data and the recovered clock signal as a function of VCO free-running jitter for selected values of the free-running laser signal jitter when the pulse shaper is used. FWHM of the recovered optical clock signal: 1 ps. (b) Integrated jitter of the regenerated data signal as a function of clock signal pulse width for selected values of the input signal pulse width. The integrated input data and the recovered clock signal jitter is 225 fs and 110 fs, respectively.

regenerated data signal is computed as a function of the input data signal jitter. Figure 4.6(b) shows that the jitter reduction can still be obtained for the entire range of input data signal jitter; however, the reduction is very limited. Furthermore, the effect of the control signal pulse width is small and the performance improves as the FWHM is increased from 0.5 ps to 2.5 ps due to a more flat-top. This result is in accordance with Figure 4.4(b). However, it is not possible to match the jitter of the recovered clock signal and thereby obtain strong jitter reduction.

4.3 Optical demultiplexing: an introduction

In section 4.2.1, we have investigated the effects of input data and recovered clock signal timing jitter on the performance of the 3R regenerator, at 40 Gb/s and 160 Gb/s. In this and the following sections, we are going to investigate experimentally the effects of timing jitter on the optical demultiplexer at 160 Gb/s.

In order to demultiplex the high-speed signal, an ultra fast all-optical switch

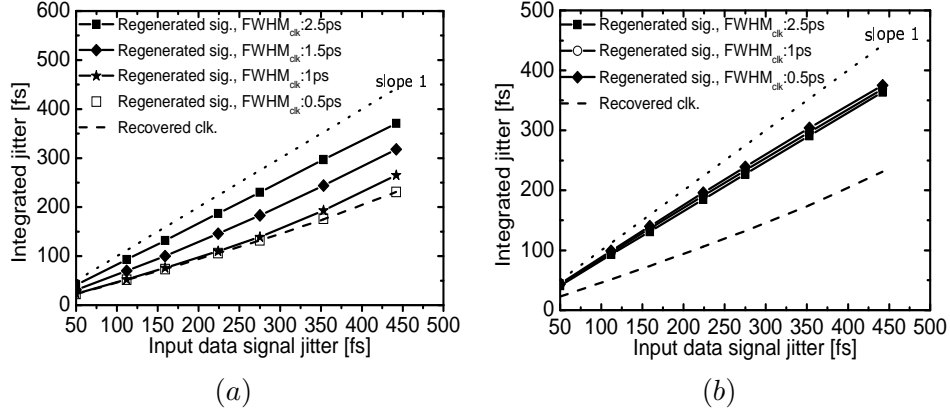


Figure 4.6: Integrated jitter of the regenerated data signal as a function of the integrated input signal data signal jitter for selected values of the clock signal pulse width. Input data signal FWHM: 2.8 ps. Input data signal bit-rate: 160 Gb/s. (a) With pulse shaper. (b) Without pulse shaper.

is needed. There are several ways to perform optical demultiplexing, either by using semiconductor [56–58] or fibre-based solutions [16–19]. Successful demultiplexing from 320 Gb/s to 40 Gb/s has been performed using semiconductor components [56]. Also, successful demultiplexing from 640 Gb/s to 10 Gb/s has been performed using fibre-based switches [17–19]. However, as the bit-rate is significantly increased fibre-based demultiplexer become a very promising solution due to their very high-speed operation. Furthermore, using fibre-based demultiplexer, the effect of ASE is avoided.

A Non-linear Optical Loop Mirror (NOLM) has on numerous occasions shown great potential for providing ultra-fast switching, due to the ultra-fast response associated with fibres [19]. As the data signal bit-rate is increased, timing jitter requirements on the data and control signal become more stringent for the demultiplexing operation [46]. It has been shown theoretically that relative timing jitter between the data and control signal can severely degrade the BER performance [46, 131]. However, the impact of timing jitter has only been investigated experimentally at the line rate of 2.5 Gb/s and 20 Gb/s, where the requirements to jitter are modest. In the following, we will investigate experimentally the effects of timing jitter on a NOLM based demultiplexer at 160 Gb/s. We will use two pulse sources for control signal generation with different timing jitter properties. Furthermore, the two pulse sources considered have different mechanism to

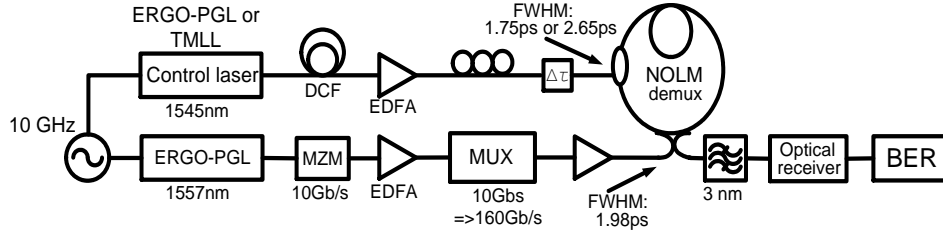


Figure 4.7: Experimental set-up used to investigate the effects of data to control signal timing jitter on NOLM switching BER performance.

obtain the synchronization to an external clock signal and this will influence the demultiplexing performance. In addition, we investigate the interplay between the control signal pulse width and timing jitter to achieve error-free performance of the system.

4.4 Experimental set-up for investigation of the impact of timing jitter

The experimental set-up used to generate a 160 Gb/s OTDM data signal and to demultiplex the data signal to 10 Gb/s is shown in Figure 4.7. The experimental set-up was built by Darko Zibar with assistance from Hans Christian H. Mulvad. The measurements were performed by Darko Zibar under guidance of Leif K. Oxenløwe.

The optical data signal is generated by a solid state modelocked Erbium Glass Oscillator Pulse Generating Laser (ERGO-PGL) at 10 GHz and 1557 nm. The ERGO-PGL uses an internal phase-locked loop in order to obtain synchronization with a reference clock. The integrated timing jitter of the ERGO-PGL transmitter laser is ~ 90 fs. The data signal pulses are externally modulated with a pseudorandom bit sequence (PRBS) with a length of $2^7 - 1$ using a Mach-Zender Modulator (MZM) and injected into a high-power EDFA. The pulses generated from the ERGO-PGL at the transmitter are approximately 2 ps wide. The 10 Gb/s data signal is then multiplexed to 160 Gb/s by a passive fibre delay polarisation and PRBS maintaining multiplexer. The 160 Gb/s data signal is additionally amplified by an EDFA to 15 dBm before being injected into the NOLM. For the control pulses required to demultiplex the 160 Gb/s signal, we have tested two different types of pulse sources: a solid state ERGO-PGL (similar to

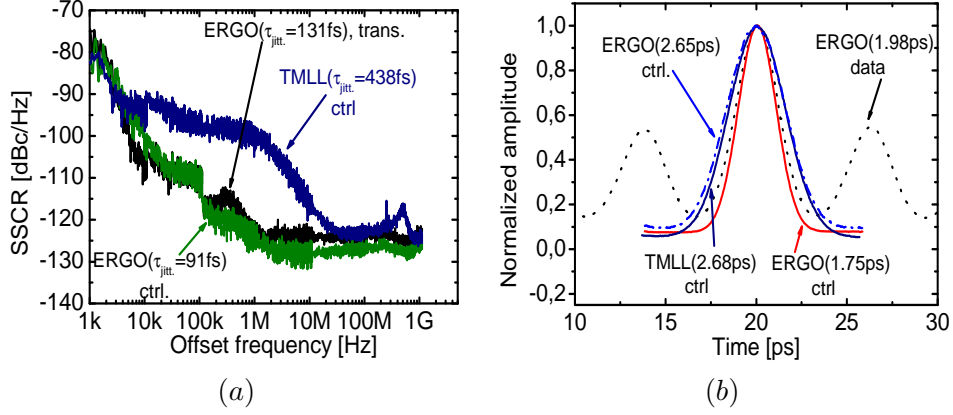


Figure 4.8: (a) Measured SSCR of the data and control signal pulse sources measured at the input of NOLM. Jitter integration range: 20 KHz-80MHz. (b) Measured autocorrelation traces of the data and control signal pulses at 160 Gb/s and 10 GHz, respectively, measured at the input of the NOLM.

the one at the transmitter) and an external cavity semiconductor Tunable Mode Locked Laser (TMLL). The ERGO-PGL and TMLL used to generate the control signal pulses have different phase noise (timing jitter) properties. The FWHM of the control signal pulses is varied (2.68 ps and 1.75 ps) in order to investigate the performance of the NOLM-gate dependency of the control signal pulse width and the timing jitter at high bit-rates. The data and control signal pulses are synchronized to the same synthesizer. The wavelength of the control signal pulses is kept constant at 1545 nm. The HNLF used in the NOLM is a 500 m commercially available fibre with a relatively flat dispersion slope (zero disp. ~ 1551 nm, slope ~ 0.017 ps/nm², $\gamma \sim 10$ W⁻¹km). Finally, after the demultiplexing, the 10 Gb/s signal is sent through a 3 nm optical band-pass filter to filter away control pulses and it is then injected into a pre-amplified 10 Gb/s receiver for BER evaluation.

4.5 Experimental signal characterization

In Figure 4.8(a), the SSCRs of the data and control signal pulse sources are shown. It is observed that the data signal SSCR curve ($\tau_{jitt.} = 131$ fs, ERGO-PGL) and the control signal SSCR ($\tau_{jitt.} = 91$ fs, ERGO-PGL) closely follow each other as expected (they are synchronized to the same clock source). The reason for the slight increase in the data signal jitter is due to multiple

EDFA amplification. Figure 4.8(a) shows that the TMLL exhibits more phase noise in the 20 kHz – 10 MHz range compared to the ERGO-PGL pulse sources. This is because the modulation bandwidth of the TMLL is relatively large (1 MHz), allowing the phase noise from the reference (synthesizer) signal to be directly transferred. Furthermore, the TMLL itself is noisy due to ASE. The ERGO-PGL lasers use an internal PLL, with a bandwidth of only 10 kHz, in order to obtain synchronization with the reference signal. In this way, the phase noise contribution from the reference signal is filtered away. Furthermore, the ERGO-PGL itself has low noise at frequencies exceeding the PLL bandwidth. By using the ERGO-PGL and TMLL as control signal pulse sources, the impact of excess phase noise in the range from 20 kHz – 10 MHz is investigated. In Figure 4.8(b), autocorrelation traces of the data signal together with the control signal pulses are portrayed. Clean and smooth autocorrelation traces of the 160 Gb/s data signal pulses with FWHM of 1.98 ps (deconvolved pulse) are observed. Figure 4.8(b) also contains the autocorrelation traces for the ERGO-PGL control signal laser source with a FWHM of 2.65 ps (deconvolved pulse) and 1.75 ps (deconvolved pulse) together with the TMLL with FWHM of 2.68 ps (deconvolved pulse). By using short pieces of Dispersion Compensating Fibre (DCF), we were able to tune the pulse widths of the ERGO-PGL and TMLL.

4.6 Experimental investigation of ERGO-PGL and TMLL as a control pulse source for NOLM

In order to exclude the influence of the control signal pulse width, the pulse width of the ERGO-PGL and the TMLL are matched to approximately 2.65 ps, see Figure 4.8(b).

Figure 4.9 shows the measured switching windows, (integrated power of the demultiplexed signal as the relative time delay, τ , (Figure 4.7), between the data and control signal is varied), when the ERGO-PGL and the TMLL are used. It is observed that the width of the switching windows are nearly the same. The average power of the control pulses, i.e. the ERGO-PGL and the TMLL, was set to 22 dBm. Figure 4.10(a) shows BER curves for the 160 Gb/s to 10 Gb/s demultiplexed signal in the two cases. In Figure 4.11(a), the receiver sensitivity of all the 16 channels is shown when the ERGO-PGL control pulse source is used. All channels are error-free with an average sensitivity of -37.5 dBm. The corresponding receiver sensitivity is -28.3 dBm when the TMLL is used for control signal generation;

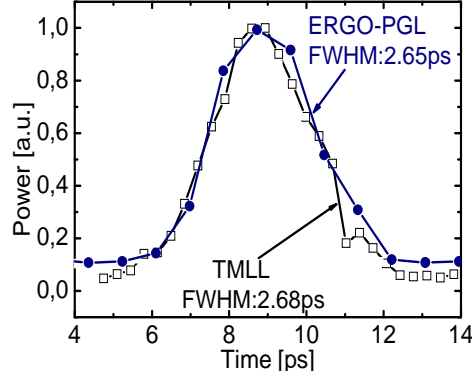


Figure 4.9: Measured switching windows when the ERGO-PGL and TMLL are used as control pulse sources..

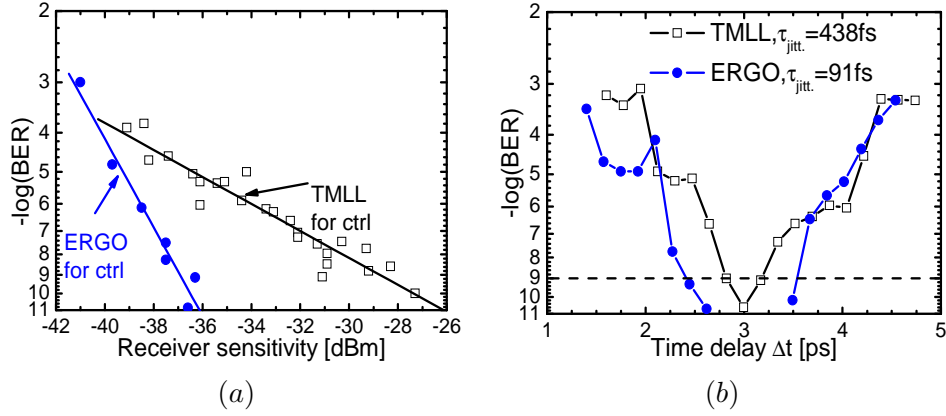


Figure 4.10: (a) Measured BER curves for the demultiplexed 10 Gb/s signal. FWHM of the controls signals: 2.65ps. (b) BER as a function of relative time delay between data and control signal pulses.

see Figure 4.10(a). Since the autocorrelations and the measured switching windows are nearly identical when the ERGO-PGL or the TMLL are used, the results suggest that the pulse shape differences should not influence the measurements. Moreover, if we look at the tails of the switching windows (or the autocorrelations), it is observed that these are slightly higher when the ERGO-PGL is used. This indicates that the TMLL has a higher extinction ratio than the ERGO-PGL. The big difference in BER between

the ERGO-PGL and the TMLL can therefore be contributed to the higher timing jitter of the TMLL. The TMLL with 438 fs timing jitter results in approximately 9 dB penalty compared to the ERGO-PGL laser with 91 fs timing jitter. In Figure 4.10(b), BER as a function of relative time delay, τ , is plotted for the ERGO-PGL and the TMLL used as control pulse sources. The power of the demultiplexed signal, for which the measurement was performed, was the power of the demultiplexed signal for which a BER of 10^{-9} is obtained plus an additional 3 dB. It is observed that when the timing jitter is 91 fs we have a time-offset margin of 1.4 ps for the error free performance (BER= 10^{-9}) compared to 0.34 ps when the timing jitter is 438 fs. Thus, as the timing jitter in the 20 kHz – 10 MHz range is increased, the requirement for the synchronization between data and control signal pulses, in order to obtain error free operation increases. In real transmission systems, this would mean that the clock recovery bandwidth would have to be increased in order to limit the effect of time jitter. However, increasing the clock recovery bandwidth in the presence of timing jitter (noise) may compromise its stability.

4.7 Experimental investigations for different Full Width Half Maximum

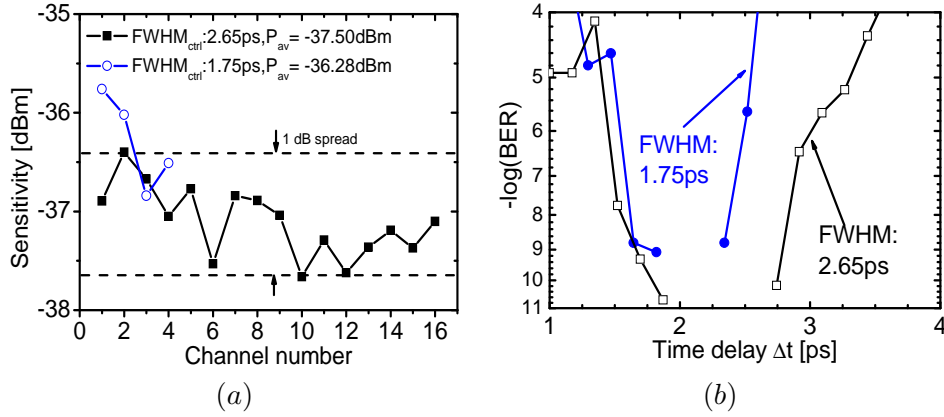


Figure 4.11: (a) The ERGO-PGL is used for control signal generation and the FWHM is 2.65ps and 1.75ps, respectively. (a) Measured sensitivity. (b) Measured BER as a function of relative time delay between data and control signal pulses.

In this section, the ERGO-PGL is used for control signal generation. The

system performance, in the presence of timing jitter, is investigated as the control signal pulse width is decreased. The FWHM of the control signal pulses takes values of 2.65 ps and 1.75 ps. The receiver sensitivity of the 160 Gb/s to 10 Gb/s demultiplexed data signal is shown in Figure 4.11(a) for the FWHM of 2.65 ps and 1.75 ps, respectively. The corresponding receiver sensitivity is -37.5 dBm and -36.3 dBm, respectively. Furthermore, BER curves have been measured as a function of relative time delay, τ , between data and control signal pulses, see Figure 4.11(b). It is observed that as the FWHM of the control pulses is decreased from 2.65 ps to 1.75 ps, the time delay tolerance, in order to obtain a BER of 10^{-9} , decreases from 1.10 ps to 0.50 ps. This is because the FWHM of the data signal pulses is 1.98 ps and for the narrow control data signal pulse width of 1.75 ps, the effects of timing jitter become more severe due to the almost equal pulse widths. As the control signal pulse width is increased to 2.65 ps, the control signal pulses are broader than the data signal pulses, thus overlapping all the time irrespective of the jitter. The switching (demultiplexing) of the data signal pulses is then less affected by the control signal timing jitter. However, the control signal pulse width should not become too large in order to avoid the crosstalk from the neighboring channels.

4.8 Summary

A brief review of regeneration schemes in optical communication systems was given in this chapter. The advantages and disadvantages of the conventional (electrical) and optical regeneration schemes have been discussed. Furthermore, we derived a novel analytical expression for the power spectral density spectrum of regenerated data signal in the presence of clock recovery. The analytical expression is very useful in evaluating the performance of the optical regeneration (retiming) scheme. We showed that for flat-top data signal pulses and sufficiently narrow optical clock signal pulses, the timing jitter of the regenerated optical data signal coincided with the jitter of the recovered optical clock signal. This behavior seemed to be independent of the free-running jitter of the VCO and the laser in the clock recovery.

We also saw that the requirements for the clock signal pulse width became more stringent as the data signal pulse width was decreased. Moreover, for Gaussian data signal pulses timing jitter reduction could still be obtained, however, in much less extent compared to when the data signal pulses have flat-top. The impact of timing jitter on a 160 Gb/s demultiplexer was also

investigated. It was shown that the excess timing jitter in the frequency range from 20 kHz to 10 MHz is of great importance; an increase from 91 fs to 438 fs leads to a penalty of 9 dB and decreases the allowable time misalignment between the data and control signal pulses, by approximately 1 ps. Furthermore, the impact of timing jitter could be reduced if the control signal pulses are broader than the data signal pulses.

Chapter 5

Novel Phase-Locked Coherent Demodulator with Feedback and Sampling

This chapter is commenced with a brief introduction of Radio-over-Fibre (RoF) systems. We introduce and describe link parameters which are typically used to characterize the performance of RoF optical links. Thereafter, a review of RoF links is given with the emphasis on best link performance.

A novel phase-locked coherent optical phase demodulator with feedback and sampling, for phase modulated RoF optical links, is thereafter presented and investigated. A new time-domain numerical model is developed and compared with measurements. Using the model, we investigate how loop gain, amplitude and timing jitter, tracking LO phase-modulator non-linearities and amplitude modulation influence the Signal-to-Intermodulation Ratio (SIR) and Signal-to-Noise Ratio (SNR) of the demodulated signal. The induced penalty by the optical sampling, in terms of the SIR, is also investigated using the developed numerical model. Furthermore, in order to alleviate non-linearities associated with the tracking LO phase-modulator, we report a novel cancellation technique.

Experimental results, for the two-tone measurement, for the proposed optical phase demodulator with feedback and sampling are also presented. Measured dynamic ranges of the phase demodulator with and without sampling are compared.

The entire work presented in this chapter was performed at the University of California, Santa Barbara.

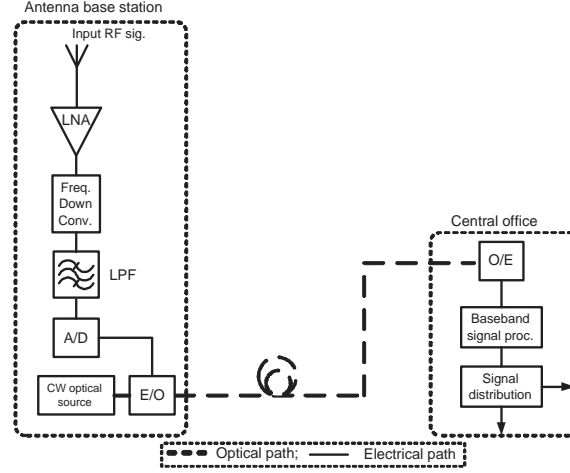


Figure 5.1: Broadband network architecture combining fixed and wireless transport. LNA: low-noise amplifier, LPF: low-pass filter, A/D: analog-to-digital-converter, CW: continuous wave E/O: electro-to-optical conversion, O/E: optical-to-electrical conversion.

5.1 Radio-over-Fibre systems: introduction

In this section, a brief introduction to RoF systems is given. We want to emphasize that we only concentrate on the downlink, i.e. signal transport from the antenna base station to the central office. A typical broadband network architecture combines wireless and fixed signal transport infrastructure by using optical fibres to connect antenna base stations with the central office, see Figure 5.1 [25]. It is common practice today to use electronics to digitalize the received wireless RF signal directly at the antenna base station and then to transport it over an optical fibre link to the central office, see Figure 5.1. This approach is called baseband-over-fibre [132]. The received wireless RF signal, in Figure 5.1, is first passed through a low-noise amplifier (LNA), frequency downconverted, low-pass filtered and then converted into digital domain using an analog-to-digital (A/D) converter. The A/D converter is then followed by an electro-optical (E/O) modulation device which imposes the digitalized wireless signal onto an optical carrier.

However, as the carrier frequencies of the wireless signals increase beyond ~ 10 GHz, digitalization becomes complex and troublesome [133]- [135]. It becomes very challenging to support broadband operation with a high

dynamic range and low noise properties at high carrier frequencies [133]-[135]. In addition, emerging electronics operating at high carrier frequencies suffer from high power consumption and temperature stability problems [134]-[135]. A simple antenna base station is mandatory to reduce cost and overall network maintenance, especially in the case of high carrier frequency millimeter-wave wireless systems, where a large number of antenna base stations will be deployed [26].

Instead of performing signal digitalization at the antenna base station, a more straightforward approach is to directly impose the received wireless RF signal onto an optical carrier and transmit it via an optical link to the central office; this is referred to as radio-over-fibre [25, 27, 132]. In this way, low-noise amplification, frequency downconversion and A/D conversion is avoided at the antenna base station. The goal of RoF systems is therefore to replace low-noise amplification, frequency conversion and analog-to-digital conversion with an optical link that has these functionalities.

Radio-over-fibre systems are finding some commercial deployment, and this approach simplifies significantly the antenna base station because complex signal digitalization and processing is performed at the central office [27, 136]. All electronics and analog-to-digital converters are thereby placed at the central office instead of the antenna base station. A concentration of complex and expensive equipment in the central office brings several advantages such as reduced cost and space requirements. It also becomes easier to provide the power and temperature stability to the emerging high performance electronics and analogue-to-digital converters. Moreover, subsequent network upgrades, control, management and maintenance can then easily be done from the central office rather than at each individual antenna base station [26, 27, 132].

5.2 Link parameters

In Figure 5.2, we have depicted a RoF optical link. The wireless RF signal can be imposed onto an optical carrier by directly modulating an optical source or by using an external modulator. A complete RoF optical link involves conversion of wireless RF signal into the optical domain, transport of the signal and conversion to the electrical domain.

It has become common practice to measure the performance of RoF optical links using the same parameters that are used to characterize RF components, namely [28]:

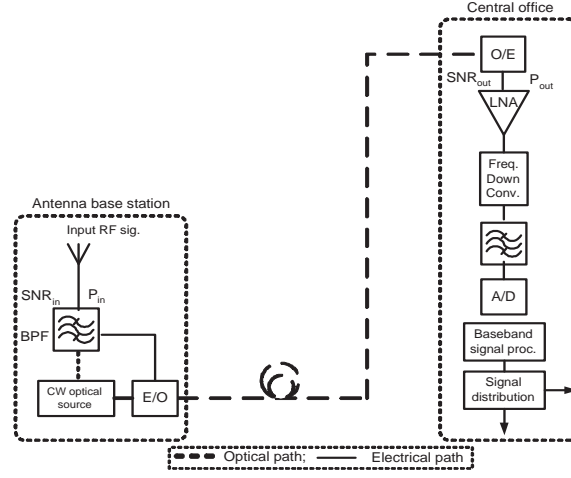


Figure 5.2: Radio-over-fibre optical link. BPF: band-pass filter.

- Link gain
- Noise Figure (NF)
- Spur-free dynamic range (SFDR)

5.2.1 Link gain

The link gain is an important parameter in setting the overall performance of the optical link. It is important to keep the link gain sufficiently high, otherwise the benefit of low-loss fibre transmission is lost [137]. The link gain is defined as the ratio between the RF power delivered to a matched load at the receiver, P_{out} , and the power of the input RF signal, P_{in} . The link gain of RoF optical link can be expressed as [28]:

$$g = \frac{P_{out}}{P_{in}} = \alpha s_{md}^2 r_d^2 \frac{R_L}{R_{md}} \quad (5.1)$$

where α is the loss of the optical fibre, s_{md} is the slope efficiency of the modulation device and r_d is the responsivity of the detection device. R_L and R_{md} are the link's output load and modulation device resistances, respectively. In practice, it is possible to obtain the same values for the modulation and the load resistance, i.e. $R_L = R_{md}$. There are no fundamental limits that set a minimum or maximum link gain. The link gain is

set by the limits of the slope efficiency of the modulation device and the responsivity of the detection device. Simply, if the link gain is less than 1 the link acts as an attenuator and if the link gain exceeds 1 it works like an RF amplifier.

5.2.2 Noise figure

Noise is an important parameter of the RoF optical link setting the limit on the available dynamic range, see section 5.2.3. The effect of noise on the system performance can be expressed using NF. The NF is a measure of the degradation in the signal-to-noise ratio between the input and output of the link and is defined as the ratio between the signal-to-noise ratio at the input of the link, SNR_{in} , and the signal-to-noise ratio at the output of the link, SNR_{out} in Figure 5.2 [138]:

$$NF = \frac{SNR_{in}}{SNR_{out}} = \frac{P_{in}}{\sigma_{in}^2} \cdot \frac{(\sigma_{in}^2 + \sigma_{add}^2)}{P_{out}} = \left(1 + \frac{\sigma_{add}^2}{k_B T \Delta f g} \right) \quad (5.2)$$

where g is the link gain defined in equation (5.1) and σ_{in}^2 represents the input noise power expressed as: $\sigma_{in}^2 = k_B T \Delta f$, where k_B is a Boltzmann constant, T is the absolute temperature and Δf is the noise bandwidth. σ_{add}^2 is the additive noise term originating from laser and photodetector (shot and thermal) noise.

5.2.3 Spur-free dynamic range

The dynamic range is an important parameter for the link design. It is the only link parameter that cannot be improved by adding an amplifier before or after the link. The dynamic range is especially an important parameter in analog systems (e.g. antenna remoting for commercial and military applications) and applications where many RF sub-carriers are involved (e.g. distribution of cable television signals (CATV) and systems employing Orthogonal Frequency Division Multiplexing (OFDM)).

It is a well known fact that for sufficiently high input power/voltage levels, the output of a system or device will begin to saturate. This means that the output power/voltage will no longer increase linearly as the input power/voltage is increased, i.e. the system exhibits a non-linear behavior. The output of a system, $v_{out}(t)$, having a non-linear response can be written in Taylor series as follows [138]:

$$v_{out}(t) = a_0 + a_1 v_{in}(t) + a_2 v_{in}^2(t) + a_3 v_{in}^3(t) \dots \quad (5.3)$$

where $v_{in}(t)$ is the input signal to the system and a_0, a_1, a_2 and a_3 are constants. Ideally, we would like to have a linear link with the linear term, $a_1 v_{in}(t)$, therefore providing the desired response. In order, to illustrate the effect of system non-linearities let's assume that the input signal $v_{in}(t)$ consists of two closely spaced frequencies ω_1 and ω_2 (the so-called two-tone test):

$$v_{in}(t) = a \cos[\omega_1 t] + b \cos[\omega_2 t] \quad (5.4)$$

where a and b are constants with equal powers. If the signal $v_{in}(t)$ in equation (5.4) is inserted in equation (5.3), the frequency spectrum of $v_{out}(t)$ will consist of all harmonics on the form $n\omega_1 + m\omega_2$ where m and n may be positive or negative integers [138]. The order of a given product is then defined as $|n| + |m|$. Harmonics at frequencies $2\omega_1, 2\omega_2, \omega_1 - \omega_2$ and $\omega_1 + \omega_2$ will originate from the second order non-linearity $v_{in}^2(t)$ and are therefore called second-order products. Second order products are typically far away from the fundamental frequency components ω_1 and ω_2 and can be easily filtered away.

The $v_{in}^3(t)$ term will lead to third order products such as $3\omega_1, 3\omega_2, 2\omega_1 + \omega_2$ and $2\omega_2 + \omega_1$, however, these terms are also far away from the fundamental frequency components and can thereby also be filtered away. On the other hand third order products $2\omega_1 - \omega_2$ and $2\omega_2 - \omega_1$, also originating from the $v_{in}^3(t)$ term, fall within the bandwidth of interest and cannot be filtered away. The $2\omega_1 - \omega_2$ and $2\omega_2 - \omega_1$ terms are called: the 3rd order intermodulation products and are especially important since they may limit the dynamic range or bandwidth of the system. Higher order terms in equation (5.3) may also contribute to such harmonics, but usually the most significant contribution is from the $v_{in}^3(t)$ term.

In Figure 5.3, a so called intercept diagram is shown. The intercept diagram illustrates how the output power at the fundamental frequency component (ω_1 or ω_2) and 3rd order intermodulation product ($2\omega_2 - \omega_1$ or $2\omega_1 - \omega_2$) depends on the input power. A measure of 3rd intermodulation product is given by the intercept points which are the input/output power levels where the output power of the fundamentals equals the 3rd order intermodulation products. The Spur-Free Dynamic Range (SFDR) is defined, in this report, as the ratio between the power of the fundamental frequency components and the 3rd order intermodulation products, when the power of the 3rd

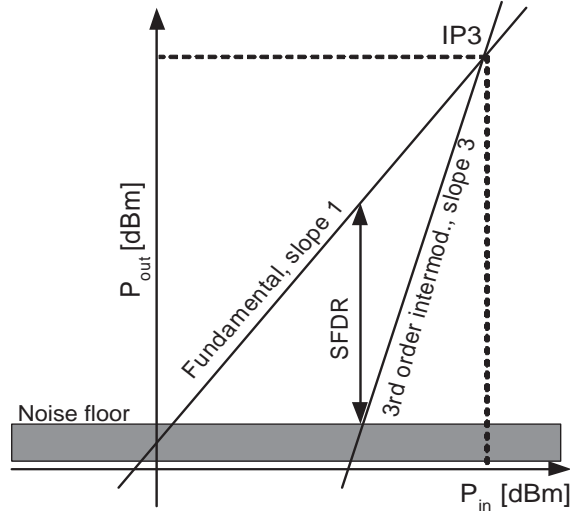


Figure 5.3: The output power of the fundamental frequency components, the 3rd order intermodulation product and the noise floor as a function of input power.

order intermodulation product equals the noise floor, see Figure 5.3. It can be observed in Figure 5.3 that by increasing the noise floor and the 3rd order intermodulation products, the SFDR will decrease. The Signal-to-Intermodulation Ratio (SIR) is defined as the ratio between the power in the fundamental frequency component (ω_1 or ω_2) and the 3rd order intermodulation product ($2\omega_2 - \omega_1$ or $2\omega_1 - \omega_2$).

5.3 Review of Radio-over-Fibre systems

Until now, RoF optical links for signal transmission from the antenna base station to the central office have mostly employed intensity modulation (IM) of the optical carrier [28]. Intensity modulated RoF optical links can be divided into: directly modulated and externally modulated RoF optical links. In the directly modulated RoF optical links, the high-frequency wireless RF signal directly modulates the current of the laser. For the externally intensity modulated radio-over-fibre links, the laser is connected to the external intensity modulator. The high-frequency wireless RF signal is then used to modulate the external intensity modulator which then modulates the light injected by the laser. For frequencies beyond 10 GHz, externally intensity modulated RoF optical links offer an improvement in

terms of link gain, noise figure and dynamic range compared to directly modulated RoF optical links [28]. However, externally intensity modulated RoF optical links have been limited in performance by the non-linear response of the optical modulators. The underlying reason for this is that the response of the optical intensity modulators is 'hard-limited' by zero and full transmission.

The highest reported link gain for an externally intensity modulated RoF optical link operating at frequencies beyond 10 GHz is -16 dB at a frequency of 15 GHz [28]. Moreover, the best noise figure reported so far is 12 dB for a carrier frequency of 8 GHz [28]. For broadband communication systems, an estimated noise figure of around 3 dB is required in order to achieve a targeted bit-error rate of 10^{-6} [25]. State-of-the-art externally intensity modulated links, assisted by optical or electrical linearization schemes, have demonstrated a spurious-free dynamic range of $115 \text{ dB}\cdot\text{Hz}^{2/3}$ (in 1 Hz bandwidth) at a frequency of 15 GHz. However, as the RF frequency is increased the linearization becomes more complex especially over a large bandwidth. The ability to handle wide-dynamic ranges is very important since it may result in higher system capacities. Furthermore, spurious-free dynamic range requirements exceeding $115 \text{ dB}\cdot\text{Hz}^{2/3}$ seem reasonable for receivers that deal with interference [137].

In most cases of radio-over-fibre systems employing intensity modulation, simple direct-detection is used for the detection of optical signals. The combined effects of fibre dispersion and the direct detection technique will result in power fading of the received optical signal, thus limiting the transmission distance [26]. As the frequency is increased the effect of power fading significantly reduces the link length and is a major problem. However, it should be emphasized that if the coherent receiver is used for signal detection, the effect of power fading does not occur [26].

Recently, radio-over-fibre optical links employing optical phase-modulation at the antenna base station have emerged [29]- [37]. An immediate advantage of optical phase modulation over optical intensity modulation is that the process of imposing a high frequency wireless signal on the optical carrier is inherently linear when conventional phase-modulators are used. The phase modulation thus has no fundamental limit on the dynamic range. This aspect also tends to improve the noise figure and link gain since higher power levels of the wireless RF signals can be tolerated [36,38]. In addition, the operational frequency of the optical phase-modulators can be extended up to $\sim 100 \text{ GHz}$ range [39]. A traditional coherent receiver, typically used

to demodulate a phase-modulated optical signal, has a sinusoidal (non-linear) response. This non-linear response will therefore limit the dynamic range and the benefits of the linear optical phase-modulation are lost. In order to preserve the benefits of a phase-modulated radio-over-fibre optical link, the main challenge lies in the implementation of a high-frequency linear coherent receiver.

The two dominating approaches reported so far for realizing a linear coherent receiver are based on: 1) optical phase locked loop for linear phase tracking [29]- [35], 2) digital linearization techniques [36]- [37].

The digital linearization techniques reported in [36]- [37] use electronics to perform the linearization and thereby do not require a feedback loop. The loop delay limitations are thereby avoided. However, only non-linearities associated with a single device (in this case the sinusoidal response of the coherent receiver) can be compensated [36]. Nonlinearities associated with the photodiodes and electronics are thereby not compensated and this results in a lower dynamic range, higher noise figure and lower link gain [36]. Furthermore, this approach is limited by the speed, linearity and noise characteristics of the analogue-to-digital converters and electronics. Successful operation of digital linearization techniques has been demonstrated at frequencies up to 300 MHz with limited improvement in the linearity [36]- [37]. A satisfactory performance beyond 10 GHz seems very challenging to achieve.

In this chapter, we focus on a linear coherent receiver for phase modulated radio-over fibre optical links employing phase-locked loop for linear phase tracking. A novel coherent phase-locked optical demodulator with feedback and sampling is proposed and presented in Figure 5.4. Two types of loops are considered, i.e. a baseband and a sampling loop. The only difference between the baseband and the sampling loop is that for the baseband loop a CW optical source is used and for the sampling loop a pulsed optical source is used. The targeted operation frequency of the baseband loop is up to 2 GHz. However, in order to operate the phase demodulator at frequencies approaching 20 GHz, the baseband loop bandwidth would need to be very large to obtain a high degree of linearity. A loop operating at 20 GHz would require > 100 GHz of loop bandwidth. Considering the delay in the feedback loop and the challenges of making electronics operate beyond 100 GHz this is far beyond feasible. To overcome this problem, a novel approach using optical sampling at the demodulator is proposed and investigated [31,32].

In Figure 5.4, we observe that the output from the phase demodulator (a balanced optical mixer) is amplified and filtered by a loop filter, and then fed back to a tracking LO phase-modulator. Within the loop bandwidth, the effect of the feedback is to reduce the difference in phase between the local optical wave and the incoming wave. Therefore, the effective swing across the phase demodulator is reduced due to the feedback loop, forcing the demodulator to operate in the linear regime, and thus resulting in an improved SFDR. This reduction could also be obtained by reducing the modulation depth at the transmitter but the SNR is reduced as a consequence. In contrast, in the proposed optical phase demodulator, both the signal and the noise swings are reduced by the same factor (loop gain) retaining the SNR while improving the SFDR as shown in [29,31]. However, to achieve a high bandwidth phase-locked receiver, compact semiconductor phase-modulators have to be used to keep the loop delay sufficiently low. These modulators can have a fairly non-linear response limiting significantly the dynamic range of the receiver [139]. Furthermore, the modulator distortion usually dominates over the photodiode distortion. Compensating for the modulators non-linearities is therefore of significant importance [28].

5.4 Mathematical model of phase-locked demodulator

5.4.1 Baseband loop

The set-up of the phase-locked optical demodulator, on which we base our model, is shown in Figure 5.4. The demodulator can either be operated as a baseband loop (a CW optical source is used) or a sampling loop (a pulsed optical source is used). Due to simplicity, we start by considering the baseband loop. The received RF signal, $V_{in}(t)$, is used to directly modulate an optical phase-modulator at the antenna base station. The corresponding optical signal $E_{in}(t)$, see Figure 5.4, is then written in complex notation as¹:

$$E_{in}(t) = \sqrt{P_{in}} e^{j(\omega_0 t + \phi_{in}(t))} \quad (5.5)$$

where ω_0 is the optical frequency and P_{in} is the power of the optical field. Taking into consideration the non-linearities associated with the (input) phase-modulator, located at the antenna base station, the phase of the optical signal, $\phi_{in}(t)$, is expressed as:

¹The scalar notation is used for both $E_{in}(t)$ and $E_{LO}(t)$ by assuming that the two fields are polarized identically.

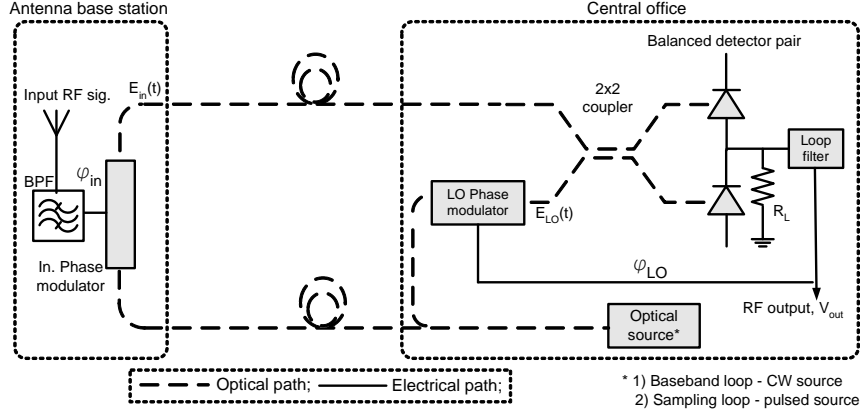


Figure 5.4: General outline of phase-modulated optical link and phase-locked optical demodulator at the receiver unit.

$$\phi_{in}(t) = \frac{\pi V_{in}(t)}{V_{\pi, in}} \left(1 + \frac{a_2}{a_1 V_{\pi, in}} V_{in}(t) + \frac{a_3}{a_1 V_{\pi, in}^2} V_{in}^2(t) \right) \quad (5.6)$$

where $V_{\pi, in}$ is the voltage of the input phase-modulator, in order to obtain π phase shift and a_1, a_2 and a_3 represent the terms of the polynomial expansion of the input phase-modulator non-linear phase response. In order to characterize the dynamic range of the demodulator, the input RF signal $V_{in}(t)$ is assumed to consist of relatively closely spaced tones [138]:

$$V_{in}(t) = V_1 \sin[\omega_1 t] + V_2 \sin[\omega_2 t] \quad (5.7)$$

where V_1 and V_2 are the amplitudes of the input RF signals and ω_1 and ω_2 are the input RF signal frequencies. The optical signal $E_{in}(t)$ is then transported to the central office (receiver unit) where its phase, is compared to the phase of the local optical signal $E_{LO}(t)$, using the balanced detector pair with load resistance R_L . A single optical source is used for both the antenna base station and the central office. The optical LO signal, $E_{LO}(t)$, is thereby expressed as:

$$E_{LO}(t) = \sqrt{P_{LO}(t)} e^{j(\omega_0 t + \phi_{LO}(t))} \quad (5.8)$$

where $\phi_{LO}(t)$ is the reference phase (signal) and is a function of the feedback loop parameters, see Figure 5.4, and P_{LO} is the power of the optical field².

²Due to the residual amplitude modulation of the tracking LO phase-modulator P_{LO} will be time dependent. This is explained in more details later in the text

Following Figure 5.4 after the 3-dB coupler, we have in one arm:

$$E_1(t) = \frac{1}{\sqrt{2}}\sqrt{P_{in}}e^{j(\omega_0 t + \phi_{in}(t) - \pi/2)} + \frac{1}{\sqrt{2}}\sqrt{P_{LO}(t)}e^{j(\omega_0 t + \phi_{LO}(t) - \pi)} \quad (5.9)$$

The output of the second arm is:

$$E_2(t) = \frac{1}{\sqrt{2}}\sqrt{P_{in}}e^{j(\omega_0 t + \phi_{in}(t) - \pi)} + \frac{1}{\sqrt{2}}\sqrt{P_{LO}(t)}e^{j(\omega_0 t + \phi_{LO}(t) - \pi/2)} \quad (5.10)$$

An ideal coupler has been assumed in equations (5.9) and (5.10), i.e. equal splitting ratios. Taking into account the non-linearities associated with the photodetectors, the photocurrents generated in each branch of the balanced receiver, $I_1(t)$ and $I_2(t)$, containing the phase difference between $\phi_{in}(t)$ and $\phi_{LO}(t)$ are then expressed as:

$$\begin{aligned} I_1(t) &= R_{pd}|E_1(t)|^2 \left(1 + \frac{b_2|E_1(t)|^2}{b_1} + \frac{b_3|E_1(t)|^3}{b_1} \right) \\ &= R_{pd} \sum_{n=1}^3 \frac{b_n}{b_1} \left(\frac{1}{2}P_{in} + \frac{1}{2}P_{LO}(t) \right. \\ &\quad \left. - \sqrt{P_{in}P_{LO}(t)} \sin[\phi_{in}(t) - \phi_{LO}(t)] \right)^n \end{aligned} \quad (5.11)$$

$$\begin{aligned} I_2(t) &= R_{pd}|E_2(t)|^2 \left(1 + \frac{b_2|E_2(t)|^2}{b_1} + \frac{b_3|E_2(t)|^3}{b_1} \right) \\ &= R_{pd} \sum_{n=1}^3 \frac{b_n}{b_1} \left(\frac{1}{2}P_{in} + \frac{1}{2}P_{LO}(t) \right. \\ &\quad \left. + \sqrt{P_{in}P_{LO}(t)} \sin[\phi_{in}(t) - \phi_{LO}(t)] \right)^n \end{aligned} \quad (5.12)$$

where R_{pd} is the responsivity of the photodetectors and is assumed equal for both photodetectors. b_n represent the terms of the polynomial expansion of the non-linear response of the photodiodes and n is an integer. In practise, it is only necessary to consider $n = 1, 2$ and 3 . The output signal from the balanced photodetector pair with load resistance R_L contains the phase difference between $\phi_{in}(t)$ and $\phi_{LO}(t)$ and is expressed as:

$$V_{pd}(t) = R_L(I_2(t) - I_1(t)) \quad (5.13)$$

The signal $V_{pd}(t)$ is then used to control the feedback loop. (After the loop has acquired a lock, the phase difference $\varphi_{in}(t) - \varphi_{LO}(t)$ will approach zero.) The signal $V_{pd}(t)$ is then passed through the loop filter (low pass) and amplified:

$$\frac{dV_{out}}{dt} = \left[A \frac{V_{pd}(t) - V_{out}(t)}{\tau_{LF}} \right] \quad (5.14)$$

where $\tau_{LF} = 1/2\pi f_{LF}$ is inversely proportional to the bandwidth of the loop filter and A is the gain of the loop filter. $V_{out}(t)$ is the output of the loop filter and the desired demodulated RF signal. $V_{out}(t)$ is then applied to the tracking LO phase-modulator. The phase vs. voltage characteristic of the LO phase-modulator is non-linear. In practice, for the semiconductor phase-modulators the quadratic and cubic non-linearity terms will dominate over the higher order terms. The phase-voltage relation can thus be expressed as:

$$\phi_{LO}(t) = \frac{\pi V_{out}(t)}{V_{\pi, LO}} \left(1 + \frac{c_2}{c_1 V_{\pi, LO}} V_{out}(t) + \frac{c_3}{c_1 V_{\pi, LO}^2} V_{out}^2(t) \right) \quad (5.15)$$

where c_1, c_2 and c_3 represent the terms of the polynomial expansion of the LO phase-modulator response. In addition to the non-linearity associated with phase vs. voltage characteristic of the LO phase-modulator, any residual amplitude modulation, as would be expected in practice, may affect the performance of the demodulator in an adverse way. The normalized E-field amplitude of the (optical) LO signal can therefore be expressed as:

$$\sqrt{\frac{P_{LO}(t)}{P_0}} = \frac{A_{LO}(t)}{A_0} = 1 + D_1 V_{out}(t) + D_2 V_{out}^2(t) + D_3 V_{out}^3(t) \quad (5.16)$$

where A_0 is the E -field amplitude of the LO signal in the absence of amplitude modulation. In order to determine the overall dynamical response of the loop, the total phase error is defined as:

$$\phi_e(t) = \varphi_{in}(t) - \varphi_{LO}(t) \quad (5.17)$$

Taking the derivative of equations (5.17), (5.15) and (5.6), we obtain the differential equation describing the total phase error in the loop expressed as:

$$\begin{aligned}
\frac{d\phi_e}{dt} = & \frac{\pi}{V_{\pi,in}}(\omega_1 V_1 \cos[\omega_1 t] + \omega_2 V_2 \cos[\omega_2 t]) \\
& \cdot \left(1 + \frac{2a_2}{a_1 V_{\pi,in}}(V_1 \sin[\omega_1 t] + V_2 \sin[\omega_2 t]) \right. \\
& + \left. \frac{3a_3}{a_1 V_{\pi,in}^2}(V_1 \sin[\omega_1 t] + V_2 \sin[\omega_2 t])^2 \right) \\
& - \frac{A\pi}{\tau_{LF} V_{\pi,LO}} \left(1 + \frac{2c_2}{c_1 V_{\pi,LO}} V_{out}(t) + \frac{3c_3}{c_1 V_{\pi,LO}^2} V_{out}^2(t) \right) \\
& \cdot (V_{pd}(t) - V_{out}(t))
\end{aligned} \tag{5.18}$$

It should be noted that equation (5.18) includes the effects of cascaded sources of non-linearities associated with the input phase-modulator, the sinusoidal response of the balanced receiver, the photodetectors and the tracking LO phase-modulator, giving a good insight into demodulators dynamics. We are therefore going to base our model on equations (5.18) and (5.14). By solving equation (5.14) together with equation (5.18), the desired demodulated signal, $V_{out}(t)$, is obtained characterizing the overall non-linear response of the loop. Equations (5.18) and (5.14) are first order non-linear differential equations and their solutions can be obtained numerically. Loop gain is defined as: $K = (\pi\sqrt{P_{in}P_0}AR_{pd}R_L)/V_{\pi,LO}\tau_{LF}$.

When the input RF signal consists of relatively closely spaced frequencies, the non-linear response of the loop components will result in intermodulation distortion of the demodulated signal. The 3rd order intermodulation products are especially important because they may set the SFDR of the system as explained in section 5.2.3. In order to evaluate the performance of the phase demodulator, the demodulated signal, $V_{out}(t)$, will be characterized by the SIR (defined in section 5.2.3).

5.4.2 Linearity analysis based on perturbation theory

The time domain numerical model, based on the non-linear differential equations (5.18) and (5.14), is of a rather complex nature and there are many parameters involved. The model is therefore highly detailed and in good agreement with experimental results, as it will be shown in section 5.5. However, due to the complex behavior of the non-linear systems, it may be cumbersome to interpret the results. In this section, we therefore derive simple approximate analytical expressions which are qualitatively in

good agreement with the time domain numerical model. The analytical expressions will be used to interpret results obtained by the detailed time domain numerical model. For simplicity, the loop filter and non-linearities associated with the photodetectors are not considered. Furthermore, V_π is assumed to be the same for the input and the tracking LO phase-modulator.

Let $V_{ref}(t)$ denote the signal incident at the tracking LO phase-modulator when the loop is open:

$$\begin{aligned}
V_{ref}(t) &= 2R_{pd}\sqrt{P_{in}P_{LO}}R_L A \sin\left[\frac{\pi}{V_{\pi,LO}}V_{in}(t)\right. \\
&\quad \left.- \frac{\pi}{V_{\pi,LO}}\left(V_{out}(t) + c'_2V_{out}^2(t) + c'_3V_{out}^3(t)\right) + \phi_0\right] \\
&\approx 2R_{pd}\sqrt{P_{in}P_{LO}}R_L A \sin[\phi_0] \\
&\quad + G_1\left[V_{in}(t) - V_{out}(t) - c'_2V_{out}^2(t) - c'_3V_{out}^3(t)\right] \\
&\quad - G_2\left[V_{in}(t) - V_{out}(t) - c'_2V_{out}^2(t) - c'_3V_{out}^3(t)\right]^2 \\
&\quad - G_3\left[V_{in}(t) - V_{out}(t) - c'_2V_{out}^2(t) - c'_3V_{out}^3(t)\right]^3
\end{aligned} \tag{5.19}$$

where $c'_2 = c_2/(c_1V_{\pi,LO})$, $c'_3 = c_3/(c_1V_{\pi,LO}^2)$, and ϕ_0 is a constant. $G_1 = (2R_{pd}\sqrt{P_{in}P_{LO}}R_L A \pi \cos[\phi_0])/V_{\pi,LO}$, $G_2 = (2R_{pd}\sqrt{P_{in}P_{LO}}R_L A \pi^2 \sin[\phi_0])/2V_{\pi,LO}^2$, and $G_3 = (2R_{pd}\sqrt{P_{in}P_{LO}}R_L A \pi^3 \cos[\phi_0])/6V_{\pi,LO}^3$. However, since the DC term, $(2R_{pd}\sqrt{P_{in}P_{LO}}R_L A \sin[\phi_0])$, is out of the signal band and it is filtered away when the loop is locked, we chose not to consider the DC term. The response of the optical phase demodulator will be non-linear due to the inherently non-linear response of the balanced receiver (tracking signal $V_{ref}(t)$ is a non-linear function), allowing for the output signal of the demodulator, $V_{out}(t)$, after locking the loop to be approximated by [138]:

$$V_{out}(t) = A_1V_{in}(t) + A_2V_{in}^2(t) + A_3V_{in}^3(t) \tag{5.20}$$

where A_1 , A_2 and A_3 are constants. We assume that the 3rd intermodulation product $A_3V_{in}^3(t)$ will have larger impact on the SFDR than the second order mixing product $A_2V_{in}^2(t)$ and we chose therefore not to consider the second order mixing product. Furthermore, the input signal $V_{in}(t)$ consists

of closely spaced tones: $V_{in}(t) = V_1 \sin[\omega_1 t] + V_1 \sin[\omega_2 t]$. In order to find A_1 and A_3 , we lock the loop $V_{ref}(t) = V_{out}(t)$ and insert equation (5.20) in equation (5.19). Using the method of harmonic balance the coefficients A_1 and A_3 are found. The demodulated signal, $V_{out}(t)$, is then expressed as:

$$V_{out}(t) = \frac{G_1}{1 + G_1} V_{in}(t) - \frac{(c'_3 G_1^4 + G_3 - 2G_2 c'_2 G_1^2)}{(1 + G_1)^4} V_{in}^3(t) \quad (5.21)$$

It is observed from equation (5.21), that for a specific c'_2 , the 3rd intermodulation product of the demodulated signal can be (theoretically) brought to zero ($A_3 = 0$) by adjusting the loop parameters. In other words, the feedback loop in combination with second order non-linearity, c_2 , associated with the tracking LO phase-modulator response results in a cancellation of the 3rd order intermodulation product of the demodulated signal.

In addition to the non-linearities of the LO phase-modulator, any residual amplitude modulation would be present (c.f. section 5.4.1). We therefore need to investigate the impact of the residual amplitude modulation on the SIR. Inserting equations (5.16) and (5.20) in (5.19), and locking the loop, i.e. $V_{ref}(t) = V_{out}(t)$, the output signal, $V_{out}(t)$, can be obtained. To begin with, the non-linearities associated with phase response of the LO phase-modulator are set to zero, i.e. $c_2 = 0$ and $c_3 = 0$. The output signal, $V_{out}(t)$, is then expressed as:

$$V_{out}(t) = \frac{G_1}{1 + G_1} V_{in}(t) - \left(\frac{D_1 G_1 G_2 + G_3 - D_2 G_1^3}{(1 + G_1)^4} V_{in}^3(t) \right) \quad (5.22)$$

Equation (5.22) shows that the residual effect of amplitude modulation will result in an increased 3rd order intermodulation product. However, it is noticed that the 3rd order intermodulation product is independent of the cubic term of the amplitude modulation, D_3 . For specific values of residual amplitude modulation coefficients; D_1 and D_2 , the 3rd order mixing product can be brought to zero by adjusting the loop gain.

In the presence of non-linearities associated with the phase and amplitude response of the tracking LO phase-modulator, the output signal, $V_{out}(t)$, can be expressed as:

$$\begin{aligned}
V_{out}(t) &= \frac{G_1}{1 + G_1} V_{in}(t) \\
&- \left(\frac{2G_2c'_2G_1^2 + D_1G_1^4c'_2 + c'_3G_1^4 + D_1G_1G_2 + G_3 - D_2G_1^3}{(1 + G_1)^4} \right) V_{in}^3(t)
\end{aligned} \tag{5.23}$$

The result presented in equation (5.23) is encouraging since the non-linearities associated with the tracking LO phase-modulator can be cancelled out. Either loop gain can be used in order to obtain a cancellation of the non-linearities, or the tracking LO can be tailored such that the non-linearities cancel out for a specific loop gain.

5.4.3 Sampling loop

So far, we have concentrated on the baseband loop. However, in order to increase the operation frequency of the demodulator, optical sampling is used. This is achieved simply by changing the CW optical source with an optical pulsed source, see Figure 5.4. The non-linear differential equations (5.14) and (5.18) can also be used to describe the sampling loop by including the time-varying amplitude of the optical pulse source in the equations. As a result of the optical sampling, the input RF signal expressed in equation (5.7) is now downconverted to an Intermediate Frequency (IF): $\omega_{IF} = \omega_1 - \omega_{ls}$. ω_{ls} is the repetition frequency of the pulsed optical source time-varying amplitude and it is chosen such that ω_{IF} will fall within the bandwidth of the baseband loop. In terms of the performance, we want the sampling loop to match the baseband loop. In order to compare the performance of the sampling loop with the baseband loop, we need to look at the total phase error $\phi_e(t)$.

When the tracking LO phase-modulator non-linearities are set to zero and assuming that the phase detection process is linear, $\sin[\varphi_e(t)] \approx \varphi_e(t)$, the total phase error for the (locked) baseband loop can be computed by analytically solving equation (5.18). The solution is expressed as:

$$\varphi_e(t) = \frac{1}{K^2 + \omega_1^2} \left(M_{in}\omega_1^2 \cos[\omega_1 t] - K \sin[\omega_1 t] \right) \tag{5.24}$$

where $M_{in} = (\pi/V_{\pi,in})V_1$. In equation (5.24), it is assumed for simplicity that the input RF signal consists of only one tone at frequency ω_1 with the amplitude V_1 . Since there are no non-linearities in the loop, $\varphi_e(t)$ contains

only a frequency component at ω_1 , as expected. Equation (5.24) also shows that as the loop gain, K , approaches infinity, the total phase error, $\varphi_e(t)$, approaches zero, i.e. the demodulated signal, $V_{out}(t)$, is a replica of a input RF signal. Under the same assumption as for the baseband loop, the total phase error for the sampling loop is governed by the following equation:

$$\begin{aligned} \varphi_e(t) = & M_{in} \cdot \left(\cos[\omega_1 t] - \frac{K \sin^3[\omega_1 t]}{6\omega_1} - \frac{A_0 K \sin[\omega_1 t] - \omega_1 t \cos[\omega_1 t]}{2\omega_1} \right) \\ & \cdot e^{-\frac{1}{2} \frac{A_0 K (\cos[\omega_1 t] \sin[\omega_1 t] + \omega_1 t)}{\omega_1}} \end{aligned} \quad (5.25)$$

In equation (5.25), we have assumed a raised cosine pulse shape and we have used the following approximation $\cos[x] \approx 1 - x^2/4$ in order to be able to obtain the analytical solution. A_0 is the amplitude of the pulses. It is observed in equation (5.25), that $\varphi_e(t)$ not only contains the frequency components at ω_1 , but also the multiples of ω_1 (harmonics). The amplitude of the harmonics increases as the loop gain is increased. This is contrast with the baseband loop. Furthermore, by inspecting (5.25), we can qualitatively conclude that the amplitude of the harmonics can be reduced by increasing the frequency ω_1 . In section 5.7.1, we confirm that the sampling induces extra non-linearities in the overall loop response, by using the time domain large signal numerical model based on the non-linear differential equations describing $\phi_e(t)$ and $V_{out}(t)$.

5.5 Experimental results

The simulation results presented in this and the next sections are obtained by using the detailed time domain numerical model based on equations (5.18) and (5.14). We emphasize that the baseband loop is considered in this section. An experimental set-up, similar to Figure 5.4, was constructed in order to verify the numerical model [29]. The experimental bandwidth was limited by the time delay imposed by discrete components of the receiver. In Figure 5.5(a), a one-tone measurement is shown together with the corresponding simulation results. Due to the experimental bandwidth limitation, the input RF signal frequency is only $f_1 = 150$ kHz and the loop filter bandwidth is 1.1 MHz. The amplitude of the signal after balanced photodetection, $V_{pd}(t)$, is plotted as a function of the loop gain, K . Experimental and simulation results show that as the loop gain is increased, the amplitude of the $V_{pd}(t)$ is reduced, i.e. the linearity of the demodulator

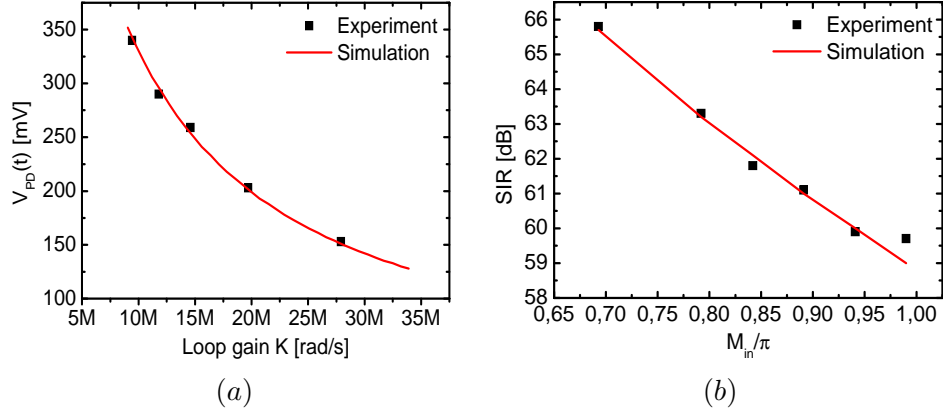


Figure 5.5: (a) One-tone measurement. Output of the balanced photodetector, $V_{pd}(t)$, as a function of loop gain. (b) Two-tone measurement. SIR as a function of input signal modulation depth, M_{in} .

is improved. A good agreement between the experimental and simulation results is obtained for the one tone measurement. In Figure 5.5(b), results of a two tone measurement are shown together with the corresponding simulation results. The SIR is plotted as a function of the modulation depth, $M_{in} = (\pi/V_{\pi,in})V_1$, of the input RF signal. V_1 is the amplitude of the input RF signal and is assumed equal for both tones. The input RF signal frequencies are: $f_1 = 150$ kHz and $f_2 = 170$ kHz. As expected, the SIR decreases as M_{in} is increased. Once again a good agreement between the model and experimental results is observed.

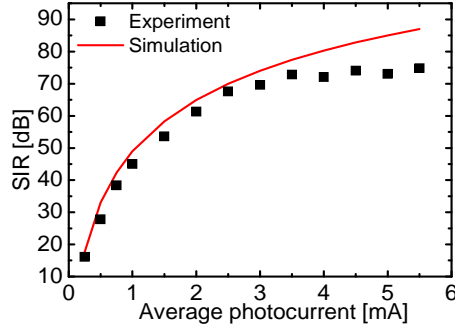


Figure 5.6: Two tone measurement. SIR as a function of average photocurrent.

In Figure 5.6, experimental and simulation results of the SIR as a function of increasing average photocurrent (\sim increasing loop gain) are shown. It is observed that for relatively low values of the average photocurrent (≤ 2.5 mA), there is good agreement between the experimental results and the simulation results. However, as the average photocurrent is increased, photodetector non-linearities are enhanced resulting in a discrepancy between the experimental and simulation results. It is observed in Figure 5.6 that the photodetector non-linearities limit the SIR. For the simulation, the photodetector non-linearities have been set to zero.

5.6 Simulation results baseband loop

5.6.1 Effects of loop gain and LO phase-modulator non-linearities

We set a goal of 90 dB of the SIR and SNR (500 MHz bandwidth) for the modulation depth of $\pi/2$. These requirements will lead to a SFDR of 90 dB in 500 MHz bandwidth.

As mentioned in the introduction, the modulator distortion, especially of the tracking LO phase-modulator in the considered case, will usually dominate over the photodiode distortion. We therefore assume that the tracking LO phase-modulator is much more non-linear than the photodiodes, i.e. $c_2/b_2 \gg 1$ and $c_3/b_3 \gg 1$. Furthermore, electronics non-linearities can be suppressed by the feedback loop and only need to be lower than the non-linearities of the tracking LO phase-modulator response. In contrast, the tracking LO phase-modulator non-linearities are not suppressed and must therefore be carefully considered. However, a linear input phase-modulator is considered.

In Figure 5.7(a), the SIR is computed as a function of the loop gain when the ratio between the loop filter bandwidth and the RF input signal frequency, (f_{LF}/f_1) , is varied. The input RF signal $V_{in}(t)$ includes two closely spaced frequencies: $\omega_1/2\pi = f_1$ and $\omega_2/2\pi = f_2$, as shown in equation (5.7). A linear tracking LO phase-modulator is assumed in Figure 5.7(a).

Figure 5.7(a) illustrates that as the loop gain K is increased, the performance of the phase-locked demodulator improves in terms of SIR, i.e. the SIR of the demodulated signal increases. As the ratio, (f_{LF}/f_1) , is significantly increased, the SIR converges. Furthermore, the SIR approximately increases with 60 dB/decade as the normalized loop gain is increased (the

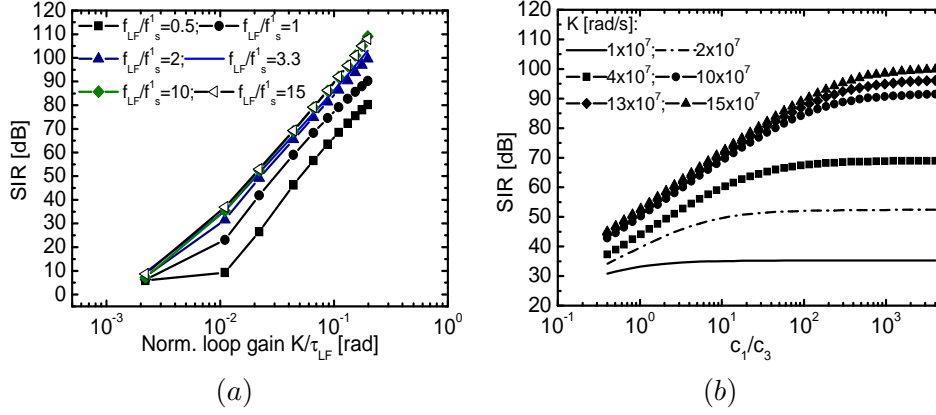


Figure 5.7: The input RF signal modulation depth: $M_{in} = \pi/2$. (a) The SIR of the demodulated signal as a function of normalized loop gain for selected values of the ratio f_{LF}/f_1 . (b) The SIR of the demodulated signal as a function of the ratio c_1/c_3 of the LO phase-modulator. Quadratic term $c_2 = 0$.

SIR has the slope of ~ 3). As observed in Figure 5.7(a) relatively large values of the SIR can be obtained provided a large loop gain and linear tracking LO phase-modulator. Using Equation (5.21) and setting $c_2, c_3 = 0$, the expression for the SIR can be obtained:

$$SIR = 20 \log \left[\frac{(8^{1/3}(1 + G_1))^3}{M_{in}^2} \right] = 20 \log[(8^{1/3}(1 + G_1))^2] - 20 \log[M_{in}^2] \quad (5.26)$$

Even though equation (5.26) is derived for a loop without a loop filter, it is in good correspondence with Figure 5.7(a). Equation (5.26) shows that the SIR can be significantly increased by increasing G_1 (loop gain is directly proportional to G_1). Moreover, equation (5.26) shows that the SIR increases with 60 dB/decade as G_1 is increased.

One of the key challenges in creating a linear demodulator is the linearity of the tracking LO phase-modulator. The phase-change vs. voltage characteristic of the LO phase-modulator is non-linear and thereby reducing the SIR of the demodulated signal. First, we are going to investigate the impact of a cubic non-linearity on the SIR. In Figure 5.7 (b), SIR is computed as the ratio between the linear term (c_1) and cubic term (c_3) of the LO phase-modulator response for selected values of the loop gain. In general, the SIR decreases as the ratio c_1/c_3 decreases. The values of c_1/c_3 for which SIR

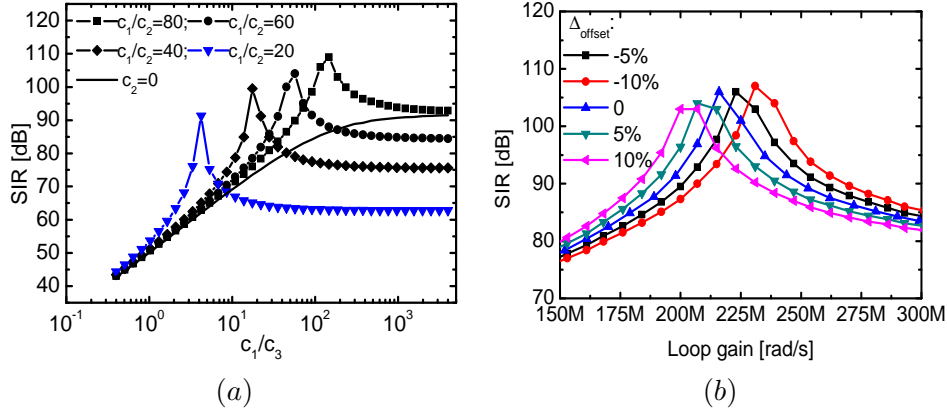


Figure 5.8: SIR of the demodulated signal as a function of c_1/c_3 . The ratio c_1/c_2 takes values: 80, 60, 40, 20. (b) SIR of the demodulated signal as a function of loop gain, K . $c_1/c_2 = 40$ and $c_1/c_3 = 20 + \Delta_{offset}$.

starts to decrease are loop gain dependent since the non-linearities of the LO phase-modulator become more enhanced as the loop gain is increased. This is also in accordance with equation (5.21), i.e. as the loop gain is increased the 3rd order intermodulation product increases as well. For a loop gain of $K = 10 \cdot 10^7$ rad/s, the ratio c_1/c_3 needs to be > 200 , to maintain the SIR of 90 dB. Very recently, the ratio between the cubic term and the linear term c_1/c_3 of the semiconductor phase-modulator has been measured to be 26 [139]. Obviously, a lot of improvement is needed in order to obtain a c_1/c_3 of 200.

Next, we investigate how the quadratic term c_2 of the non-linear response of the phase-modulator can be used to cancel out 3rd order intermodulation product of the demodulated signal. In Figure 5.8(a), SIR of the demodulated signal is computed as a function of the ratio c_1/c_3 for selected values of c_1/c_2 ratio. For reference, a SIR is also plotted for the case when $c_2 = 0$. The selected values of the ratio c_1/c_2 are experimentally obtainable for electrical circuits [138]. To the author's best knowledge measurements of the quadratic term of the semiconductor phase-modulators has not yet been performed. We have therefore varied the ratio c_1/c_2 in order to cover low and high values. Figure 5.8(a) shows that as the ratio c_1/c_2 is decreased the SIR becomes severely limited by the second order non-linearity. Furthermore, for low values of the third order non-linearity of the LO phase-modulator, the SIR becomes completely dominated by the

second order non-linearity, i.e. independent of the c_1/c_3 ratio. This is in accordance with equation (5.21), i.e. if c_3 is low the 3rd order intermodulation product of the demodulated signal is dominated by c_2 . However, the results in Figure 5.8(a) also show that for non-zero values of c_2 , there exists a combination of c_2 and c_3 for which the 3rd order intermodulation product is minimized, i.e. peaking (resonance) of the SIR. This implies that the combined effects of the non-linearities associated with the balanced receiver and the non-linear LO phase-modulator response plus the gain contribution from the feedback loop, result in a cancellation of the 3rd order intermodulation product. This is also observed in equation (5.21), as stated earlier. Furthermore, the resonance peak moves towards higher values of c_3 as c_2 is increased. So, by having second order non-linearity associated with the tracking LO phase-modulator, we can tolerate more cubic non-linearity, c_3 . Having the ratio c_1/c_3 of only 20 and $c_1/c_2 = 40$, the SIR of 90 dB can still be obtained.

Suppose that we want to tailor the phase-modulator such that SIR peaking is obtained. In practise, we may not be able to match exactly the required values of c_1/c_3 and c_1/c_2 in order to obtain SIR peaking, so we need to investigate what happens if we are slightly off. In Figure 5.8 (b), the SIR is computed as a function of loop gain when the ratio c_1/c_3 is varied from the exact value of c_1/c_3 for which the SIR peaking is obtained, i.e. $c_1/c_3 = 20 + \Delta_{offset}$. The ratio $c_1/c_2 = 40$ is held constant. Figure 5.8 (b) shows that the SIR peaking is dependent on the loop gain and it occurs in a relatively wide band of the loop gain. It is also noticed that as the ratio c_1/c_3 is varied, the resonant peak of the SIR moves as well. So, by adjusting the loop gain resonant peaking of the SIR can be re-obtained. Another thing which should be addressed is the frequency dependence of c_3 if the demodulator is operated over a wide frequency range. Frequency dependence will cause the c_1/c_3 to vary, and Figure 5.8(b) can therefore be used to observe the effect of varying c_1/c_3 . If the ratio c_1/c_3 varies with the frequency for a specific loop gain, we will move away from the resonant peaking of the SIR. One solution could be to re-adjust the loop gain or to design a wide-band tracking LO phase-modulator. Furthermore, the demodulator could be designed to operate in a narrow frequency band.

5.6.2 Effects of residual amplitude modulation

In this section, the impact of residual amplitude modulation on the SIR is considered. Nonlinear phase response associated with the tracking LO

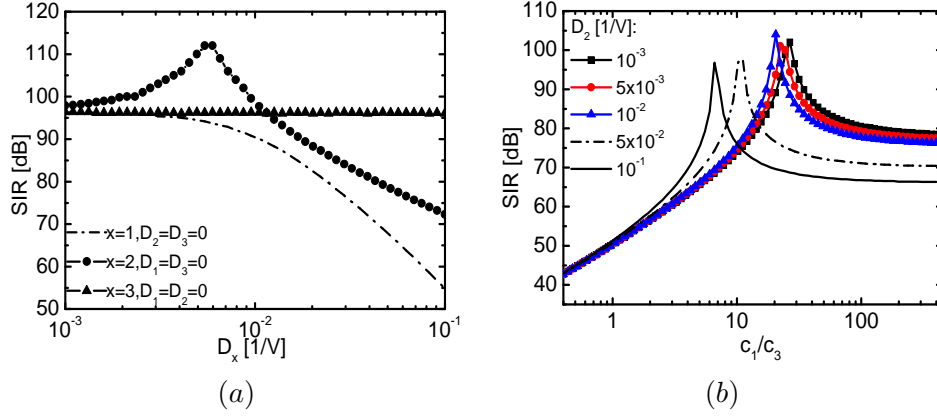


Figure 5.9: (a) SIR of the demodulated signal as a function of D_x for $x=1,2,3$. (c_2, c_3) = 0. (b) SIR of the demodulated signal as a function of c_1/c_3 for selected values of the quadratic term, D_2 , of the residual amplitude modulation. $c_1/c_2 = 40$ and $D_1 = 0.03$ 1/V.

phase-modulator is set to zero, i.e. $c_2 = c_3 = 0$. From equation (5.23) it can be observed that D_1 and D_2 will have impact on the 3rd order intermodulation product of the demodulated signal, whereas D_3 has no impact on the 3rd order mixing product of the demodulated signal. In practice, coefficients D_1 , D_2 and D_3 can be related to the absorption coefficient of the tracking LO phase-modulator.

In Figure 5.9 (a), the SIR of the demodulated signal is computed as a function of D_x where $x = 1, 2$ and 3. We assume that terms D_x where $x > 3$ are negligible, as it would be expected in practice. Figure 5.9(a) shows that as D_1 ($D_x = 0, x = 2, 3$) is increased beyond $5 \cdot 10^{-3}$ 1/V, the SIR starts to decrease. When D_2 is varied ($D_x = 0, x = 1, 3$) resonant behavior of the SIR, similar to Figure 5.8(b), is observed, i.e. there exist a value of D_2 for which the 3rd order mixing product is minimized. Furthermore, we observe, Figure 5.9(a), that the effect of D_1 is more deteriorating than that of D_2 . For the case when D_3 is varied ($D_x = 0, x = 1, 2$), the SIR is not affected and in accordance with equation (5.23). Usually, for the semiconductor phase-modulator, the quadratic term of the amplitude modulation is more difficult to minimize than the linear term. We therefore need to concentrate on the quadratic term of the amplitude modulation.

5.6.3 Combined effects of non-linearities and their cancellation

In this section, the combined effect of non-linearities (non-linear phase response and residual amplitude modulation) associated with tracking LO phase-modulator are considered. We pay more attention to the quadratic term, D_2 , of the residual amplitude modulation since it is more difficult to reduce (in practice) for semiconductor phase-modulators, compared to the linear term, D_1 .

In Figure 5.9(b), SIR is computed as a function of the ratio c_1/c_3 when D_2 is varied from 10^{-3} 1/V to 10^{-1} 1/V. The ratio c_1/c_2 is set to 40 and $D_1 = 0.03$ 1/V. It is observed from Figure 5.9(b) that for low values of cubic non-linearity (c_3) of the phase-modulator response, the SIR is fully limited by the quadratic term of the residual amplitude modulation, D_2 . However, Figure 5.9(b) also shows that there exists a combination of c_3 , c_2 , D_1 and D_2 for which the 3rd order mixing product is minimized, i.e. peaking (resonance) of the SIR. The resonant peak moves towards lower values of c_1/c_3 ratio as D_2 is increased. Figure 5.9(b) is in accordance with equation (5.23) which states that the 3rd order intermodulation product can be reduced by proper combination of the loop gain and tracking LO phase-modulator non-linearities. The cascaded sources of non-linearities associated with a balanced detector, phase and amplitude modulation of the tracking LO phase-modulator cancel out each other. Figure 5.9 (b) illustrates that even though the ratio c_1/c_3 is low for the measured semiconductor phase-modulator ($c_1/c_3 = 26$ [139]), high values of the SIR are still obtainable.

Next, we are going to investigate how the resonant peaking of the SIR scales with the input RF signal voltage and frequency. We must be sure that the cancellation of the non-linearities does not only occur at a single power level or a single frequency. Nonlinearities associated with the tracking LO phase-modulator are chosen such that resonant peaking of the SIR is obtained, see Figure 5.9(b). In Figure 5.10(a), we plot the amplitude of the fundamental of the demodulated signal (f_1) and amplitude of the 3rd order intermodulation product ($2f_1 - f_2$) as a function of input signal voltage, V_{in} . The amplitude of the fundamental and the 3rd order intermodulation product (IM₃ curve) of the demodulated signal increases with the input signal voltage, as expected. It should be observed that there are no dips in the IM₃ curve as the input signal voltage is varied. This means that the cancellation of the non-linearities associated with the balanced receiver

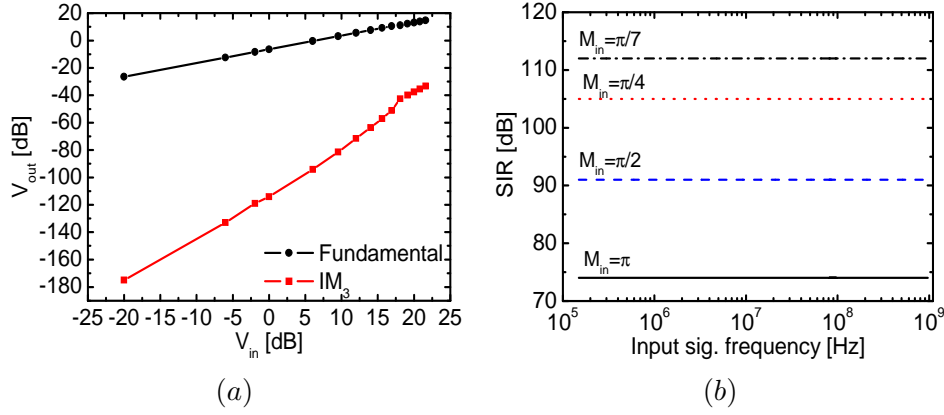


Figure 5.10: $c_1/c_3 = 10$, $c_1/c_2 = 40$, $D_1 = 0.03$ 1/V and $D_2 = 0.05$ 1/V. (a) Amplitude of the fundamental and the 3rd order intermodulation product (IM₃ curve) of the demodulated signal as a function of input signal voltage, V_{in} . (b) SIR of the demodulated signal as a function of input signal frequency for selected values of modulation depth M_{in} .

and phase-modulator occurs over a broad range of input RF signal powers. In Figure 5.10(b), the SIR is computed as a function of input RF signal frequency as the input signal modulation depth, M_{in} , takes values from π to $\pi/7$. Figure 5.10(b) shows that the SIR remains constant as the input RF signal frequency is increased. As the input RF signal modulation depth decreases, the SIR increases as expected.

5.7 Sampling loop simulation results

5.7.1 Sampling vs. baseband loop

In this section, the linearity of the optical phase demodulator, based on a sampling loop, is investigated by computing the SIR of the demodulated signal. An input RF signal modulation depth of $\pi/2$ is assumed in all simulation results and the pulse shape of the sampling pulses is assumed Gaussian.

In Figure 5.11(a), the SIR is computed as a function of the FWHM of the pulsed optical source signal for selected values of K . The input RF signal frequencies are $\omega_1 = 20$ GHz + 0.9 GHz, $\omega_2 = 20$ GHz + 1.6 GHz and $\omega_{ls} = 20$ GHz. The downconverted IF components, at which the loop will operate,

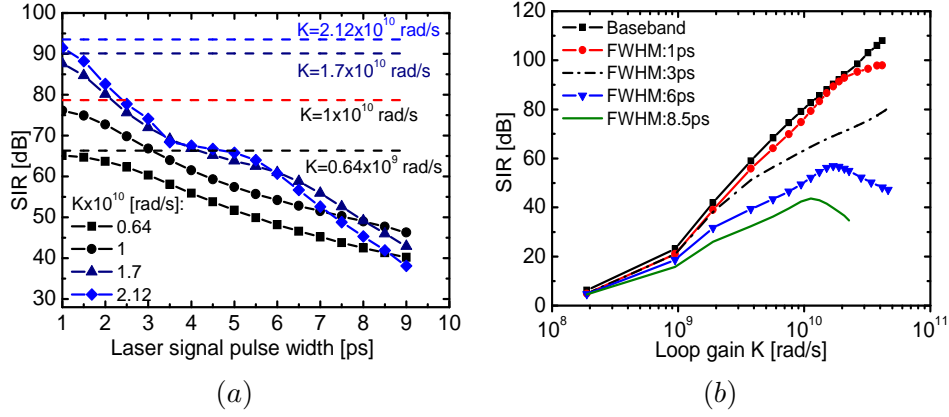


Figure 5.11: (a) SIR as a function of pulse width (FWHM) of the laser signal for selected values of the loop gain. (b) SIR as a function of loop gain, K , for selected values of the FWHM of the pulsed laser source.

are: $\omega_{IF,1} = \omega_1 - \omega_{ls}$ and $\omega_{IF,2} = \omega_2 - \omega_{ls}$. The constant lines correspond to the SIR obtained by the baseband loop for the corresponding values of a loop gain. For the baseband case $\omega_1 = 0.9$ GHz and $\omega_2 = 1.6$ GHz. In general, Figure 5.11(a) shows that the SIR decreases as the pulse width is increased and thereby indicates that the sampling induces a penalty in the SIR. This is in accordance with equation (5.25). However, it should be noted that for the open-loop sampling system there is no degradation in the SIR as the pulse width is varied. Furthermore, we observe from Figure 5.11(a) that as the pulse width is decreased, the SIR of the sampling loop approaches the value obtained by the baseband loop. However, very short pulses (~ 1 ps) are required in order to preserve the SIR. Figure 5.11(a) furthermore indicates that for relatively high values of the pulse width, increasing K does not improve the SIR. We therefore need to investigate how the SIR of the demodulated signal is affected by increasing the loop gain. In Figure 5.11(b), the SIR is computed as a function of the loop gain for different values of the pulse width. As a reference, we also plot the SIR obtained by the baseband loop in the same figure. Figure 5.11(b) shows that for increasing loop gain and pulse width, the SIR of the demodulated signal, obtained by the sampling loop deviates from the SIR obtained by the baseband loop. The penalty in the SIR is increased as the loop gain is increased. This is in accordance with equation (5.25), as discussed earlier. However, the penalty decreases for decreasing pulse width. Figure 5.11(b)

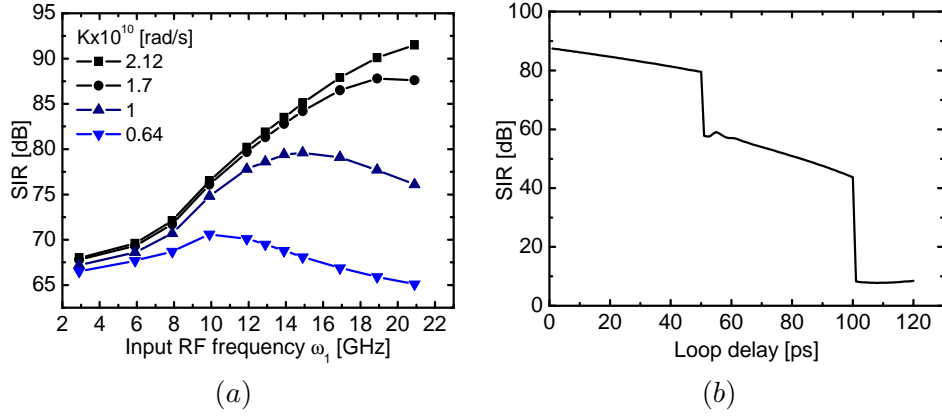


Figure 5.12: (a) The SIR as a function of input signal frequency for selected values of the loop gain. Pulse width of the laser signal: 1 ps. (b) The SIR as a function of loop delay in a sampled second order feedback loop with 20 GHz pulse rate and 1 GHz IF.

also shows that the SIR is less sensitive to the pulse width for low values of the loop gain. Furthermore, the SIR curve starts to decrease, for pulse widths of 6 ps and 8.5 ps, as the loop gain is sufficiently increased. The value of the loop gain for which the SIR starts to decrease, decreases with increasing pulse width. For a pulse width of 8.5 ps pulse width, the loop loses its lock if the loop gain is increased beyond 2.5×10^{10} rad/s.

Next, it is investigated how SIR is affected by the input RF signal frequency (Figure 5.12(a)). The pulse width is set to 1 ps. The frequency difference $\omega_1 - \omega_{ls}$ and $\omega_2 - \omega_{ls}$ is held constant and the sampling loop will therefore operate at a constant intermediate frequency. Figure 5.12(a) indicates that for relatively high values of loop gain: 2.12×10^{10} and 1.17×10^{10} rad/s, the SIR of the demodulated signal increases as the input signal frequency is increased. For relatively low values of the input RF signal frequency, the dependency of the SIR on the loop gain is negligible. In that case, the SIR is limited by the non-linearities of the low-frequency optical pulsed source. For the relatively low value of the loop gain ($K = 0.64 \times 10^{10}$ rad/s) the SIR does not seem to be much affected by increasing ω_1 . Furthermore, for $K = 0.64 \times 10^{10}$ and 1×10^{10} rad/s, there is an optimal value of the input signal frequency for which the SIR is increased. So far, the effect of the finite delay in the loop has been considered to be small.

In a baseband loop, the effect of loop delay is well known to limit the

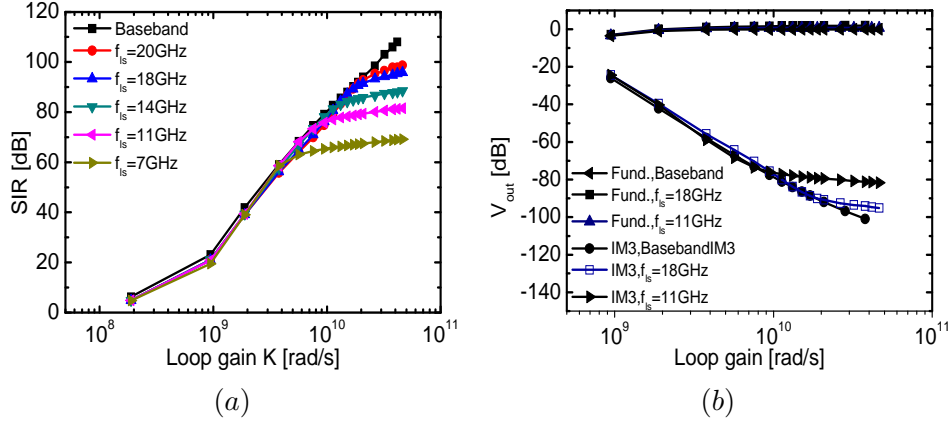


Figure 5.13: Optical pulse width: 2 ps. (a) SIR as a function of loop gain, K , for selected values of optical pulse source frequency f_{ls} . (b) Amplitude of the fundamental and 3rd order intermodulation product (IM₃ curve) as a function of loop gain, K , for selected values of optical pulse source frequency.

available loop gain while maintaining stability [80]. The delay gets more significant the higher the frequency. In a sampled loop, the effective feedback delay is given by that of the pulse rate. No information can in this case be forwarded when no pulse is present in the loop, assuming the pulse width to be narrower than the physical delay of the loop. In other words; the sampled loop will operate more efficiently at higher input frequencies. Figure 5.12(b) shows the effect of a physical loop delay in a sampled loop. A 20 GHz pulse rate and 1 GHz IF have been assumed. Furthermore, idealized delta pulses have been assumed to clearly isolate the impact of the loop delay. The performance degrades slowly with increasing delay up to the point where the delay exceeds the pulse repetition ratio, after which a steep degradation in performance occurs corresponding to a delayed feedback to the second proceeding pulse. It can be observed that the effect of the loop delay is relatively weak compared to the pulse rate.

In Figure 5.13(a), the SIR is computed as a function of loop gain, K , for selected values of optical pulse source frequency (sampling frequency) f_{ls} . The pulse width is kept constant at 1 ps (2% duty cycle). Once again, we plot the SIR obtained by the baseband loop in the same figure. Figure 5.13(a) shows that the deviation of the SIR obtained by the sampling loop from the baseband loop becomes more pronounced as the sampling frequency, f_{ls} , is decreased. This is because the delay between the consec-

utive pulses increases as the sampling frequency is decreased. The lower the delay between the consecutive pulses, the more the operation of the sampling loop resembles the operation of the baseband loop.

In Figure 5.13(b), the amplitude of the fundamental and the 3rd order intermodulation product (IM₃ curve) is plotted as a function of loop gain for selected values of the optical pulse source frequency, f_{ls} . It is observed that the dependence of the amplitude of the fundamental on the optical pulse source frequency is negligible. Contrary to this, the amplitude of the 3rd order intermodulation product is dependent on the optical pulse source frequency especially as the loop gain is increased. For the baseband loop the IM₃ curve decreases linearly with the loop gain. However, for the sampling loop the IM₃ curve has more exponential decay as a function of loop gain. This is also in accordance with equation (5.25) which states that the total phase error of the sampling loop will have a term with an exponential decay.

5.7.2 Signal-to-noise ratio as a function of phase and amplitude noise

In this section, the effect of amplitude and phase noise of the optical pulse source on the SNR of the demodulated signal is determined. The SNR is computed in a 500 MHz bandwidth. The optical input, $E_{in}(t)$, and LO signal, $E_{LO}(t)$, are in the presence of amplitude and phase noise are thereby expressed as:

$$E_{in}(t) = \sqrt{[1 + \beta(t)]P_{in}(t + \alpha(t))}e^{(j\omega_0 + \phi_{in}(t))} \quad (5.27)$$

$$E_{LO}(t) = \sqrt{[1 + \beta(t)]P_{LO}(t + \alpha(t))}e^{(j\omega_0 + \phi_{LO}(t))} \quad (5.28)$$

where $\beta(t)$ and $\alpha(t)$ represent the amplitude and phase noise, respectively, and are defined in equations (2.18) and (2.7). $P_{in}(t) = P_{LO}(t)$ is the envelope of the pulsed optical source. We use the stochastic Euler scheme, equation (2.48), to solve the stochastic differential equations (2.7) and (2.18) representing the phase and amplitude noise, respectively. In Figure 5.14(a), the SNR of the demodulated signal is computed as a function of timing jitter of the optical pulsed source. Timing jitter of the optical pulse source is computed using equation (2.14). The integration range of the timing jitter is: 100 Hz – 10 GHz. Figure 5.14(a) illustrates that the SNR decreases as the timing jitter increases, as expected. Moreover, it is observed that the timing jitter requirements on the optical pulse source are not very severe if a SNR of 90 dB is aimed. In Figure 5.14(b), the effect of amplitude noise

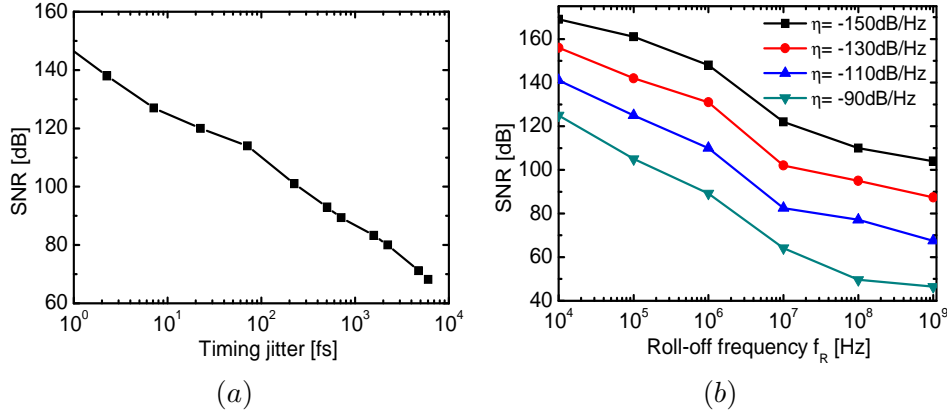


Figure 5.14: The SNR of the demodulated signal in 500 MHz bandwidth. (a) The SNR as a function of timing jitter of the pulsed source. Integration range: 100 Hz - 10 GHz. (b) The SNR as a function of roll-off frequency f_R when η is varied.

on the SNR is investigated. Even though η is relatively large, high values of SNR can be obtained by having low f_R . This is in accordance with Figure 2.6 which illustrates that the amount of high-frequency noise is significantly reduced as f_R is decreased.

In Figure 5.15, the combined effect of amplitude and phase noise on the SNR of the demodulated signal is determined. η is held constant at -130 dB/Hz which is a practically obtainable value with a relatively good laser design. The SNR is computed as a function of c (defined in equation (2.7)), for selected values of the roll-off frequency f_R . It is observed in Figure 5.15 that for low values of c the effect of phase noise on the SNR is negligible and the SNR is fully dominated by the amplitude noise. However, as c is increased beyond 10^{-22} s, the effect of phase noise on the SNR is observed and the effect of amplitude noise on the SNR becomes negligible.

5.8 Experimental demonstration of a novel phase-locked demodulator with feedback and sampling

In this section, experimental results for novel coherent optical phase demodulator with feedback and sampling are presented. The results presented in

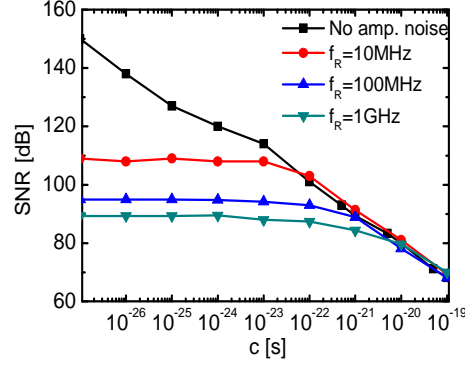


Figure 5.15: The SNR of the demodulated signal in 500 MHz bandwidth as a function of c for selected values of f_R . $\eta = -130$ dB/Hz.

this section are taken from reference [31] where also the experimental set-up is described. The experimental set-up was built by Hsu-Feng Chou now with LuminentOIC Inc.

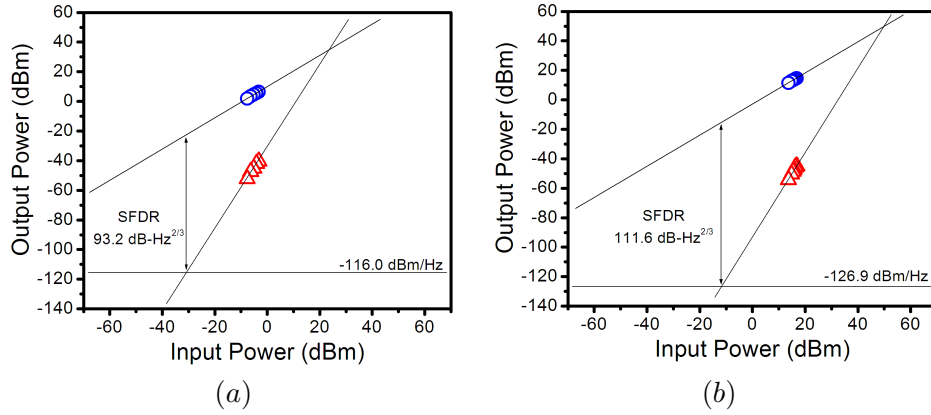


Figure 5.16: The output power of fundamental frequency component (circle), 3rd order intermodulation product (triangle) and the noise floor as a function of input power. (The Figure is a courtesy of Hsu-Feng Chou LuminentOIC Inc.) (a) Traditional phase demodulator without feedback. (b) Baseband phase demodulator with feedback.

In Figure 5.16, two-tone measurement, for the baseband optical phase demodulator, is shown [29]. Due to the experimental bandwidth limitations,

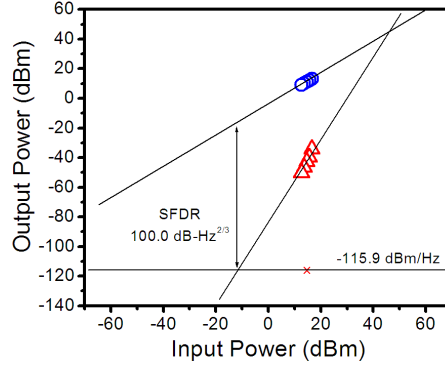


Figure 5.17: The output power of the fundamental frequency component (circle), the 3rd order intermodulation product (triangle) and the noise floor as a function of input power for the sampling loop. The Figure is a courtesy of Hsu-Feng Chou LuminentOIC Inc.

the frequency of the RF tones used for the two-tone (test) measurement are 160 kHz and 180 kHz. In Figure 5.16(a), two-tone measurement is shown for the open loop baseband demodulator which corresponds to the traditional phase demodulator. It is observed in Figure 5.16(a) that the SFDR obtained for the open loop is 93.2 dB·Hz^{2/3}. For the closed baseband loop, the two-tone measurement is shown in Figure 5.16(b). The obtained SFDR for the closed baseband loop is 111.6 dB·Hz^{2/3}. This corresponds to an improvement of 18.4 dB compared to the traditional phase demodulator. It should be observed in Figures 5.16(a) and (b) that the SNR is approximately the same for the open and closed loop case. It is observed in Figure 5.16(b) that the power in the fundamental frequency component and the noise floor is decreased compared to the open loop, resulting in the preserved SNR. However, compared to Figure 5.16(a), the 3rd order intermodulation product in Figure 5.16(b) is suppressed. This proves the effectiveness of the feedback to improve the linearity of the demodulator. Furthermore, the input IP3 is significantly improved for the demodulator with feedback. For the open loop, the input IP3 is approximately 1 dBm and for the closed loop it is approximately 41 dBm.

In Figure 5.17, two-tone measurement for the sampling loop is shown. The frequency of the RF tones used for the two-tone (test) measurement for the sampling loop are 110.140 MHz and 110.160 MHz. The optical pulse source consists of a CW laser source which is first amplified by an EDFA and

then carved by push-pull type LiNbO_3 intensity modulator before being boosted by the high-power EDFA. The sampling frequency of the optical pulse source is 110 MHz and the corresponding FWHM is 1.08 ns (12% duty cycle). The measured SFDR in Figure 5.17 is $100 \text{ dB}\cdot\text{Hz}^{2/3}$. The SFDR obtained for the sampling loop is smaller than the SFDR obtained for the baseband loop. A degradation in SFDR is expected when going from baseband loop to sampling loop, as discussed in section 5.7. Furthermore, the decrease in SFDR for the sampling loop could be attributed to the noisy pulse source which increases the noise floor by 11 dB compared to the baseband loop.

5.9 Summary

A novel phase-locked coherent optical demodulator was presented and numerically investigated. A time-domain numerical model of the phase-locked demodulator has been developed and shown to be in good agreement with initial experimental results. For the baseband loop, we have shown that the tracking LO phase-modulator non-linearities translate into the 3rd order intermodulation product of the demodulated signal which severely limits the SIR of the demodulated signal.

A novel cancellation technique, which is RF signal power and frequency independent, is proposed and numerically investigated in order to alleviate non-linearities associated with the tracking LO phase-modulator. It has been shown that the interplay between the loop gain of the feedback circuit and non-linearities of the tracking LO phase-modulator may result in a cancellation of the 3rd order intermodulation product of the demodulated signal. The cancellation of the 3rd order intermodulation product can be achieved by carefully tailoring the non-linearities of the tracking LO phase-modulator, or for the specific set of non-linearities, the loop gain can be adjusted such that the cancellation is obtained. The proposed technique significantly reduces linearity requirements for the tracking LO phase-modulator and large values of the signal-to-intermodulation ratio can thereby be obtained even though the tracking LO phase-modulator is fairly non-linear.

Furthermore, a novel approach of using optical sampling, in order to increase operation frequency of the baseband optical phase demodulator, has been presented and investigated. The optical sampling inherently induces a penalty in the SIR compared to the baseband loop. However, for very

short pulse widths (< 2 ps) and high input signal frequencies, the penalty is very small making this technique promising for high-frequency analog optical links.

Chapter 6

Conclusion and future directions

In this thesis, we presented a modelling technique, phase noise analysis and experimental realizations of optoelectronic phase-locked loop based clock recovery. We treated the problem using the techniques from stochastic processes and stochastic differential equations. The effects of timing jitter on optical regenerator and demultiplexer operating at 160 Gb/s were investigated as well. Moreover, a receiver concept for linear optical phase demodulation, for phase-modulated radio-over-fibre optical links, was presented, theoretically investigated and experimentally demonstrated. In this chapter, we summarize the main results and conclusions of this work, and point to some future directions which research within this area/subject may take.

6.1 Main results and conclusions

6.1.1 Model of an optoelectronic phase-locked loop with noise sources

A theoretical model of an optoelectronic phase-locked loop based clock recovery including the effects of signal phase noise, loop time delay and laser transfer function was developed. The developed model is believed to be a powerful tool when designing high performance optoelectronic phase-locked loop based clock recovery for ultra-high speed optical communication systems.

Using the model, numerical simulations were carried out to investigate the

performance of the optoelectronic phase-locked loop based clock recovery at a bit-rate of 160 Gb/s. In general, it was shown that increasing the loop gain resulted in an increased amount of transferred jitter from the input data signal to the recovered clock signal. Furthermore, decreasing the loop gain resulted in a decreased amount of jitter transferred from the input signal to the recovered clock signal.

It was shown that the time delay in the loop degraded the performance of the optoelectronic phase-locked loop based clock recovery. Increasing the loop length resulted in an increase in the recovered clock's timing jitter as the time delay approached its critical values for which the loop became unstable. The impact of time delay on the recovered clock's jitter could be reduced by using a low loop filter bandwidth, low loop gain and low noise VCO.

By using the numerical simulations, timing jitter requirements for the free-running electrical and optical local clock sources were determined in order to obtain a recovered clock signal with less timing jitter than the degraded input data signal. It was shown that this is achievable, as long as the jitter of the free-running VCO is less than the input signal jitter, even though the free-running optical clock signal generating laser contained more timing jitter than the input. This result may relax timing jitter requirements when designing optical pulse sources for high-bit-rate systems. In general, if the laser had more jitter than the VCO, it was preferable to have laser transfer function with a large bandwidth, whereas if the laser had less jitter than the VCO, it was preferable to have a laser transfer function with a small bandwidth.

6.1.2 Experimental demonstration of 320 Gb/s clock recovery using optoelectronic phase-locked loop

The effects of ultra-fast filtering-assisted cross-phase modulation in a semiconductor optical amplifier were used to achieve mixing between the data and locally generated optical clock signal and thereby demonstrating a 10 GHz clock extraction from a 320 Gb/s data signal. The measured timing jitter of the recovered clock signal was 800 fs. Even though the timing jitter of 800 fs of the recovered clock signal was not sufficient for error-free demultiplexing, with additional optimizations of the set-up, the presented technique looked very promising for ultra-high-speed clock recovery. In order to reduce the timing jitter of the recovered clock signal the physical loop length should be shortened. In terms of optical clock pulse source,

it would be desirable to have narrower clock pulses enabling a larger error signal.

6.1.3 Model of an optical regenerator with noise sources

A theoretical model of an optical regenerator (retimer) with clock recovery including the effects of signal phase noise, pulse shape and width was developed. We analytically modelled the whole system as an Ornstein-Uhlenbeck process and an analytical expression for the power spectral density of the retimed data signal was derived. The analytical expression was a very convenient tool in order to efficiently evaluate the performance of the optical regeneration. Numerical simulations were thereafter performed to investigate the performance of an optical regenerator at 40 Gb/s and 160 Gb/s.

We showed that for flat-top input data signal pulses and sufficiently narrow optical clock signal pulses, the timing jitter of the retimed optical data signal could be significantly reduced compared to the timing jitter of the degraded input data signal. This behavior seemed to be independent of free-running jitter of the VCO and the laser in the clock recovery. However, the optical clock signal pulse width needed to be relatively short compared to the optical data signal pulse width in order for the retimed data signal timing jitter to coincide with the recovered clock jitter, i.e. 3.5 ps at 40 Gb/s and 0.5 ps at 160 Gb/s.

Furthermore, we found that for Gaussian input data signal pulses, the timing jitter of the retimed optical data signal could still be reduced compared to the jitter of the degraded input data signal; however, the reduction was very limited. The effect of optical clock pulse width was small, however, the performance in terms of jitter reduction improved as the pulse width of optical clock signal was increased from 0.5 ps to 2.5 ps due to the more flat-top when the data signal bit-rate was 160 Gb/s. It was, however, not possible to match the timing jitter of the recovered optical clock signal and thereby obtain a strong jitter reduction.

6.1.4 Experimental investigation of the impact of gating timing jitter on a 160 Gb/s demultiplexer

The impact of timing jitter on 160 Gb/s demultiplexer was investigated by using two control pulse sources with different timing jitter properties. Furthermore, we investigated the interplay between the control signal pulse width and timing jitter.

It was shown that the excess timing jitter, of the control pulse source, in the frequency range from 20 kHz to 10 MHz was of great importance; an increase from 91 fs to 438 fs lead to a penalty of 9 dB and decreased the allowable time misalignment between data and control signal pulses, by approximately 1 ps. Furthermore, the impact of timing jitter could be reduced if the control signal pulses were broader than the data signal pulses.

6.1.5 Phase-locked coherent receiver with feedback – base-band loop

A novel phase-locked coherent receiver with feedback was presented and analyzed. A time-domain numerical model taking into account system non-linearities was developed and the model was shown to be in excellent agreement with experimental results. Furthermore, a simple perturbation analysis of the phase-locked coherent receiver with feedback was performed and simple analytical results were derived.

It was shown that in the presence of non-linearities associated with balanced receiver only, the demodulator was capable of demodulating phase modulated signals with "perfect" linearity provided sufficient loop gain. We also showed that non-linearities associated with phase and amplitude response of tracking LO phase-modulator could severely limit the signal-to-intermodulation ratio of the demodulated signal. If the quadratic term of tracking LO phase-modulator is fully cancelled, the cubic term should be approximately $< 5 \cdot 10^{-3}$ in order to maintain high values (> 90 dB) of signal-to-intermodulation ratio. The simultaneous presence of quadratic and cubic term of the tracking LO phase-modulator response was seen to severely limit the signal-to-intermodulation ratio unless they cancelled each other. Furthermore, only the linear and the quadratic term of the amplitude modulation limited the SIR of the demodulated signal.

It was shown experimentally that the proposed coherent receiver with feedback resulted in a 18 dB of SFDR improvement compared to the traditional coherent receiver without the feedback. The feedback technique effectively reduced the signal swing at the coherent receiver and achieved a higher level of linearity while preserving the SNR. The currently realized SFDR of $111.6 \text{ dB} \cdot \text{Hz}^{2/3}$ was limited by the excessive noise of the loop components and the loop time delay limiting the available loop gain. Furthermore, the experimental bandwidth was also limited by the time delay imposed by the discrete components of the receiver. Improved performance at higher operating speeds would be possible by using advanced integration technologies.

6.1.6 Linearized phase-locked coherent receiver with feedback

A review of different linearization (cancellation) techniques can be found in reference [137]. So far, linearization techniques have been applied to intensity modulated analog optical links and mostly applied on the transmitter side. In many cases, the linearizer circuit was designed to cancel either the quadratic or the cubic non-linearities and the cancellation of non-linearities occurred in a relatively narrow frequency band [137].

We showed that we could simultaneously cancel non-linearities associated with the balanced receiver and tracking LO phase-modulator by adjusting the loop gain and tailoring the non-linearities of the tracking LO phase-modulator. The loop gain could also adjusted such that cancellation was obtained for the specific set of non-linearities. No extra circuitry was needed in order to obtain the cancellation. The proposed cancellation technique was frequency and power independent and has not been previously reported. Using the proposed linearization technique, linearity requirements for the tracking LO phase-modulator could be significantly relaxed and large values of the signal-to-intermodulation ratio could thereby be obtained even though the tracking LO phase-modulator was fairly non-linear.

6.1.7 Sampling phase-locked coherent receiver with feedback

By introducing optical sampling, the phase-locked coherent receiver with feedback was capable of operating at high-frequencies by downconverting the high frequency input RF signal to the frequency range of the baseband loop. The receiver could thereby operate up to 20 GHz RF frequency without requiring large loop bandwidth. The advantage of this approach is that we can simply use the same baseband receiver and thereby keep relatively low-speed loop electronics, by simply exchanging the CW optical source with the pulsed optical source.

We showed that the optical sampling inherently induced a penalty in the SIR compared to the baseband loop. However, for very short pulse widths (< 2 ps) and high input signal frequencies, the penalty was very small making this technique promising for high-frequency phase-modulated analog optical links. Moreover, the amplitude and timing jitter of the pulse source degraded the SNR of the demodulated signal as expected. For the operation frequency of 20 GHz, the optical pulse source with timing jitter

of less than 500 fs (integration range: 100 Hz – 10 GHz) was desirable in order to achieve SNR of 90 dB in 500 MHz. Furthermore, by keeping the roll-off frequency up to 1 MHz, it was possible to tolerate relatively large amount of low-frequency amplitude noise and still obtain 90 dB of SNR in 500 MHz bandwidth.

Successful experimental demonstration of a sampling phase-locked coherent receiver with feedback at 110 MHz was presented as well. The SFDR of 100 dB·Hz^{2/3} was been obtained.

6.2 Future work

We found that the time delay deteriorated the jitter performance of the optoelectronic phase-locked loop based clock recovery resulting in increased jitter. For this reason, it is imperative to minimize time delay in the loop. This can be done by using a phase comparator (SOA) with high enough efficiency so that an EDFA is not needed to amplify the optical signal. Furthermore, an optical clock generating laser producing short pulses (~ 1 ps) should be used, so that the pulse compression, which uses highly non-linear dispersion-shifted fibres (HNL-DSF), can be avoided. In addition, a potential integration of loop components on a single chip would certainly reduce the loop length.

The model of optoelectronic phase-locked loop needs should be extended to take the effect of amplitude noise and colored noise sources into account. In particular, it would be of significant importance to include the flicker noise which has power spectral density inversely proportional to frequency. A large signal model of the optoelectronic phase-locked loop would as well be very useful such that large signal excitations can be simulated thereby taking system non-linearities and noise non-stationarity into account. In this way, we would be able to determine the amount of noise that the loop can tolerate in order to obtain successful locking and also determine the amount of noise that could potentially bring the loop out of lock. Developing a large signal model would require research into numerical schemes for time-efficient solution of stochastic differential equations. In addition, a rigorous connection between the BER for the system and the noise performance characterization of the clock recovery and regenerator needs to be established.

It would be very useful and very important to conduct a more detailed experimental verification of the derived theoretical model of the optoelec-

tronic phase-locked loop. We could thereby also determine the limitations of the model and form a connection between theory and experiments.

With regard to the modelling of coherent receiver with feedback and sampling, it would be desirable to include noise associated with loop components in the time domain model. Since the sampling loop is a hybrid between an analog and digital systems there are regions where the sampling loop exhibits unstable operation even though the baseband loop operation in that particular range is stable. In order to accurately quantify the stability regions of the sampling loop, techniques borrowed from sampled-data systems must be explored.

Bibliography

- [1] E. B. Desurvire, "Capacity demand for technology challenges for light-wave systems in the next two decades," *Journal of Lightwave Technology*, vol. 24, no. 12, december 2006, pp. 4697–4710
- [2] Y. Kim, B. J. Jeong, J. Chung, C. S. Hwang, J. S. Ryu, K-H. Kim and Y. K. Kim, "Beyond 3G: Vision, requirements and enabling technologies," *Communication Magazine*, pp. 120–124, March 2003
- [3] K. Takenouchi, "A 40 Gbit/s 16-bit photonic parallel-to-serial converter," in *in Proceedings of IEEE Laser Electro Optics Society (LEOS) 2002 Annual meeting, Glasgow, Scotland, UK*, paper ME4, 2002
- [4] J.K. Lucek, "100 Gbit/s parallel-to-serial and serial-to-parallel conversion using electroabsorption modulators ," in *in Proceedings of IEEE Laser Electro Optics Society (LEOS) 1998, Annual meeting, Orlando Florida*, paper TuB3, 1998
- [5] <http://www.mintera.com/NEWSARTICLES/04-05-07.htm>
- [6] A. H. Gnauck, G. Charlet, P. Tran, P. Winzer, C. Doerr, J. Centanni, E. Burrows, T. Kawanishi, T. Sakamoto, K. Higuma PDP19, "25.6-Tb/s C+L-Band Transmission of Polarization-Multiplexed RZ-DQPSK Signals," in *Proceedings of Optical Fiber Communication Conference (OFC) 2007, Anaheim, California, USA,*, PDP19, 2007
- [7] H. Masuda, A. Sano, T. Kobayashi, E. Yoshida, Y. Miyamoto, Y. Hibino, K. Hagimoto, T. Yamada, T. Furuta and H. Fukuyama, "20.4 Tb/s (204×111 Gb/s) transmission over 240 km using bandwidth-maximized hybrid Raman/EDFAs" in *Proceedings of Optical Fiber Communication Conference (OFC) 2007, Anaheim, California, USA*, PDP20, 2007

- [8] P. J. Winzer, G. Raybon, M. Duelk, "107-Gb/s optical ETDM transmitter for 100G Ethernet transport", in *Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland, UK*, postdeadline paper P1, 2005
- [9] K. Murata, K. Sano, H. Kitabayashi, S. Sugitani, H. Sugahara and T. Enoki, "100 Gb/s logic using IC using 100 nm gate length In-AlAs/InGaAs/InP, HEMTS" in *Proceedings of International Electron Devices Meeting*, 2002
- [10] K. Schuh, B. Junginger, E. Lach and G. Veith, "1 Tb/s (10×107 Gb/s ETDM) serial NRZ transmission over 480 km SSMF," in *Proceedings of Optical Fiber Communication Conference (OFC) 2007, Anaheim, California, USA*, PDP23, 2007
- [11] J. H. Sinsky, A. Adamiecki, G. Raybon, P. Winzer, O. Wohlgenuth, M. Duelk, C. R. Doerr, A. Umbach, H. G. Bach and D. Schmidt, "107 Gb/s optoelectronic receiver with hybrid integrated photodetector and demultiplexer," in *Proceedings of Optical Fiber Communication Conference (OFC) 2007, Anaheim, California, USA*, PDP30, 2007
- [12] K. Murata, K. Sano, H. Furukama, T. Kosugi, M. Nakamura, H. Sugahara, M. Tkumitsu, T. Enoki, "InP based IC technologies," in *Thin Solid Films*, 515, pp. 4313–4320, 2007
- [13] J. Hallin, T. Kjellberg and T. Swalin, "A 165-Gb/s 4:1 multiplexer in InPDHBT technology," *Journal of Solid State Circuits*, vol. 41, no. 10, october 2006, pp. 2209–2214
- [14] R. E. Makon, R. Driad, K. Schneider, M. Ludwig, R. Aidam, R. Quay, M. Schlechtweg, G. Weimann, "InP DHBT-based monolithically integrated CDR/DEMUX IC operating at 80 Gb/s," *Journal of Solid-State Circuits*, vol. 41, no. 10, October 2006
- [15] I. D. Philips, A. D. Ellis, T. Widdowson, D. Nasset, A. E. Kelley and D. Trommer, "100 Gb/s optical clock recovery using electrical phase-locked loop consisting of commercially available components," *Electronic Letters*, vol. 36, no. 7, pp. 650–652, 2000
- [16] S. Watanabe, "Optical signal processing using nonlinear fibres," *Journal Optical Fiber Communication Rep.* 3, 1–24, 2005

- [17] S. Watanabe, R. Okabe, F. Futami, R. Hinberger, C. Schmidt-Langhorst, C. Schubert and H. G. Weber, "Novel fibre Kerr-switch with parametric gain: demonstration of optical demultiplexing and sampling up to 640 Gb/s," in *Proceedings of European Conference on Optical Communication (ECOC) 2004, Stockholm, Sweden*, postdeadline paper Th4.1.6, 2004
- [18] L. K. Oxenlowe, K. S. Berg, A. T. Clausen, J. Seoane, A. I. Siahlo, P. Jeppesen, M. Schmidt, M. Schilling, Q. Le, "Specialty fibers for 160, 320 and 640 Gb/s signal processing," in *Proceedings of Conference on Laser Electro Optics (CLEO) 2004, San Francisco, California, USA*, vol. 2, paper CThQ1, 2005
- [19] T. Yamamoto, E. Yoshida and M. Nakazawa, "Ultrafast nonlinear optical loop mirror for demultiplexing 640 Gb/s TDM signals," *Electronics Letters*, vol. 34, no. 10 pp. 1013–1014, 1998
- [20] H. G. Weber, S. Ferber, M. Kroh, C. S. Langhorst, R. Ludwig, V. Maremebert, C. Boerner, F. Futami, S. Watanabe and C. Schubert, "Single channel 1.28 Tb/s and 2.56 Tb/s DQPSK transmission" in *Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland, UK*, postdeadline paper Th 4.1.2, 2005
- [21] M. Nakazawa, T. Yamamoto and K. R. Tamura, "1.28 Tbit/s - 70 km OTDM transmission using third-and fourth-order simultaneous dispersion compensation with a phase modulator," in *Proceedings of European Conference on Optical Communication (ECOC) 2000, Munich, Germany*, postdeadline paper 2.6, 2000
- [22] M. Nakazawa, T. Yamamoto and K. R. Tamura, "1.28 Tbit/s - 70 km OTDM transmission using third-and fourth-order simultaneous dispersion compensation with a phase modulator," *Electronics Letters*, vol. 36, no. 24, 2027–2029, 2000
- [23] K. Osamu and K. Satoki, "Prescaled Timing Extraction from 400 Gb/s Optical Signal Using a Phase Lock Loop Based on Four-Wave-Mixing in a Laser Diode Amplifier," *Journal of Lightwave Technology*, vol 8, no. 8, pp. 1094-1096, August 1996.
- [24] D. Zibar, L. K. Oxenløwe, J. Mørk, A. T. Clausen and P. Jeppesen, "Analysis of the effects of pulse shape and width on the retiming properties of a 3R regenerator," in *Proceedings of European Conference on*

Laser and Electro-Optics (CLEO) 2007, Munich, Germany, paper CI2, 2007

- [25] T. Kurniawan, A. Nirmalathas, C. Lim, D. Novak and R. Waterhouse, "Performance analysis of optimized millimeter-wave fiber radio links", *Transaction on Microwave Theory and Techniques*, vol. 54, no. 2, pp. 921–928, February 2006
- [26] A. Wiberg, P. P. Millan, M. V. Andres, P. A. Andrekson and P. O. Hedekvist, "Fiber-optic 40 GHz mm-wave link with 2.5 Gb/s data transmission," *Photonics Technology Letters* vol. 17, no. 9, pp. 1938–1940, 2005
- [27] A. Seeds and K. J. Williams, "Microwave photonics," *Journal of Lightwave Technology*, vol. 24, no. 12, pp. 4628–4641, December 2006
- [28] C. H. Cox, E. I. Ackerman, G. E. Bets and J. L. Prince, "Limits on performance of RF-over-fibre links and their impact on device design," *Transaction on Microwave Theory and Techniques*, vol. 54, no. 2, 906–920, February 2006
- [29] H. F. Chou, A. Ramaswamy, D. Zibar, L.A. Johansson, L. Coldren and J. Bowers, "SFDR Improvement of a Coherent Receiver Using Feedback," in *Proceedings of Conference on Coherent Optical Technologies and Applications (COTA) 2006, Whistler, Canada*, paper CFA3, (2006)
- [30] H. F. Chou, A. Ramaswamy, D. Zibar, L.A. Johansson, J. E. Bowers, M. Rodwell and L. Coldren, "Highly-linear coherent receiver with feedback," *accepted for publication in Photonics Technology Letters*, 2007
- [31] H. F. Chou, L.A. Johansson, , Darko Zibar, A. Ramaswamy, M. Rodwell and J.E. Bowers , "All-Optical Coherent Receiver with Feedback and Sampling," in *Proceedings of International Topical Meeting on Microwave Photonics (MWP) 2006, Grenoble France*, paper W3.2, 2006
- [32] D. Zibar, L. A. Johansson, H. F. Chou, A. Ramaswamy, M. Rodwell and J. E. Bowers, "Novel optical phase demodulator based on a sampling phase-locked loop," *Photonics Technology Letters*, vol. 19, no. 9, pp. 686–688, May 2007

- [33] Darko Zibar, L.A. Johansson, H. F Chou, A. Ramaswamy, M. Rodwell and J.E. Bowers, "Dynamic Range Enhancement of a Novel Phase-Locked Coherent Optical Phase Demodulator", *Optics Express*, 15(1) pp. 33-44, January 2007
- [34] D. Zibar, L. A. Johansson, H. F. Chou, A. Ramaswamy and J. E. Bowers, "Time Domain Analysis of a Novel Phase-Locked Coherent Optical Demodulator," in *Proceedings of Conference on Coherent Optical Technologies and Applications (COTA) 2006*, Whistler, Canada, paper JWB11, 2006
- [35] Y. Li, D. Yoo, P. Hercfeld, A. Rosen, A. Madjar and S. Golwasser , "Receiver for a coherent fiber-optic link with high dynamic range and low noise figure," in *Proceedings of International Topical Meeting on Microwave Photonics (MWP) 2005*, Seoul, Korea, paper Tp-39, 2005
- [36] T. R. Clark and M. L. Dennis, "Experimental demonstration of coherent optical I/Q demodulation of analog RF signals," in *Proceedings of Conference on Coherent Optical Technologies and Applications (COTA) 2006*, Whistler, Canada, paper CFA5, 2006
- [37] J. Basak and B. Jalali, "Adaptive electronic linearization of a coherent heterodyne optical receiver," in *Proceedings of IEEE Microwave Symposium, San Francisco, USA*, pp. 18–21, 2006
- [38] F. Bucholt, V. J. Urick, M. S. Rogge and K. J. Williams, "Performance of analog photonic links employing phase modulation," in *Proceedings of Conference on Coherent Optical Technologies and Applications (COTA) 2006*, Whistler, Canada, paper CFA6, (2006)
- [39] M. Lee, H. E. Katz, C. Erben, D. M. Gill, P. Gopalan, J. D. Heber, D. J. McGee, "Broadband modulation of light by using an electro-optic polymer," *Science*, vol. 298 no. 5597, pp. 1401–1403, 2002
- [40] A. Ramaswamy, L. A. Johansson, J. Klamkin, C. Sheldon, H. F. Chou, M. J. Rodwell, L. A. Coldren, J. E. Bowers, "Coherent Receiver Based on a Broadband Optical Phase-Lock Loop," in *Proceedings of Optical Fiber Communication Conference (OFC) 2007*, Anaheim, California, USA, 2007, PDP3
- [41] M. Kroh, C. M. Weinert, C. S. Langhorst, C. Schubert, J. Berger, C. Borner, R. Ludwig and H. G. Weber, "Semiconductor mode-locked

- laser pulse source for 320 and 640 Gb/s RZ data transmitter," in *Proceedings of European Conference on Optical Communication (ECOC) 2004, Stockholm, Sweden*, paper We3.5.7, 2004
- [42] M. Nakazawa, E. Yoshida, E. Yamamoto, E. Yamada and A. Sahara, "640 Gb/s transmission experiment over 60 km using 400 fs pulse train and walk-off free, dispersion flattened nonlinear optical loop mirror," *Electronics Letters*, vol. 34, no. 9, 1998
- [43] J. Seone, A. I. Siahlo, A. T. Clausen, L. K. Oxenloewe, P. Jeppesen, M. Schmidt, M. Schilling, E. Lach, "Ultra high-speed demultiplexing using a NOLM based on a commercially available highly nonlinear fibre," in *Proceedings of European Conference on Optical Communication (ECOC) 2004, Stockholm, Sweden*, paper We1.5.4, 2004
- [44] E. Lach, M. Schmidt, M. Witte, K. Schuh, H. Buelow, E. Buchali, A. T. Clausen, L. K. Oxenloewe, B. Sartorius, C. Kazmierksi, S. Vorbeck, R. Leppla, E. Le-Rouzic, B. Cuenot, A. Richter, F. Ramos, R. Lorent, G. Charlet, P. Pecci and D. Rouvillain, "DWDM transmission at ultra-high channel bit-rates: European TOPRATE project view," in *Proceedings of European Conference on Optical Communication (ECOC) 2004, Stockholm, Sweden*, paper Tu1.1.2, 2004
- [45] A. T. Clausen, H. N. Poulsen, L. K. Oxenløwe, A. Siahlo, J. Seone and P. Jeppesen, "Pulse Source Requirements for OTDM Systems," in *Proceedings of IEEE Laser Electro Optics Society (LEOS) 2003 Annual meeting, Tucson, Arizona, USA*, paper TyY2, 2003
- [46] M. Jinno, "Effects of crosstalk and timing jitter on all-optical time division multiplexing using a nonlinear fiber Sagnac interferometer switch," *Journal of Quantum Electronics* vol. 30, no. 12, pp. 2842–2853, 1994
- [47] K. E. Stubkjaer, "Semiconductor optical amplifier based all-optical gates for high-speed optical processing," *Journal of Selected Topics in Quantum Electronics*, vol. 6, pp. 1428–1435
- [48] D. Wolfson, A. Kloch, T. Fjelde, C. Janz, B. Dagens and M. Renaud, "40 Gb/s all-optical wavelength conversion, regeneration and demultiplexing in a SOA based all-active Mach-Zender interferometer," *IEEE Photonic Technology Letters*, vol. 12, 332–334, 2000

- [49] Y. Ueno, S. Nakamura and K. Tajima, "Penalty free error-free all-optical data pulse regeneration at 84 Gb/s with symmetric Mach-Zender type regenerator," in *Proceedings of Optical Fiber Communication Conference (OFC) 2001, Anaheim, CA, USA*, paper MG5-1, 2001
- [50] T. Otani, T. Miyazaki and S. Yamamoto, "Optical 3R regenerator using wavelength converters based on electroabsorption modulator for all-optical network applications," *Photonics Technology Letters*, vol. 12, 431–433 (2000)
- [51] M. N. Sysak, L. A. Johannson, J. W. Raring, H. N. Poulsen, D. J. Blumenthal, L. A. Coldren, "Broadband Return to Zero (RZ) Signal Regeneration and Wavelength Conversion Using a Monolithically Integrated, Photocurrent Driven Wavelength Converter," *Electronics Letters*, Vol. 42, no. 25, December 2006
- [52] M. N. Sysak, J. W. Raring, J. S. Barton, H. N. Poulsen, D. J. Blumenthal, L. A. Coldren, "Extinction ratio regeneration, signal reamplification (2R), and broadband wavelength switching using a monolithically integrated photocurrent driven wavelength converter," *Optics Express*, vol. 14, no. 23, pp. 11348–11353, November 2006
- [53] S. Watanabe and F. Futami, "All-optical signal processing using highly-nonlinear optical fibres," *IEICE Transactions*, E84-B, 1179–1189, 2001
- [54] G. Raybon, Y. Su, J. Leuthold, R. J. Essiambre, T. Her, C. Joergensen, P. Steinvurzel, K. Dreyer and K. Feder, "40 Gb/s pseudo linear transmission over one million kilometers," in *Proceedings of Optical Fibre Communication Conference (OFC) 2002, USA*, paper FD10, 2002
- [55] S. Watanabe, F. Futami, R. Okabe, Y. Takita, S. Ferber, R. Ludwig, C. Schubert, C. Schmidt and H. G. Weber, "160 Gb/s optical 3R regenerator in a fiber based transmission experiment," in *Proceedings of Optical Fibre Communication Conference (OFC) 2003, USA*, paper PD16, 2003
- [56] E. Tangdiongga, Y. Liu, H. de Waardt, G. D. Khoe and H. J. S. Doreen, "320-to-40-gb/s demultiplexing using a single SOA assisted by an optical filter," *Photonics Technology Letters*, vol. 18, no. 8, 908–910, 2006

- [57] T. Ohara, H. Takara, I. Shake, T. Yamada, M. Ishii, I. Ogawa, M. Okamoto, S. Kawanoshi, "Highly stable 160 Gb/s OTDM technologies based on integrated MUX/DEMUX and drift-free PLL-type clock recovery," *Journal of Selected Topics in Quantum Electronics*, vol. 13, no. 1, pp. 40–48, 2007
- [58] H. Murai, M. Kagawa, H. Tsuji and K. Fujii, "EA-modulator based optical time division multiplexing/demultiplexing techniques for 160 Gb/s optical signal transmission," *Journal of Selected Topics in Quantum Electronics*, vol. 13, no. 1, pp. 70–78, 2007
- [59] D. Zibar, J. Mørk, L. K. Oxenløwe and A. T. Clausen, "Phase noise analysis of clock recovery based on an optoelectronic phase-locked loop," *Journal of Lightwave Technology*, vol. 25, no. 3, pages 901–914, March 2007
- [60] C. Kouloumentas, A. Tzanakaki and I. Tomokos, "All-optical clock recovery at 160 Gb/s and beyond, based on a Fabry-Perot filter and self-phase modulation effects," in *Proceedings of International Conference on Transparent Optical Networks (ICTON), 2006, Nottingham, United Kingdom*, paper Tu.B1.8, 2006
- [61] M. Funabashi, Z. Pan, Z. Zhu and S. J. B. Yoo, "Nanosecond guard time packet-by-packet burst-mode optical 3R regenerator in an optical-label switching router," *Photonics Technology Letters*, vol. 18, no. 8, May 2006
- [62] J. Renaudier, B. Lavigne, P. Gallion, G-H. D, "Study of phase-noise properties and timing jitter of 40-GHz all optical clock recovery using self-pulsating semiconductor lasers," *Journal of Lightwave Technology*, vol. 24, no. 10, October 2006
- [63] C. Bornholdt, B. Satorius, S. Schelhase, M. Mohrle, and S. Bauer, "Self-pulsating DFB laser for all-optical clock recovery at 40 Gb/s," *Electron Letters*, vol. 40, no. 4, pp. 265–267, February 2000
- [64] T. Ohno, K. Sato, R. Iga, Y. Kondo, T. Ito, T. Furuta, K. Yoshino and H. Ito, "Recovery of 160 GHz optical clock from 160 Gb/s data stream using modelocked laser diode," *Electronics Letters*, vol. 40, no. 4, pp. 265–267, February 2004

- [65] S. Arahira, S. Sasaki, K. Tachibana and Y. Ogawa, "All-optical 160 Gb/s clock extraction with a mode-locked laser diode module," *Photonics Technology Letters*, vol. 16, no. 6, pp. 1558–1560, 2004
- [66] J. Lasri, P. Devgan, R. Tong and P. Kumar, "Ultralow timing jitter 40 Gb/s clock recovery using self-starting optoelectronic oscillator," in *Photonics Technology Letters*, vol. 16, no. 1, 263–265, January 2004
- [67] H. F. Chou and J. Bowers, "High-speed OTDM and WDM networks using travelling-wave electroabsorption modulators," *Journal of Selected Topics in Quantum Electronics*, vol. 13, no. 1, January 2007 (invited paper)
- [68] Leif K. Oxenløwe, Darko Zibar, Michael Galili, Anders T. Clausen, Lotte J. Christiansen, Palle Jeppesen, "Filtering-assisted cross-phase modulation in a semiconductor optical amplifier enabling 320 Gb/s clock recovery," in *Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland*, pp. 113–114, 2005
- [69] J.P. Turkiewicz, E. Tangdiongga, G. D. Khoe and H. de Waardt, "Clock recovery and demultiplexing performance of 160-Gb/s OTDM field experiments," *Photonics Technology Letters*, vol. 16, no. 6, pp. 1555–1557, June 2004
- [70] T. Ohara, H. Takara, I. Shake, T. Yamada, M. Ihii, I. Ogawa, M. Okamoto and S. Kawanishi, "Highly stable 160-Gb/s OTDM technologies based on integrated MUX/DEMUX and drift-free PLL-type clock recovery," *Journal of Selected Topics in Quantum Electronics*, vol. 13, no. 1, January 2007
- [71] K. Osamu and K. Satoki, "Ultrahigh-Speed Clock Recovery Circuit Based on Four Wave Mixing in a Traveling-Wave Diode Amplifier," *Journal of Lightwave Technology*, vol 11, no. 12, pp. 1757–1767, August 1996.
- [72] D. Tong, B. Mikkelsen, G. Raybon, T. N. Nielsen, F. K. Dreyer, and J. E. Johnson, "Optoelectronic Phase-Locked Loop with Balanced Photodetection for Clock Recovery in High-Speed Optical Time-Division-Multiplexing Systems," *Photonics Technology Letters*, vol. 12, no. 8, pp. 1064–1066, August 2000
- [73] L. Oxenløwe, C. Schubert, C. Schmidt, E. Hilliger, J. Berger, U. Feiste, R. Ludwig and H. G. Weber, "Optical clock recovery employing an

- optical PLL using cross-phase modulation in a Sagnac-interferometer," *in Proceedings of Conference on Laser Electro Optics (CLEO) 2001, Baltimore, Maryland, USA*, paper CThU2, 2001
- [74] S. Kawanishi, H. Takara, K. Uchiyama, I. Shake, K. Mori, "3 Tbit/s (160 Gbit/s/spl times/19 ch) OTDM/WDM transmission experiment," *in Proceedings of European Conference on Optical Communication (ECOC) 1999, Nice, France*, paper ThA3, 1999
- [75] S. Kawanishi, H. Takara, M. Saruwatari and N. Kioth, "Ultra-high speed phasedlocked-loop-type clock recovery circuit using a traveling-wave laser diode amplifier as a 50 GHz phase detector," *Electronics Letters*, vol. 29, no. 19, pp. 769–771, 1995.
- [76] V. Marembert, C. Schubert, S. Ferber, K. Schulze, C. S. Langhorst, C. Boerner, M. Kroh, R. Ludwig, S. Watanabe, F. Futami, R. Okabe and H. G. Weber, "Single-channel 640 Gb/s DPSK transmission over a 160 km fibre link," *in Proceedings of European Conference on Optical Communication (ECOC) 2004, Stockholm, Sweden*, postdeadline paper, Th4.4.2, 2004
- [77] T. Yamamoto, U. Feiste, J. Berger, C. Schubert, C. Schmidt, R. Ludwig, H. G. Weber, "160 Gbit/s demultiplexer with clock recovery using SOA-based interferometric switches and its application to 120 km fiber transmission," *in Proceedings of European Conference on Optical Communication (ECOC) 2001, Amsterdam, The Netherlands*, paper Tu.L.2.6, 2001
- [78] L. K. Oxenløwe, A. I. Siahlo, K. S. Berg, A. T. Clausen, B. M. Sørensen, K. Yvind. and P. Jeppesen, "A novel 160 Gbit/s receiver configuration including a glass crystal pulsed laser, photonoc crystal fiber and a simple clock recovery scheme," *in Proceedings of European Conference on Optical Communication (ECOC) 2003, Rimini Italy*, paper Th2.5.3
- [79] B. Razavi, "Design of integrated circuits for optical communication," the McGraw-Hill Companies, 2003
- [80] F. M. Gardner, "Phaselock Teschnique," New York: Wiley, 2005
- [81] A. Hajimiri and T. H. Lee, "A general theory of phase noise in electrical oscillators," *Journal of Solid-State Circuits*, vol. 33, pp. 179–194, June 1998

- [82] C. Samori, A. L. Lacaita, F. Villa and F. Zappa, "Spectrum folding and phase noise in LC tuned oscillators," *Transactions on Circuits and Systems. II*, vol. 45, pp. 781–790, July 1998
- [83] A. L. Lacaita and C. Samori, "Phase noise performance of crystal-like LC tanks," *Transactions on Circuits and Systems II*, vol. 45, pp. 898–900, July 1998
- [84] A. Hajimiri and T. H. Lee, "Design issues in CMOS differential LC oscillators," *Journal of Solid-State Circuits*, vol. 34, pp. 790–804, May 1999
- [85] A. Hajimiri, S. Limotyrakis, and T. H. Lee, "Jitter and phase noise in ring oscillators," *Journal of Solid-State Circuits*, vol. 34, pp. 790–804, June 1999
- [86] A. Hajimiri and T. H. Lee, "Oscillator phase noise: A tutorial," *Journal of Solid-State Circuits*, vol. 35, pp. 326–336, March 2000
- [87] Q. Huang, "Phase noise to carrier ratio in LC oscillators," *Transactions on Circuits and Systems I*, vol. 47, pp. 655–674, May 2000
- [88] G. J. Gornall, "A simple 2-D oscillator to determine the correct decomposition of perturbations into amplitude and phase noise," *Transactions on Circuits and Systems I*, vol. 48, pp. 896–898, July 2001
- [89] A. Demir, A. Mehrotra and Jaijeet Roychowdhury, "Phase noise in oscillators: A unifying theory and numerical methods for characterization," *Transactions on Circuits and Systems I*, vol. 47, no. 5, May 2000
- [90] A. Demir, "Analysis and simulations of noise in nonlinear electronic circuits and systems," Ph.D. dissertation, Univ. California, Berkeley, 1997
- [91] A. Demir, "Phase noise and timing jitter in oscillators with colored noise sources," *Transaction on Circuits and Systems I*, vol. 49, pp. 1782–1791, December 2002
- [92] D. Ham and A. Hajimiri, "Virtual damping and Einstein relation in oscillators," *Journal of Solid-State Circuits*, vol. 38, pp. 407–418, 2003
- [93] Athanasios Papoulis, "Probability, Random Variables and Stochastic Processes," McGraw-Hill, Inc., Third edition, 1991

- [94] A. Mehrotra, "Noise analysis of phase-locked loops," *Transactions on Circuits and Systems I*, vol. 49, no. 9, September 2002
- [95] H. Risken, "The Fokker-Planck Equation," Springer-Verlag, 2nd edition, 1989
- [96] D. von der Linde, "Characterization of the Noise in Continuously Operating Mode-Locked Laser," *Applied Physics B*, B(39), vol. 4, pp. 201–217
- [97] R.F. Kalman, J.C. Fan and L.G. Kazovsky, "Dynamic range of coherent analog fiber-optic links," *Journal of Lightwave Technology*, vol. 12, pp. 1263–1277, 1994
- [98] J. Ole and P. H. Sørensen, "Regulerings teknik," first edition, Printed in Denmark, 1995
- [99] J. Mørk, "Nonlinear dynamics and stochastic behaviour of semiconductor lasers with optical feedback," Ph.D. dissertation, the Danish Center for Applied Mathematics and Mechanics, Technical University of Denmark, Kgs. Lyngby, 1989
- [100] C. W. Gardiner, "Handbook of stochastic methods," Springer-Verlag, Berlin, 1983
- [101] J. Eising, "Linear algebra," DTU 1997
- [102] B. Sartorius, C. Bornholdt, O. Brox, D. Hoffman, M. Mohrle, G. Sahin, B. Sartorius and S. Schelhase, "Ultrafast locking optical clock for IP packet switching applications," in *Optical Fiber Communication Conference (OFC) 2000 USA*, vol.1 pp. 78–80, 2000
- [103] Agrawal P. Govind, "Fiber-Optic Communication System," second edition, John Wiley and Sons, USA, 1997
- [104] P. R. Trischitta and E. L. Varma, "Jitter in digital transmission systems," Artech House, Inc, Norwood, MA 1989
- [105] Hsu-Feng Chou, "Optical signal processing using traveling-wave electroabsorption modulators," Ph.D. dissertation, Department of Electrical and Computer Engineering, Santa Barbara, December 2005
- [106] B. Razavi, "Monolithic phase-locked loops and clock recovery circuits – theory and design," IEEE Press Marketing, USA, 1996

- [107] D. Zibar, L. K. Oxenløwe, A. T. Clausen, J. Mørk and P. Jeppesen, "Theoretical and experimental investigation of a balanced phase-locked loop based clock recovery at 160 Gb/s," in *Proceedings of IEEE Laser Electro Optics Society (LEOS) 2003, Annual meeting, Tucson, Arizona, USA*, paper TuY5, 2003
- [108] K. Kurokawa, "Injection locking of microwave solid-state oscillators," in *Proceedings of the IEEE*, vol. 61, no. 10, pp. 1386-1410, 1973
- [109] X. S. Yao, L. Maleki, Y. Ji, G. Lutes and M. Tu, "Dual loop optoelectronic oscillator," in *Proceedings of International Frequency Control Symposium*, pp. 545-549, 1998
- [110] S. Watanabe, R. Okabe, F. Futami, R. Hinberger, C. Schmidt-Langhorst, C. Schubert and H. G. Weber, "Novel fiber Kerr-switch with parametric gain: demonstration of optical demultiplexing and sampling up to 640 Gb/s," in *Proceedings of European Conference on Optical Communication (ECOC) 2004, Stockholm, Sweden*, postdeadline paper Th4.1.6, 2004
- [111] S. Vehovc, M. Vidmar, and A. Paoletti, "80 Gb/s optical clock recovery with automatic lock acquisition using electrical phase-locked loop," *Electronics Letters*, vol. 39, no. 8, pp. 673-674, 2003
- [112] H. Yokoyama, Y. Hashimoto, H. Kurita and I. Ogura, "All-optical subharmonic clock recovery and demultiplexing," in *Proceedings of Optical Fibre Communication Conference (OFC) 2000, Baltimore, Maryland, USA*, paper ThP5, 2000
- [113] M. A. Grant, W. C. Michie and M. J. Fletcher, "The Performance of Optical Phase-Locked Loops in the Presence of Nonnegligible Loop Propagation Delay," *Journal of Lightwave Technology*, vol. 5, no. 4, April 1987
- [114] D. Zibar, L. K. Oxenløwe, A. T. Clausen, J. Mørk and P. Jeppesen, "Analysis of the Effects of Time Delay in Clock Recovery Circuits Based on Phase-Locked Loops," in *Proceedings of IEEE Laser Electro Optics Society (LEOS) 2004, Annual meeting, Puerto Rico, USA*, paper TuR4, 2004
- [115] M. Nakazawa, T. Yamamoto and K. R. Tamura, "1.28 Tbit/s – 70 km OTDM transmission using third and fourth order simultaneous

- dispersion compensation with a phase modulator," *Electronics Letters*, vol. 36, no. 24, pp. 2027-2029, 2000
- [116] R. E. Best, "Phase-Locked Loops", New York: McGraw-Hill, 1984
 - [117] K. Yvind, Semiconductor Mode-Locked Lasers for Optical Communication Systems," Ph.D. dissertation, Research Centre COM, Technical University of Denmark, Kgs. Lyngby, 2003
 - [118] D. Larsson, K. Yvind, J. M. Hvam, "Wide-band residual phase-noise measurements on 40-GHz monolithic mode-locked lasers," *Photonics Technology Letters*, vol. 17, no. 11, pp. 2388-2390, November 2005
 - [119] A. M. Braun, V. B. Khalfin, M. H. Kwakernaak, W. F. Reichert, L. A. DiMarco, Z. A. Schellenberger, C. M. DePriest, T. Yilmaz, P. J. Delfyett, and J. H. Abeles, "Universality of Model-Locked Jitter Performance," *Photonics Technology Letters*, vol. 14, no. 8, 1058-1060, August 2002
 - [120] J. Buckwalter and Y. A. Robert," Time delay considerations in high-frequency phase-locked loops," in *Proceedings of Radio Frequency Integrated Circuits Symposium*, paper MO3D-5, 2002
 - [121] D. Zibar, "Theoretical and numerical investigation of optical clock recovery using a phase locked loop," M.Sc. thesis, COM•DTU, Department of Communications, Optics and Materials, Technical University of Denmark, December, 5th 2003
 - [122] L. K. Oxenløwe, "Optical Signal Processing with Semiconductor Components," Ph.D. thesis, COM•DTU, Department of Communications, Optics and Materials, Technical University of Denmark, 2002
 - [123] D. Zibar, L. K. Oxenløwe, J. Mørk, M. P. Sørensen, M. Galili, A. T. Clausen, B. Sørensen and P. Jeppesen, "Detailed Modelling and Experimental Characterisation of an Ultra-Fast Optoelectronic Clock Recovery Circuit," in *in Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland, UK*, paper We4.P.111, 2005
 - [124] L. K. Oxenløwe, A. I. Siahlo, K. S. Berg, A. T. Clausen, B. M. Sørensen, K. Yvind. and P. Jeppesen, "Pre-scaled clock recovery with compact semiconductor devices for ultra high-speed OTDM systems,"

- in Proceedings of European Conference on Optical Communication (ECOC) 2004, Stockholm, Sweden, paper We3.5.2, 2004*
- [125] L. K. Oxenløwe, L. J. Christiansen, D. Larsson, K. Yvind, A. T. Clausen, J. Seone, A. Siahlo, B. M. Sørensen and P. Jeppesen, "Low-jitter prescaled clock recovery with compact semiconductor components for ultra high-speed OTDM systems," *in Proceedings of Opto-Electronics and Communication Conference (OECC) 2004, Yokohama, Japan* pages: 888-889, 2004
 - [126] L. K. Oxenløwe, D. Zibar, M. Galili, A. T. Clausen, L. J. Christiansen and P. Jeppesen, "Filtering-assisted cross-phase modulation in a semiconductor optical amplifier enabling 320 Gb/s clock recovery," *in Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland*, pp. 113-114, 2005
 - [127] J. Savoj and B. Razavi, "A 10 Gb/s CMOS clock and data recovery circuit," *Symposium on VLSI Circuits Digest of tech. Papers*, pp. 136-139, June 2000
 - [128] M. Reinhold, "A fully integrate 40 Gb/s clock and data recovery IC with 1:4 DMUX in SiGe Technology," *Journal of Solid-State Circuits*, vol. 36, pp. 1937-1945, Dec. 2001
 - [129] F. Parmigiani, P. Ptroopoulos, M. Ibsen and D. J. Richardson, "All-optical pulse reshaping and retiming systems incorporating pulse shaping fiber bragg grating," *Journal of Lightwave Technology*, vol. 24, no. 1, January 2006
 - [130] R. Van Tuyl, D. Ingram, D. Humphreys and D. Taylor, "Optical retiming for jitter measurement calibration," *in Proceedings of European Conference on Optical Communication (ECOC) 2005, Glasgow, Scotland, UK*, paper Th 3.5.3, 2005
 - [131] J. Zhang, M. Yao, X. Chen, L. Xu, M. Chen, Y. Gao, "Bit error rate analysis for OTDM systems based on moment generating functions," *Journal of Lightwave Technology*, vol. 18, no. 11, pp. 1513-1518, 2000
 - [132] D. Novak, A. Nirmalathas, C. Lim, C. Marra, R. Waterhouse, "Fibre radio: challenges and possible solutions," *in Proceedings of International Topical Meeting on Microwave Photonics (MWP), Budapest, Hungary*, pp. 49-54, 2003

- [133] D. Cabric, M. S. W. Chen, D. A. Sobel, S. Wang, J. Yang and R. W. Brodersen, "Novel Radio Architectures for UWB, 60 GHz, and Cognitive Wireless Systems," *Journal on Wireless Communications and Networking*, vol. 2006, Article ID 17957, 2006
- [134] A. Matsuzawa, "Analog IC technologies for future wireless systems," in *Proceedings of IEICE Transactions on Electronics*, vol. E89, no. 4, (invited paper), April 2006
- [135] B. Zhao, "Analog/mixed-signal and RF integrated circuit technologies for wireless communications," in *Proceedings of International Conference on Solid-State and Integrated Circuits Technology*, vol. 2, pp. 1220–1225, 2004
- [136] T. Niiho, M. Nakaso, K. Masuda, H. Sasai, K. Utsumi and M. Fuse, "Transmission performance of mulichannel wireless LAN system based on Radio-over-Fibre techniques," *Transactions on Microwave Theory and Techniques*, vol. 54, no. 2, February 2006
- [137] C. Cox, "Analog optical links," Cambridge, U.K. Cambridge Univ. Press, 2004
- [138] D. M. Pozar, "Microwave engineering," 2nd edition, John Wiley and sons, USA, 1998
- [139] M. N. Sysak, L. A. Johannson, J. Klamkin, L. A. Coldren, J. E. Bowers, "Characterization of Distortion in InGaAsP Optical Phase Modulators Monolithically Integrated with Balanced UTC Photodetector", in *Proceedings of IEEE Lasers and Electro Optics Society (LEOS) 2006, annual meeting, Montreal, Canada*, paper TuU2, 2006